Typical Application Circuit

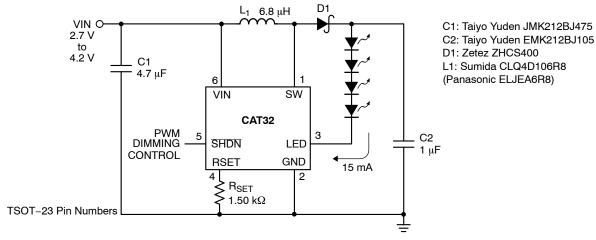




Table 1. PIN DESCRIPTION

Pin Number SOT23	Pin Number TDFN	Name	Function
1	8	SW	Switch pin. This is the drain of the internal power switch. For minimum EMI, min- imize the trace area connected to this pin.
2	5	GND	Ground pin. Connect pin 2 to ground.
3	6	LED	LED (cathode) connection pin.
4	4	RSET	RESET pin. A resistor connected from pin 4 to ground sets the LED current. This pin is also used to dim the LEDs.
5	3	SHDN	Shutdown pin.
6	2	VIN	Input supply pin. This pin should be bypassed with a capacitor to ground. A 4.7 μF capacitor mounted close to the pin is recommended.
-	1	Power Ground	Power Ground

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Unit
V _{IN} , LED, SHDN voltage	8	V
SW voltage	20	V
RSET voltage	1	V
Storage Temperature Range	-65 to +150	°C
Junction Temperature	125	°C
Lead Soldering Temperature (10 secs)	300	°C
ESD Rating – Human Body Model	2000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 3. RECOMMENDED OPERATING CONDITIONS

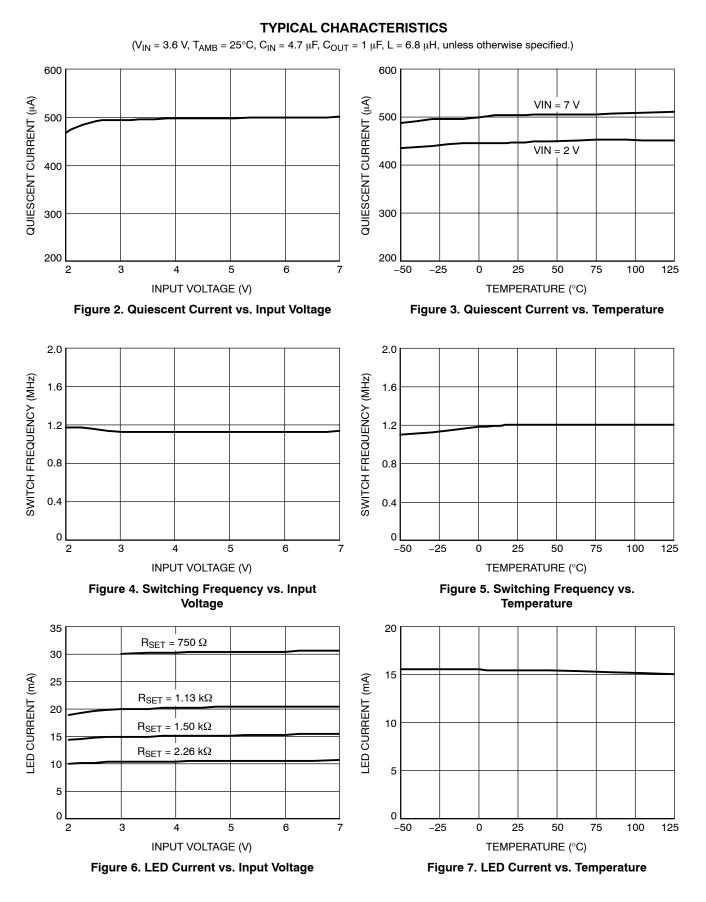
Parameter	Range	Unit
V _{IN}	2 to 7	V
Ambient Temperature Range	-40 to +85	°C
Inductor L1	6.8 ±20% typical	μΗ
Input Capacitor C1	4.7 ±20% typical	μF
Output Capacitor C2	1.0 ±20% typical	μF
I _{LED} with 1 to 4 LEDs in series	0 to 20	mA

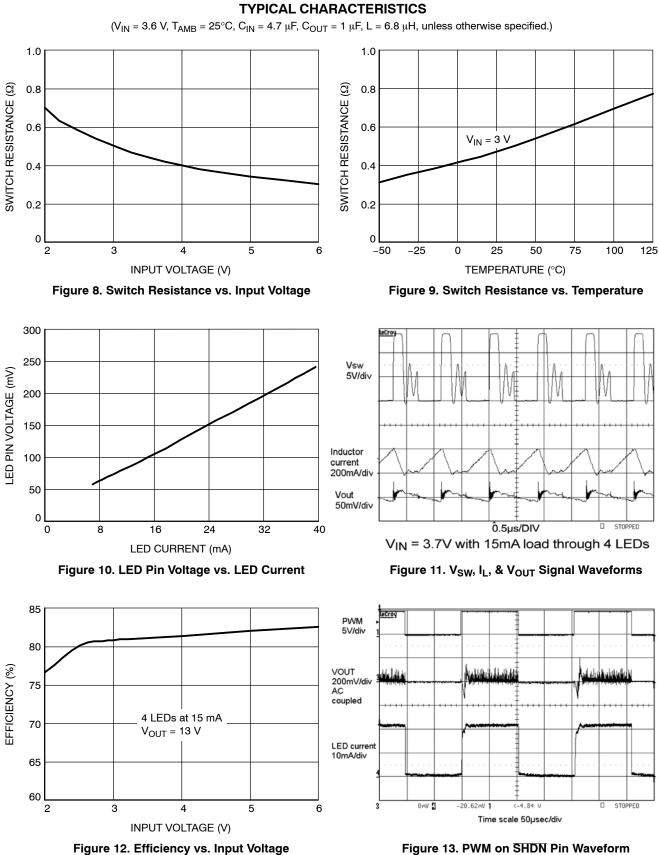
NOTE: Typical application circuit with external components is shown on page 2.

Table 4. ELECTRICAL OPERATING CHARACTERISTICS

(Over recommended operating conditions unless otherwise specified. $T_A = 25^{\circ}C$, $V_{IN} = 2 V$ and $V_{\overline{SHDN}} = 1.2 V$.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
ا _Q	Quiescent Current	V _{RSET} = 0.2 V		0.5	0.7	mA
I _{GND}	Ground Current in Shutdown	V _{SHDN} = 0 V		0.05	1	μΑ
V_{LED}	LED Pin Voltage	V _{IN} < V _{OUT} , I _{LED} = 15 mA		120	180	mV
I _{LED}	LED Current Adjust Range		5		40	mA
I _{LED}	Programmed LED Current	R _{SET} = 562 Ω	33	38	45	mA
		R _{SET} = 750 Ω	25	30	36	
		R _{SET} = 1.5 kΩ	12.5	15	17.5	
		R _{SET} = 4.53 kΩ		5		
I _{LED}	LED Pin Current Temperature Coefficient	l _{LED} = 15 mA		-0.01		mA/°C
V _{RSET}	RSET Pin Voltage	R _{SET} = 1.5 kΩ		100		mV
	Shutdown Pin Logic High Level		0.85			V
	Shutdown Pin Logic Low Level				0.25	V
f _{SW}	Boost Converter Frequency		0.8	1.2	1.6	MHz
I _{SWL}	Switch Current Limit		400	550	780	mA
R _{SW}	Switch Resistance	V _{IN} = 2 V, I _{SW} = 100 mA		0.7	1.2	Ω
		V _{IN} = 3 V, I _{SW} = 100 mA		0.5	0.9	1
	Switch Leakage Current	Switch Off, V _{SW} = 5 V		0.01	5	μΑ
	Efficiency	Components shown on Figure 1		83		%





Operation

The CAT32 device is a high efficiency, constant frequency, current regulating boost converter.

The device includes a switch and an internally compensated loop for the regulation of the LED current.

Operation can be best understood by examining the block diagram. The RSET pin is regulated at 100 mV and the current through the external resistor will set the regulated current in the LEDs (from 5 mA to 40 mA) with a multiplication factor of 225.

While maintaining LED current regulation, the CAT32 automatically adjusts the LED pin voltage to be as low as possible. A low LED pin voltage ensures high efficiency.

Current through the internal power switch is continuously monitored cycle–by–cycle. If the current limit is exceeded, the switch is immediately turned off, protecting the device, for the remainder of the cycle.

PWM dimming operation can be achieved by switching the $\overline{\text{SHDN}}$ pin or by pulling the RSET pin higher than 0.1 V.

D_1 L₁ VIN 1 C1 sw Over Voltage Protection 1.2 MHz (22 V) Oscillator LED **I**LED 3 PWM & Logic Current Sense Ş Amp 100 mV VIN 6 Current $I_{LED} = 225 \times I_S$ SHDN 5 Control 225x ls CAT32 GND RSET 2 R_{SET}

Figure 14. CAT32 Block Diagram

Block Diagram

Application Information

Inductor Selection and Efficiency

Inductor vendors are shown below. Contact the manufacturer for detailed technical data and new product information.

Inductor	L (μΗ)	Maximum DCR (mΩ)	Maximum Height (mm)	Vendor	Web	
ELJEA4R7	4.7	180	2.2	Panasonic		
ELJEA6R8	6.8	250	2.2	714.373.7334	www.panasonic.com	
LQH3C4R7M24, LQH32CN4R7M11	4.7	260	2.2	Murata		
LQH3C100K24, LQH32CN100K11	10	300	2.2	770.436.1300	www.murata.com	
LB2016B4R7	4.7	250	2.0	Taiyo Yuden	www.t-yuden.com	
LB2016B100	3.8	350	2.0	408.573.4150		
CMD4D06-4R7	4.7	216	0.8			
CMD4D06-6R8	6.8	296	0.8	Sumida		
CLQ4D10-4R7	4.7	162	1.2	847.956.0666	www.sumida.com	
CLQ4D10-6R8	6.8	195	1.2	1		

Table 5. INDUCTOR MANUFACTURERS

Capacitor Selection

Low ESR (equivalent series resistance) capacitors should be used at the output to minimize the output ripple voltage. The low ESR and small package options available with multilayer ceramic capacitors make them excellent choices. The X5R and X7R capacitor types are preferred because they retain their capacitance over wider voltage and temperature ranges than the Y5V or Z5U types. A 1.0 μ F or 2.2 μ F output capacitor is recommended for most applications.

The voltage rating of the output capacitor C2 depends on the number of LEDs driven in series. A 10 V ceramic capacitor is recommended when driving two LEDs. A 16 V ceramic capacitor is recommended when driving 3 or 4 LEDs.

Table 6. CERAMIC CAPACITOR MANUFACTURERS

Supplier	Phone	Web
Taiyo Yuden	408.573.4150	www.t-yuden.com
Murata	814.237.1431	www.murata.com
Kemet	408.986.0424	www.kemet.com

Low profile ceramic capacitors with a 1 mm maximum height/thickness are available for designs height requirements. Ceramic capacitors also make a good choice for the input capacitor, which should be mounted as close as possible to the CAT32. A 2.2 μ F or 4.7 μ F input capacitor is recommended. Table 6 shows a list of several ceramic capacitor manufacturers. Consult the manufacturers for detailed information as new products and package options are introduced regularly.

Diode Selection

Schottky diodes, with their low forward voltage drop and fast switching speed, are the ideal choice for high efficiency applications. Table 7 shows several different Schottky diodes that work well with the CAT32. Make sure that the diode has a voltage rating greater than the output voltage. The diode conducts current only when the power switch is turned off (typically less than one-third the time), so a 0.4 A or 0.5 A diode will be sufficient for most designs.

Table 7. SCHOTTKY DIODE SUPPLIERS

Part	Supplier	
MBR0520	ON Semiconductor	
MBR0530 s	www.onsemi.com	
MBR0540	800.282.9855	
ZHCS400	Zetex	

LED Current Programming

The LED current is programmed with a single resistor connected to the RSET pin. The RSET pin is internally regulated to 100 mV, which sets the current flowing out of this pin, ISET, equal to 100 mV/RSET. The CAT32 regulates the current into the LED pin, I_{LED} , to 225 times the value of I_{SET} . For the best accuracy, a 1% or better resistor is recommended. Table 8 shows several typical 1% R_{SET} values.

I _{LED} (mA)	R _{SET}		
40	562 Ω		
30	750 Ω		
25	909 Ω		
20	1.13 kΩ		
15	1.50 kΩ		
10	2.26 kΩ		
5	4.53 kΩ		

Table 8. R_{SET} RESISTOR VALUES

For other LED current values, use the following equation to choose R_{SET} .

$$R_{SET} = 255 imes rac{0.1 \text{ V}}{I_{LED}}$$

Most white LEDs are driven at maximum currents of 15 mA to 20 mA. Some higher power designs will use two parallel strings of LEDs for greater light output, resulting in 30 mA to 40 mA (two strings of 15 mA to 20 mA) flowing into the LED pin.

LED Dimming with PWM Signal

PWM brightness control provides the widest dimming range (greater than 20:1). By turning the LEDs ON and OFF using the control signal the LEDs operate at either zero or full current, but their average current changes with the PWM signal duty cycle. Typically, a 5 kHz to 40 kHz PWM signal is used. PWM dimming with the CAT32 can be accomplished two different ways.

The SHDN pin can be driven directly or a resistor can be added to drive the RSET pin. If the SHDN pin is used, increasing the duty cycle will increase the LED brightness. Using this method, the LEDs can be dimmed and turned off completely using the same control signal. A 0% duty cycle signal will turn off the CAT32, reducing the total quiescent current to near zero.

If the RSET pin is used, increasing the duty cycle will decrease the brightness. Using this method, the LEDs are dimmed using RSET and turned off completely using SHDN. If the RSET pin is used to provide PWM dimming, the approximate value of R_{PWM} should be calculated (where V_{MAX} is the "HIGH" value of the PWM signal):

$$R_{PWM} = R_{SET} \times \left(\frac{V_{MAX}}{0.15 \text{ V}} - 1\right)$$

In addition to providing the widest dimming range, PWM brightness control also ensures the "purest" white LED color over the entire dimming range. The true color of a white LED changes with operating current, and is the "purest" white at a specific forward current, usually 15 mA or 20 mA. If the LED current is less than or more than this value, the emitted light becomes more blue. Applications involving color LCDs can find the blue tint objectionable.

When a PWM control signal is used to drive the SHDN pin of the CAT32, the LEDs are turned off and on at the PWM frequency. The current through them alternates between full current and zero current, so the average current changes with duty cycle. This ensures that when the LEDs are on, they can be driven at the appropriate current to give the purest white light. LED brightness varies linearly with the PWM duty cycle.

LED Dimming with a Logic Signal

For applications that need to adjust the LED brightness in discrete steps, a logic signal can be used. RMIN sets the minimum LED current value (when the NMOS is OFF):

$$\mathsf{R}_{\mathsf{MIN}} = 255 \times \frac{0.1 \, \mathsf{V}}{\mathsf{I}_{\mathsf{LED}(\mathsf{MIN})}}$$

R_{INCR} determines how much LED current increases when the external NMOS switch is turned ON.

$$R_{INCR} = 255 \times \frac{0.1 \text{ V}}{I_{LED(Increase)}}$$

LED Dimming with a DC Voltage

$$R_{ADJ} = 225 \times \frac{V_{MAX} - 0.1 V}{I_{LED(MAX)} - I_{LEAD(MIN)}}$$

PCB Layout Guidelines

The CAT32 is a high-frequency switching regulator and therefore proper PCB board layout and component placement can minimize noise and radiation and increase efficiency. To maximize efficiency, the CAT32 design has fast switch rise and fall times. To prevent radiation and high frequency resonance problems minimize the length and area of all traces connected to the SW pin and use a ground plane under the switching regulator.

The switch, schottky output diode and output capacitor signal path should be kept as short as possible. The ground connection for the R_{SET} resistor should be tied directly to the GND pin and not be shared with other components.

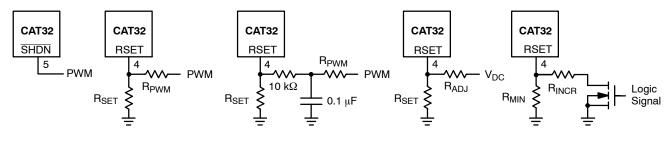
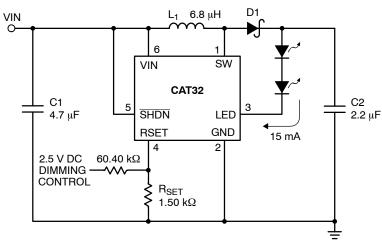


Figure 15. LED Dimming Circuits

TYPICAL APPLICATION CIRCUITS

(The application diagrams below are shown for the TSOT-23 packages.)





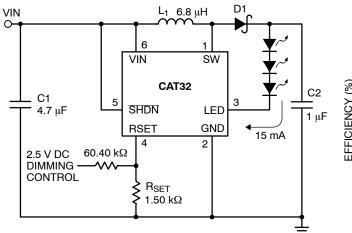


Figure 17. Three LEDs with DC Level Dimming Control

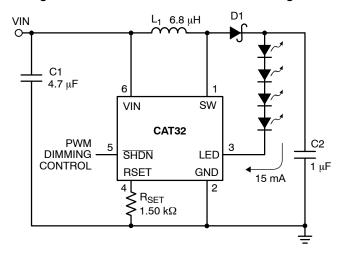
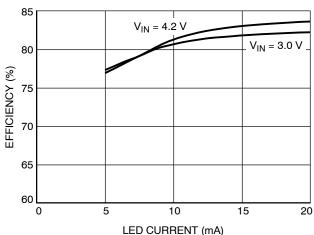
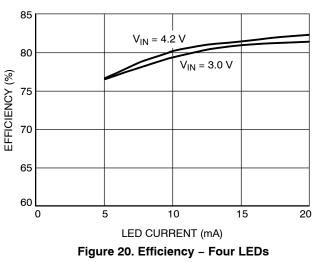


Figure 19. Four LEDs with PWM Dimming Control

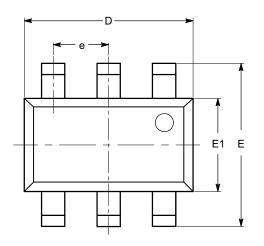






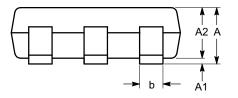
PACKAGE DIMENSIONS

TSOT-23, 6 LEAD CASE 419AF-01 ISSUE O



SYMBOL MAX MIN NOM 1.00 А A1 0.01 0.05 0.10 0.90 A2 0.80 0.87 b 0.30 0.45 с 0.12 0.15 0.20 D 2.90 BSC Е 2.80 BSC E1 1.60 BSC 0.95 TYP е L 0.30 0.40 0.50 0.60 REF L1 L2 0.25 BSC 0° 8° θ

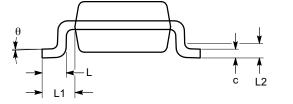
TOP VIEW





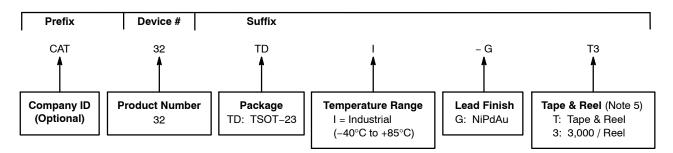
Notes:

- All dimensions are in millimeters. Angles in degrees.
 Complies with JEDEC MO-193.



END VIEW

Example of Ordering Information (Note 3)



1. All packages are RoHS-compliant (Lead-free, Halogen-free).

2. The standard lead finish is NiPdAu.

3. The device used in the above example is a CAT32TDI-GT3 (TSOT-23, Industrial Temperature Range, NiPdAu, Tape & Reel, 3,000 / Reel).

4. For additional package and temperature options, please contact your nearest ON Semiconductor Sales office.

5. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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