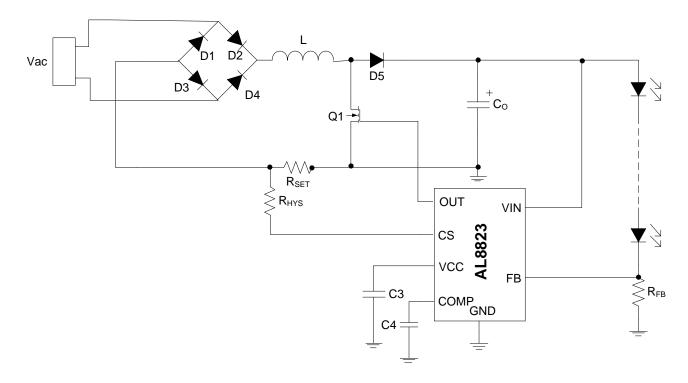


Typical Applications Circuit



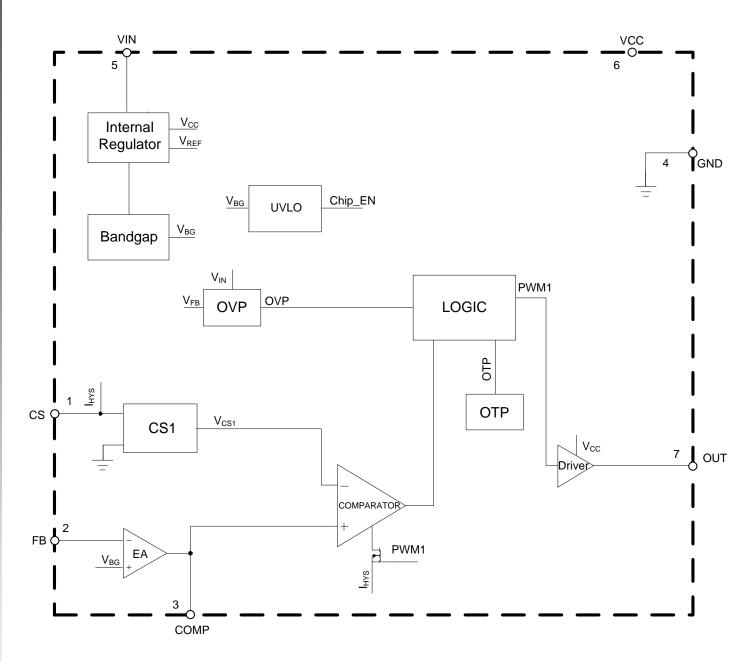
The AL8823 Boost Application Circuit

Pin Descriptions

Pin Number	Pin Name	Function	
1	CS	Input Current Sense Pin	
2	FB	LED Output Current Feedback Pin	
3	COMP	Control Loop Compensation Pin	
4	GND	Ground	
5	VIN	IC Input Voltage, Adding from Boost Output Voltage	
6	VCC	Supply Voltage For Internal Circuit	
7	OUT	Gate Driver Output	
8	NC	Not Connected	



Functional Block Diagram



The AL8823 Block Diagram



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified. Note 4)

Symbol	Parameter	Rating	Unit
V _{IN}	VIN Pin Voltage	-0.3 to 40	V
V _{OUT}	OUT Pin Voltage	-0.3 to 6	V
V _{COMP}	COMP Pin Voltage	-0.3 to 6	V
V _{CS}	CS Pin Voltage	-0.3 to 6	V
V _{FB}	FB Pin Voltage	-0.3 to 6	V
Vcc	VCC Pin Voltage	-0.3 to 6	V
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
θ_{JA}	Thermal Resistance (Junction to Ambient) (Note 5)	104	°C/W
θЈС	Thermal Resistance (Junction to Case) (Note 5)	6.6	°C/W
T _{LEAD}	Lead Temperature (Soldering, 10sec)	+300	°C
_	ESD (Machine Model)	200	V
_	ESD (Human Body Model)	2000	V

Notes:

- 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.
- 5. Device mounted on 1"x1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	VIN Pin Voltage	5	36	V
T _A	Ambient Temperature (Note 6)	-40	+105	°C

Note 6: The device may operate normally at +125°C ambient temperature under the condition not trigger temperature protection.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

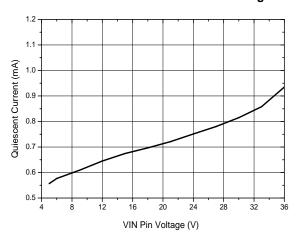
Symbol	Parameter	Condition	Min	Тур	Max	Unit
Input Supply						
V _{IN}	VIN Pin Voltage	_	5	-	36	V
IQ	Quiescent Current	No Switching	_	1	_	mA
V _{UVLO}	Under-Voltage Lockout Voltage	V _{IN} Rising	-	4.2	-	V
V _{HYS}	UVLO Hysteresis	-	_	500	-	mV
VCC Regulator						
V _{CC}	VCC Pin Voltage	-	4.5	5	5.5	V
-	Source Current Capability	V _{CC} = 5V	10	-	_	mA
-	Load Regulation	-	_	4	-	%
Gate Driver Output						
V _{OH}	Output High Level Voltage	-	4.5	5	-	V
V _{OL}	Output Low Level Voltage	-	_	-	0.4	V
t _R	Output Voltage Rise Time	C=1nF	_	32	1	ns
t _F	Output Voltage Fall Time	C=1nF	_	26	ı	ns
Compensation and Soft Start	(COMP Pin)					
GEA	Error Amplifier Trans- conductance	-	-	1000	-	μA/V
Іо-н	Sourcing Current	V _{COMP} = 0.5V	_	68	_	μΑ
I _{O-L}	Sinking Current	V _{COMP} = 4.5V	_	68	_	μΑ
V_{FB}	FB Pin Voltage	_	215	222	229	mV
Hysteresis Comparator						
Vcs_min	Boost Sense Voltage Low Level	V _{COMP} = 0V	-	-90	-	mV
I _{HYS}	Hysteresis Current	_	85	100	115	μΑ
Over-Temperature Protection	Over-Temperature Protection					
T _{OTSD}	Thermal Shutdown (Note 7)	-	-	+160	-	°C
T _{HYS}	Thermal Shutdown Hysteresis (Note 7)	-	_	+40	_	°C

Note 7: These parameters, although guaranteed by design, are not 100% tested in production.

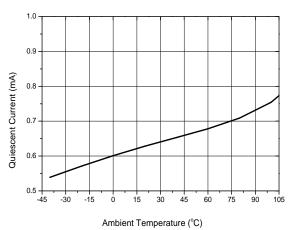


Performance Characteristics

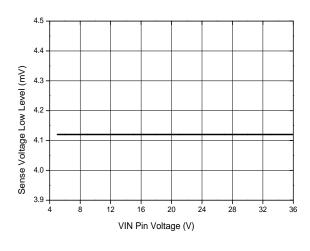
Quiescent Current vs. VIN Pin Voltage



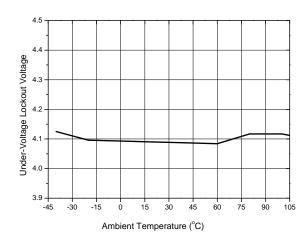
Quiescent Current vs. Ambient Temperature



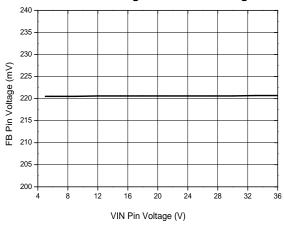
Under-Voltage Lockout Voltage vs. VIN Pin Voltage



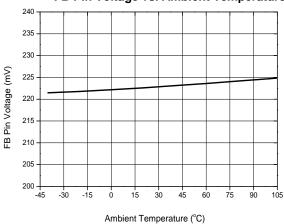
Under-Voltage Lockout Voltage vs. Ambient Temperature



FB Pin Voltage vs.VIN Pin Voltage



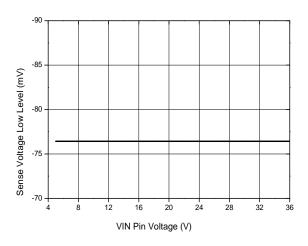
FB Pin Voltage vs. Ambient Temperature



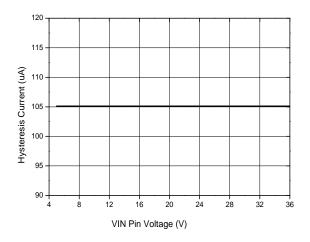


Performance Characteristics (Cont.)

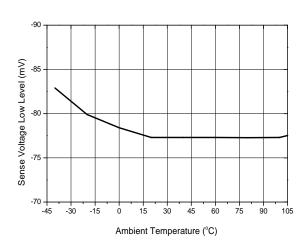
Boost Sense Voltage Low Level vs. **VIN Pin Voltage**



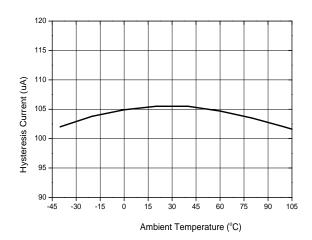
Hysteresis Current vs. VIN Pin Voltage



Boost Sense Voltage Low Level vs. **Ambient Temperature**



Hysteresis Current vs. Ambient Temperature





Application Information

Operation

The AL8823 is a boost converter that delivers an accurate constant current for driving LEDS. With hysteretic control scheme, the LED driver is compatible with most of commonly used electronic transformers. The driver can be compatible with leading-edge dimmer and trailing-edge dimmer.

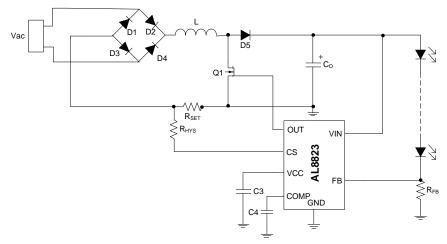


Figure 1. Typical Application Circuit

LED Current Control

The LED current is controlled by the resistor R_{FB} in Figure 1.

Connected between FB pin and Ground, the nominal average output current in the LED(s) is defined as:

$$I_{LED} = \frac{0.222}{R_{FR}}$$

R_{SET} and R_{HYS} Setting

The Boost converter of the AL8823 operates at continuous conduction mode and is based on hysteresis schematic which has lower threshold and upper threshold. Refer to Figure 2 depicting the inductor current waveform.

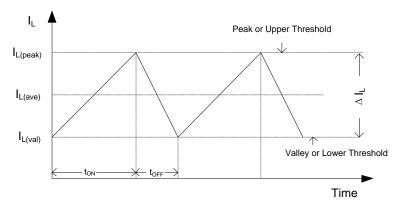


Figure 2. Inductor Current

When switch SW is turned on, the inductor current flows through R_{SET} and ramps up linearly. The rising current produces a voltage ramp across R_{SET} . When the voltage across R_{SET} reaches the upper threshold, switch SW is turned off. The inductor current continues to flow through R_{SET} but decays. The decaying current produces a falling voltage at R_{SET} . When the voltage across R_{SET} falls to the lower threshold, switch SW is turned on again.

The lower threshold voltage V_{LT} depends on the voltage V_{COMP} at COMP pin that varies with the input voltage and output load. The equation is shown as below.



Application Information (Cont.)

$$V_{LT} = \begin{cases} \frac{(V_{COMP} - 1.5) \times 0.6 + 1.4}{16} V, 1.5V \leq V_{COMP} \leq 5V \\ 0.088V, 0V \leq V_{COMP} \leq 1.5V \end{cases}$$

The range of V_{COMP} is from 0V to 5V.

The upper threshold depends on the lower threshold and the hysteresis value. The hysteresis value is set by external resister R_{HYS}. It is defined as below.

$$V_{HYS} = R_{HYS} \times 100 \mu A$$

According to the operation principle , the peak to peak current ΔI_L and the valley current $I_{L(val)}$ can be obtained by the below equations.

$$I_{L(val)} = \frac{V_{LT}}{R_{SFT}}$$

$$\Delta I_L = \frac{V_{HYS}}{R_{SET}}$$

Where:

 ΔI_L is the peak to peak current of inductor.

 $I_{L(val)}$ is the valley current of inductor.

From the Figure 2, the relationship between $I_{L(peak)}$, $I_{L(val)}$, $I_{L(ave)}$ and ΔI_{L} can be obtained as below.

$$I_{L(peak)} = I_{L(val)} + \Delta I_{L}$$

$$I_{L(ave)} = I_{L(val)} + \frac{1}{2} \times \Delta I_{L}$$

Where:

 $I_{L(peak)}$ is the peak current of inductor.

I_{L(ave)} is the average current of inductor.

As we know the average current $I_{L(ave)}$ depends on the output power, rated input voltage V_{IN1} of step-up converter and total efficiency η . So the average current $I_{L(ave)}$ can be obtained by the following equation.

$$I_{L(ave)} = \frac{(V_{LED} + 0.222) \times I_{LED}}{\eta \times V_{IN1}}$$

Where: V_{LED} is the voltage in LEDs.

Set ratio of ΔI_L to $I_{L(peak)}$ as K.



Application Information (Cont.)

$$K = \frac{\Delta I_L}{I_{L(peak)}}$$

R_{SET} and R_{HYS} can be obtained from above equations:

$$R_{SET} \! = \! \begin{cases} \frac{((V_{COMP} - 1.5) \times 0.6 + 1.4) \times (2 - K) \times \eta \times V_{IN1}}{32 \times (V_{LED} + 0.222) \times I_{LED} \times (1 \! - \! K)} 1.5V \leq V_{COMP} \leq 5V \\ \frac{0.044 \times (2 - K) \times \eta \times V_{IN1}}{(V_{LED} + 0.222) \times I_{LED} \times (1 \! - \! K)}, 0V \leq V_{COMP} \leq 1.5V \end{cases}$$

$$R_{HYS} = \frac{2 \times (V_{LED} + 0.222) \times I_{LED} \times K \times R_{SET} \times 10^4}{\eta \times V_{IN1} \times (2 - K)}$$

When the value of K, η and V_{COMP} are provided, the value of resister R_{SET} and R_{HYS} can be calculated according to these above equations. In order to get appropriate efficiency and Electronic Transformer (ET) compatibility, generally K is set between 0.4 and 0.8. Due to the range of V_{COMP} is from 0V to 5V, in order to get output voltage regulation, generally V_{COMP} is set as 3V at rated input voltage.

Inductor Selection

Because of the using of the hysteretic control scheme, the switching frequency in a boost configuration can be adjusted in accordance to the value of the inductor being used. The value of the inductor can be determined on the desired switching frequency by using the following equation:

$$L = \frac{[V_{\mathit{IN1}} - (R_{\mathit{SET}} + R_{\mathit{L}} + R_{\mathit{DSON}}) \times I_{\mathit{L(ave)}}] \times [V_{\mathit{LED}} + 0.222 + V_{\mathit{F}} + (R_{\mathit{L}} + R_{\mathit{SET}}) \times I_{\mathit{L(ave)}} - V_{\mathit{IN1}}]}{\Delta I_{\mathit{L}} \Big(V_{\mathit{LED}} + 0.222 + V_{\mathit{F}} - R_{\mathit{DSON}} \times I_{\mathit{L(ave)}} \Big) \times f_{\mathit{SW}}}$$

Where:

L is the coil inductance.

R_L is the coil resistance.

R_{DSON} is the switch SW resistance.

V_{IN1} is the rated input voltage.

V_F is the diode forward voltage.

f_{SW} is the desired switching frequency. Generally 500kHz to 800kHz switching frequency is suggested. Low switching frequency can decrease the switching loss but need to choose higher inductor values that will result in larger size in order to meet the saturation current. For example the relationship between switching frequency and inductor value is shown as below Table 1 in the same application system. Considering these factors, 500kHz switching frequency is recommend in typical application.

Inductance Value of L @ V_{AC} = 12 V_{AC} , V_{IN} = 22 V_{AC} = 180mA	Operation Frequency of SW at Peak Voltage Of V _{AC}		
10µH	840kHz		
15µH	800kHz		
22μΗ	680kHz		
33µH	465kHZ		

Table 1



Application Information (Cont.)

VIN OVP Protection

AL8823 has an internal over voltage protection to protect IC from excessive input voltage. When the voltage applied at VIN pin exceeds 39V, it will turn off the power switch. The power switch will be turned on again once the voltage at V_{IN} drops below 34V.

VCC Regulator

The VCC pin requires a capacitor C3 for stable operation and to store the charge for the large GATE switching currents. Choose a 10V rated low ESR, X7R or X5R, ceramic capacitor for best performance. A 4.7µF capacitor will be adequate for many applications. Place the capacitor close to the IC to minimize the trace length to the VCC pin and to the IC ground.

An internal current limit on the VCC output protects the excessive on-chip power dissipation. The VCC pin has set the output to 5V (typ.) to protect the internal FETs from excessive power dissipation caused by not being fully enhanced. If the VCC pin is used to drive extra circuits beside the AL8823, the extra loads should be limited to less than 8mA.

Output Capacitor Co

The capacitor C_0 is used to hold the bus voltage and reduce the ripple of LED current when the electronic transformer has no output. For most applications, it is recommended to use an aluminum electrolytic capacitor with greater than $220\mu F$ capacitance.

Compensation Capacitor C4

In applications powered by electronic transformer, the input voltage can change roughly in one cycle of AC power frequency. A $1\mu F$ ceramic capacitor C4 connected from COMP pin to ground help to stabilize the control loop of the regulator.

Diode Selection

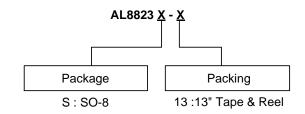
For maximum efficiency and performance, the rectifiers (D5) should be fast low capacitance Schottky diodes with low reverse leakage at maximum operating voltage and temperature. With its low power dissipation, the Schottky diode outperforms other silicon diodes and increases overall efficiency.

Over Temperature Protection

An over temperature protection feature is to protect the AL8823 from excessive heat damage. When the junction temperature exceeds +160°C, the internal FET will be turned off. When junction temperature drops below +120°C, IC will turn on both FETs and return to normal operation.

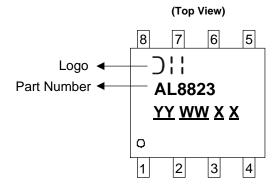


Ordering Information



Deat Newsher	Barton Oats Barton		13" Tape and Reel	
Part Number	Package Code	Package	Quantity	Part Number Suffix
AL8823S-13	S	SO-8	2500/Tape & Reel	-13

Marking Information



YY: Year: 15, 16, 17~ WW: Week: 01~52; 52

Represents 52 and 53 Week

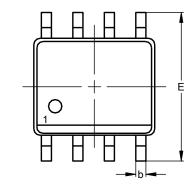
XX: Internal Code

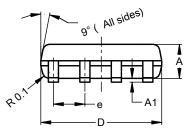


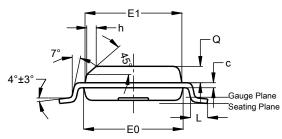
Package Outline Dimensions (All dimensions in mm(inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8





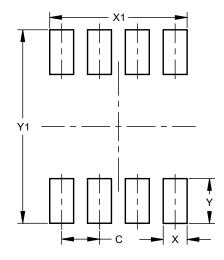


SO-8				
Dim	Min	Max	Тур	
Α	1.40	1.50	1.45	
A1	0.10	0.20	0.15	
b	0.30	0.50	0.40	
С	0.15	0.25	0.20	
D	4.85	4.95	4.90	
Е	5.90	6.10	6.00	
E1	3.80	3.90	3.85	
E0	3.85	3.95	3.90	
е	-		1.27	
h	-		0.35	
L	0.62	0.82	0.72	
Q	0.60	0.70	0.65	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8



Dimensions	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Υ	1.505		
Y1	6.50		



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