Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
LED	Forward current	lF	60	mA	
	Forward current derating (Ta ≥ 39°C)	ΔI _F / °C	-0.7	mA / °C	
	Peak forward current (100 µs pulse, 100 pps)	I _{FP}	1	А	
	Power dissipation	PD	100	mW	
	Power dissipation derating (Ta ≥ 25°C)	ΔP _D / °C	-1.0	mW / °C	
	Reverse voltage	V _R	5	V	
	Junction temperature	Tj	125	°C	
	Peak forward voltage (R_{GK} = 27 k Ω)	V _{DRM}	600	V	
	Peak reverse voltage (R_{GK} = 27 k Ω)	V _{RRM}	600	V	
	On-state current	I _{T(RMS)}	150	mA	
Detector	On–state current derating (Ta ≥ 25°C)	ΔI _T / °C	-2.0	mA / °C	
	Peak on-state current (100µs pulse, 120 pps)	I _{TP}	3	А	
	Peak one cycle surge current	I _{TSM}	2	А	
	Peak reverse gate voltage	V _{GM}	5	V	
	Power dissipation	PD	150	mW	
	Power dissipation derating (Ta ≥ 25°C)	ΔP _D / °C	-2.0	mW / °C	
	Junction temperature	Tj	100	°C	
Storage temperature range		T _{stg}	-55~125	°C	
Operating temperature range		T _{opr}	-55~100	°C	
Lead soldering temperature (10 s)		T _{sol}	260	°C	
Total p	Total package power dissipation		250	mW	
Total package power dissipation derating (Ta \ge 25°C)		ΔP _T / °C	-3.3	mW / °C	
Isolation voltage (AC, 1 min., R.H.≤ 60%)		BVS	4000	V _{rms}	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{AC}	_	_	240	Vac
Forward current	١ _F	15	20	25	mA
Operating temperature	T _{opr}	-25	_	85	°C
Gate to cathode resistance	R _{GK}	_	10	27	kΩ
Gate to cathode capacity	C _{GK}	_	0.01	0.1	μF

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

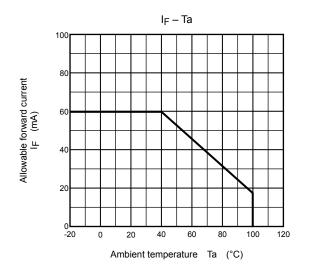
Individual Electrical Characteristics (Ta = 25°C)

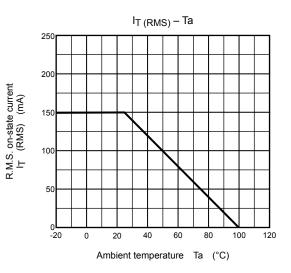
Characteristic		Symbol	Test Condition		Min.	Тур.	Max.	Unit
LED	Forward voltage	V _F	I _F = 10 mA		1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V			_	10	μA
	Capacitance	CT	V = 0, f = 1 MHz		-	30	_	pF
Detector	Off-state current		V _{AK} = 600 V R _{GK} = 27 kΩ	Ta = 25°C	-	10	5000	nA
		IDRM		Ta = 85°C	_	1	150	μA
	Reverse current	I _{RRM}	V _{KA} = 600 V	Ta = 25°C		10	5000	nA
			R _{GK} = 27 kΩ	Ta = 85°C		1	150	μA
	On-state voltage	V _{TM}	I _{TM} = 100 mA			0.9	1.3	V
	Holding current	IH	R _{GK} = 27 kΩ			0.2	_	mA
	Off-state dv / dt	dv / dt	V _{AK} = 420 V, R _{GK} = 27 kΩ			10	_	V/µs
	Capacitance C _j V = 0, f = 1 MHz	0		Anode to gate	_	20	_	~ ~
		Gate to cathode		350	_	pF		

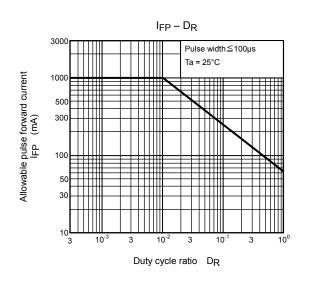
Coupled Characteristics (Ta = 25°C)

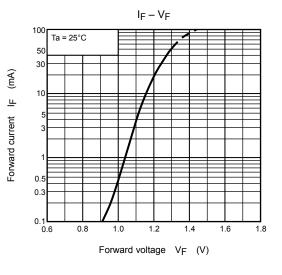
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _{AK} = 6 V, R _{GK} = 27 kΩ	_	5	10	mA
Furn-on time t_{ON} $I_F = 30 \text{ mA}, V_{AA} = 50 \text{ V}$ $R_{GK} = 27 \text{ k}\Omega$		-	10	_	μs	
Coupled dv / dt dv / dt $V_S = 500 V$, $R_{GK} = 27 k\Omega$		500	_	_	V / µs	
Capacitance input to output) C _S V _S = 0, f = 1 MHz		V _S = 0, f = 1 MHz		0.8	_	pF
Isolation resistance	R _S	V _S = 500 V	1×10 ¹²	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	4000	_	-	V
Isolation voltage		AC, 1 second, in oil	_	10000	_	V _{rms}
		DC, 1 minute, in oil	_	10000	_	V _{dc}

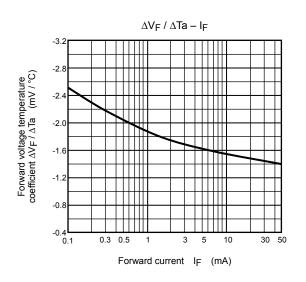
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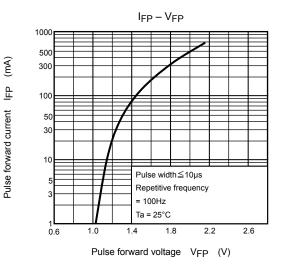




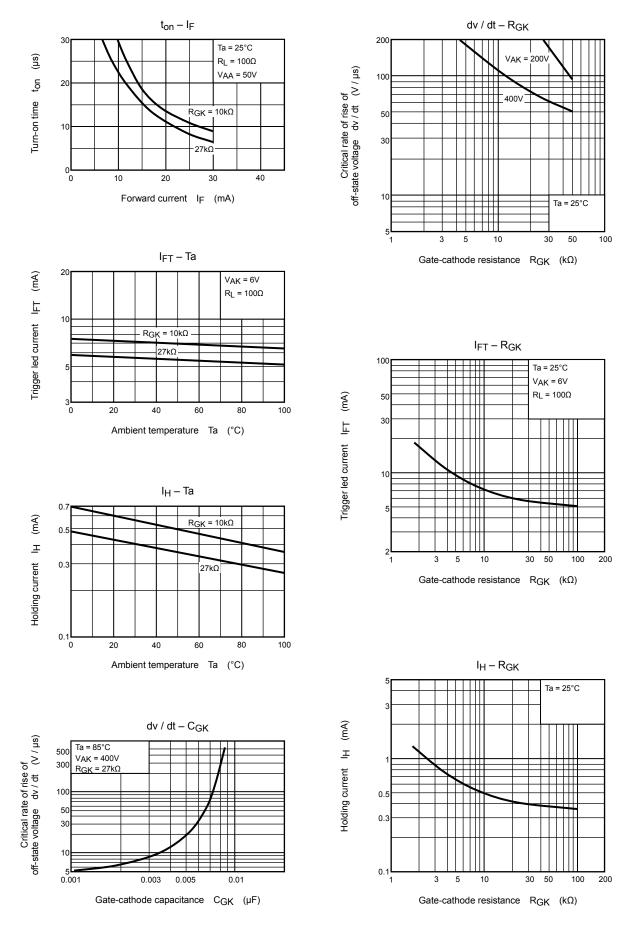








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