

TISP3xxxT3BJ Overvoltage Protector Series

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Absolute Maximum Ratings, $T_A = 25^\circ\text{C}$ (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage, (terminals 1-2 and 3-2)	'3070	± 58	V
	'3080	± 65	
	'3095	± 75	
	'3115	± 90	
	'3125	± 100	
	'3145	± 120	
	'3165	± 135	
	'3180	± 145	
	'3200	± 155	
	'3219	± 180	
	'3250	± 190	
	'3290	± 220	
	'3350	± 275	
	'3395	± 320	
Non-repetitive peak on-state pulse current (see Notes 1 and 2)	I_{PPSM}	2x250	A
2/10 (Telcordia GR-1089-CORE, 2/10 voltage wave shape)		2x250	
8/20 (IEC 61000-4-5, combination wave generator, 1.2/50 voltage wave shape)		2x150	
10/160 (TIA/EIA-IS-968 (replaces FCC Part 68), 10/160 μs voltage wave shape)		2x120	
5/310 (ITU-T K.44, 10/700 μs voltage wave shape used in K.20/.45/.21)		2x120	
5/320 (TIA/EIA-IS-968 (replaces FCC Part 68), 9/720 μs voltage wave shape)		2x100	
10/560 (TIA/EIA-IS-968 (replaces FCC Part 68), 10/560 μs voltage wave shape)		2x80	
Non-repetitive peak on-state current (see Notes 1 and 2)	I_{TSM}	2x25	A
50 Hz, 1 cycle		2x30	
60 Hz, 1 cycle		2x1.2	
1000 s 50 Hz/60 Hz a.c.			
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value < 50 A	di_T/dt	500	A/ μs
Junction temperature	T_J	-40 to +150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-65 to +150	$^\circ\text{C}$

NOTES: 1. Initially, the device must be in thermal equilibrium with $T_J = 25^\circ\text{C}$.

2. These non-repetitive rated currents are peak values of either polarity. The rated current values are applied to the terminals 1 and 3 simultaneously (in this case the terminal 2 return current will be the sum of the currents applied to the terminals 1 and 3). The surge may be repeated after the device returns to its initial conditions.

Recommended Operating Conditions

Component	Min	Typ	Max	Unit
Series resistor for GR-1089-CORE first-level surge survival	5			Ω
Series resistor for ITU-T recommendation K.20/.45/.21 (coordination with 400 V GDT at 4 kV)	6.4			
R1, R2 Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 9/720 survival	0			
Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 10/560 survival	0			
Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 10/160 survival	2.5			

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Electrical Characteristics for the 1 and 2 or the 3 and 2 Terminals, $T_A = 25\text{ }^\circ\text{C}$

Parameter	Test Conditions	Min	Typ	Max	Unit
I_{DRM} Repetitive peak off-state current	$V_D = V_{\text{DRM}}$ $T_A = 25\text{ }^\circ\text{C}$ $T_A = 85\text{ }^\circ\text{C}$			± 5 ± 10	μA
$V_{(\text{BO})}$ AC breakover voltage	$dv/dt = \pm 250\text{ V/ms}$, $R_{\text{SOURCE}} = 300\text{ }\Omega$			± 70 ± 80 ± 95 ± 115 ± 125 ± 145 ± 165 ± 180 ± 200 ± 219 ± 250 ± 290 ± 350 ± 395	V
$V_{(\text{BO})}$ Ramp breakover voltage	$dv/dt = \pm 1000\text{ V}/\mu\text{s}$, Linear voltage ramp, Maximum ramp value = $\pm 500\text{ V}$ $di/dt = \pm 20\text{ A}/\mu\text{s}$, Linear current ramp, Maximum ramp value = $\pm 10\text{ A}$			± 81 ± 91 ± 107 ± 128 ± 138 ± 159 ± 179 ± 195 ± 215 ± 234 ± 265 ± 304 ± 361 ± 403	V
$I_{(\text{BO})}$ Breakover current	$dv/dt = \pm 250\text{ V/ms}$, $R_{\text{SOURCE}} = 300\text{ }\Omega$			± 800	mA
I_H Holding current	$I_T = \pm 5\text{ A}$, $di/dt = \pm 30\text{ mA/ms}$	± 150			mA
dv/dt Critical rate of rise of off-state voltage	Linear voltage ramp, Maximum ramp value $< 0.85V_{\text{DRM}}$	± 5			kV/ μs
I_D Off-state current	$V_D = \pm 50\text{ V}$ $T_A = 85\text{ }^\circ\text{C}$			± 10	μA

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Electrical Characteristics for the 1 and 2 or the 3 and 2 Terminals, $T_A = 25\text{ }^\circ\text{C}$ (Continued)

Parameter	Test Conditions	Min	Typ	Max	Unit
C_{off} Off-state capacitance	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = 0$,		95	114	pF
			69	83	
			51	62	
	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = -1\text{ V}$		90	108	
			63	76	
			46	55	
	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = -2\text{ V}$		83	100	
			59	70	
			42	51	
	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = -50\text{ V}$		43	51	
			29	35	
			20	24	
	$f = 1\text{ MHz}$, $V_d = 1\text{ V rms}$, $V_D = -100\text{ V}$ (see Note 3)		16	19	

NOTE 3: These capacitance measurements employ a three terminal capacitance bridge incorporating a guard circuit. The unmeasured third terminal is connected to the guard terminal of the bridge.

Thermal Characteristics

Parameter	Test Conditions	Min	Typ	Max	Unit
$R_{\theta JA}$ Junction to free air thermal resistance	EIA/JESD51-3 PCB, $I_T = I_{TSM(1000)}$, $T_A = 25\text{ }^\circ\text{C}$, (see Note 4)			90	$^\circ\text{C/W}$

NOTE 4: EIA/JESD51-2 environment and PCB has standard footprint dimensions connected with 5 A rated printed wiring track widths.

Parameter Measurement Information

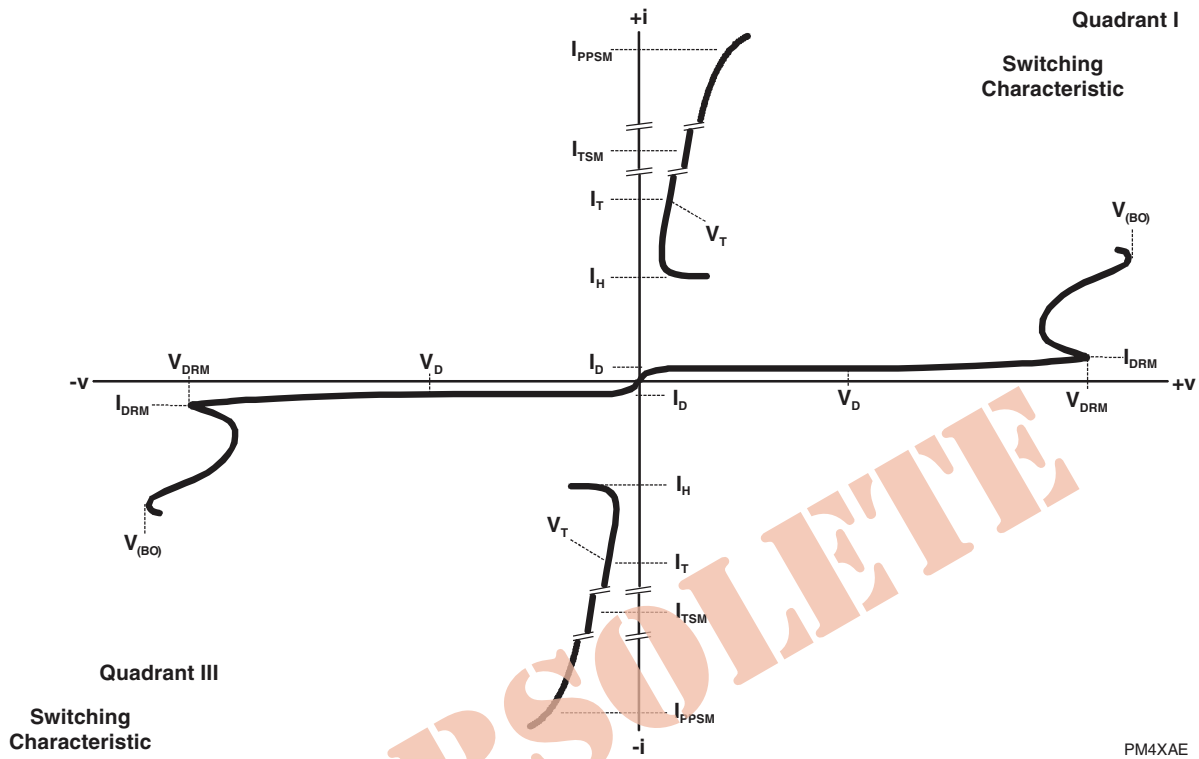


Figure 1. Voltage-Current Characteristic for Terminal Pairs 1-2 and 3-2
All Measurements are Referenced to Terminal 2

PM4XAE

Typical Characteristics

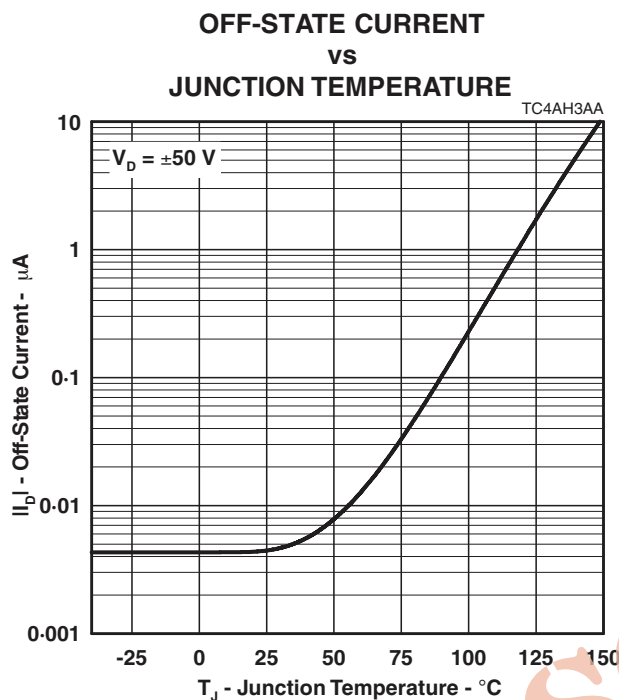


Figure 2.

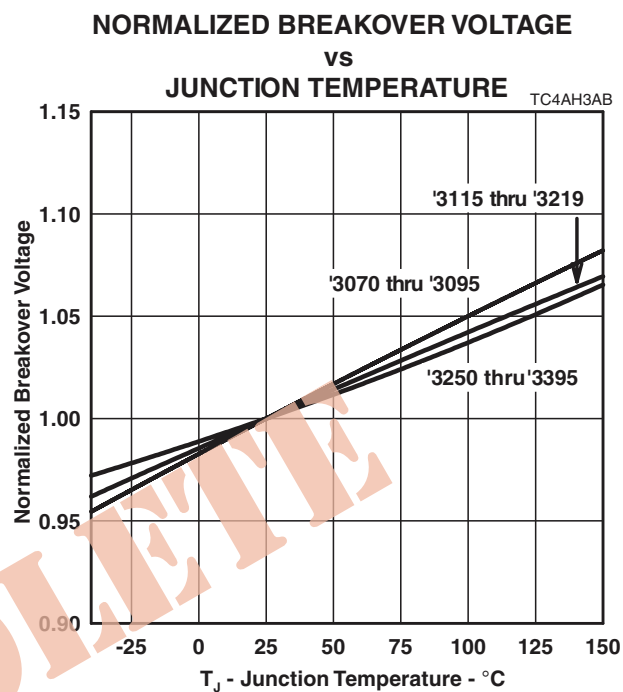


Figure 3.

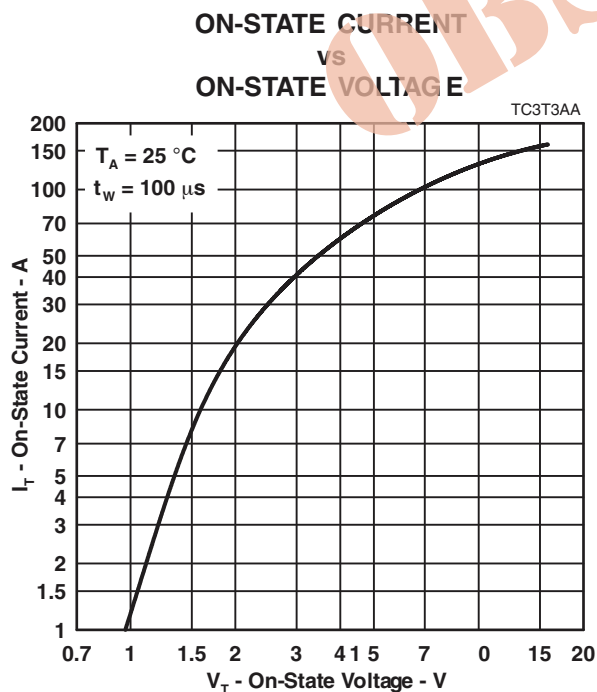


Figure 4.

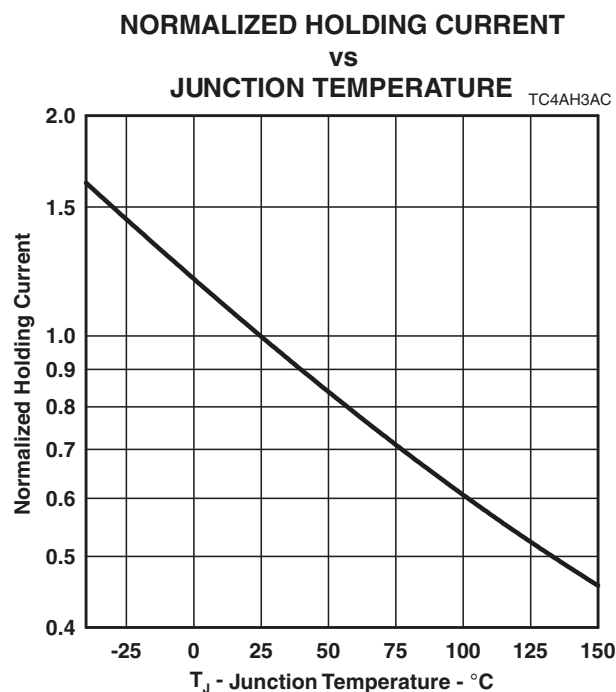


Figure 5.

Rating and Thermal Information

NON-REPETITIVE PEAK ON-STATE CURRENT VS CURRENT DURATION

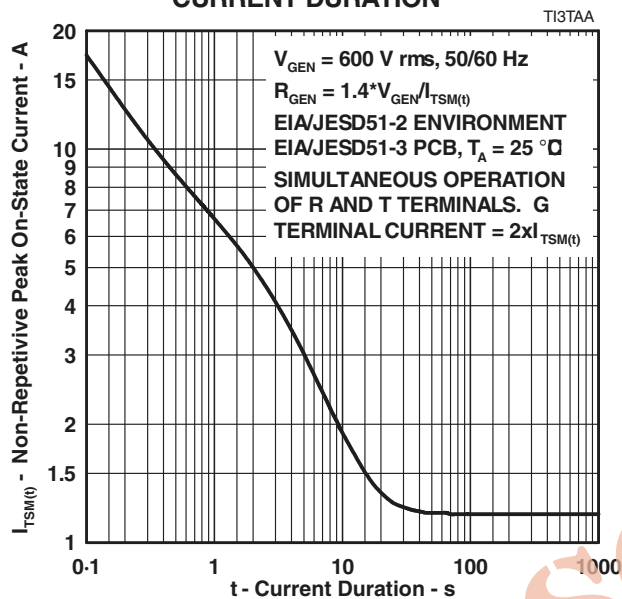


Figure 8.

V_{DRM} DERATING FACTOR VS MINIMUM AMBIENT TEMPERATURE

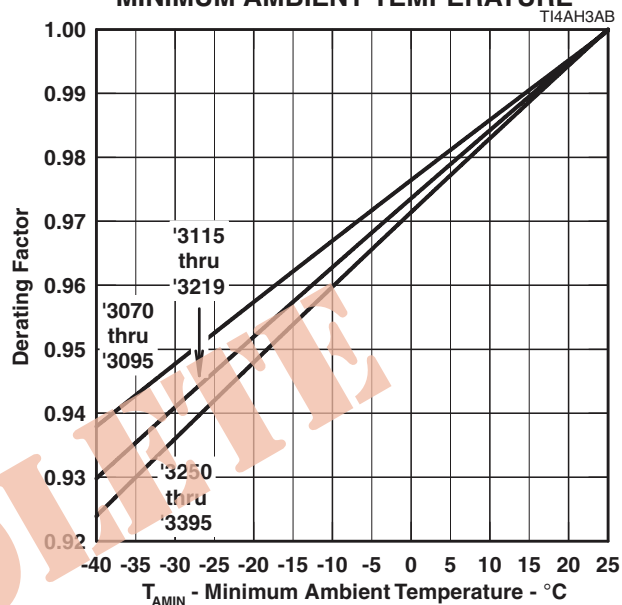


Figure 9.

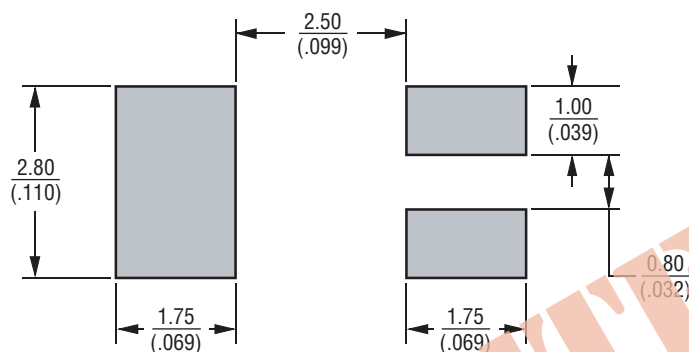
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MECHANICAL DATA

Recommended Printed Wiring Land Pattern Dimensions

SMB03 Pad Size



DIMENSIONS ARE: $\frac{\text{MILLIMETERS}}{(\text{INCHES})}$

MD3BJAAA

Device Symbolization Code

Devices will be coded as below.

Device	Symbolization Code
TISP3070T3	3070T3
TISP3080T3	3080T3
TISP3095T3	3095T3
TISP3115T3	3115T3
TISP3125T3	3125T3
TISP3145T3	3145T3
TISP3165T3	3165T3
TISP3180T3	3180T3
TISP3200T3	3200T3
TISP3219T3	3219T3
TISP3250T3	3250T3
TISP3290T3	3290T3
TISP3350T3	3350T3
TISP3395T3	3395T3

Carrier Information

For production quantities, the carrier will be embossed tape reel pack. Evaluation quantities may be shipped in bulk pack or embossed tape.

Package	Carrier	Standard Quantity
SMB	Embossed Tape Reel Pack	3000

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SEPTEMBER 2001 - REVISED MAY 2011

Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

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