SUM45N25-58

Vishay Siliconix



| Parameter | Symbol | Test Conditions | Min | Тур | Max | Unit |
|---|----------------------|--|-----|-------|-------|------|
| Static | <u> </u> | | | L | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$ | 250 | | | V |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | 2 | | 4 | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 30 \text{ V}$ | | | ± 250 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 250 V, V _{GS} = 0 V | | | 1 | μΑ |
| | | V _{DS} = 250 V, V _{GS} = 0 V, T _J = 125 °C | | | 50 | |
| | | V _{DS} = 250 V, V _{GS} = 0 V, T _J = 175 °C | | | 250 | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$ | 70 | | | Α |
| Drain-Source On-State Resistance ^a | r _{DS(on)} | V _{GS} = 10 V, I _D = 20 A | | 0.047 | 0.058 | Ω |
| | | V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C | | | 0.121 | |
| | | V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C | | | 0.163 | |
| | | V _{GS} = 6 V, I _D = 15 A | | 0.049 | 0.062 | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 20 A | | 70 | | S |
| Dynamic ^b | | | | • | | |
| Input Capacitance | C _{iss} | V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz | | 5000 | | pF |
| Output Capacitance | C _{oss} | | | 300 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 170 | | |
| Total Gate Charge ^c | Qg | V _{DS} = 125 V, V _{GS} = 10 V, I _D = 45 A | | 95 | 140 | nC |
| Gate-Source Charge ^c | Q _{gs} | | | 28 | | |
| Gate-Drain Charge ^c | Q_{gd} | | | 34 | | |
| Gate Resistance | R_{g} | f = 1 MHz | | 1.6 | | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | V_{DD} = 100 V, R_L = 2.78 Ω I_D \cong 45 A, V_{GEN} = 10 V, R_g = 2.5 Ω | | 22 | 35 | ns |
| Rise Time ^c | t _r | | | 220 | 330 | |
| Turn-Off Delay Time ^c | t _{d(off)} | | | 40 | 60 | |
| Fall Time ^c | t _f | | | 145 | 220 | |
| Source-Drain Diode Ratings and Cha | aracteristics | (T _C = 25 °C) ^b | | • | | |
| Continuous Current | I _S | | | | 45 | А |
| Pulsed Current | I _{SM} | | | | 70 | |
| Forward Voltage ^a | V _{SD} | I _F = 45 A, V _{GS} = 0 V | | 1.0 | 1.5 | ٧ |
| Reverse Recovery Time | t _{rr} | I _F = 45 A, di/dt = 100 A/μs | | 150 | 225 | ns |
| Peak Reverse Recovery Current | I _{RM(REC)} | | | 12 | 18 | Α |
| Reverse Recovery Charge | Q _{rr} | | | 0.9 | 2 | μC |

Notes:

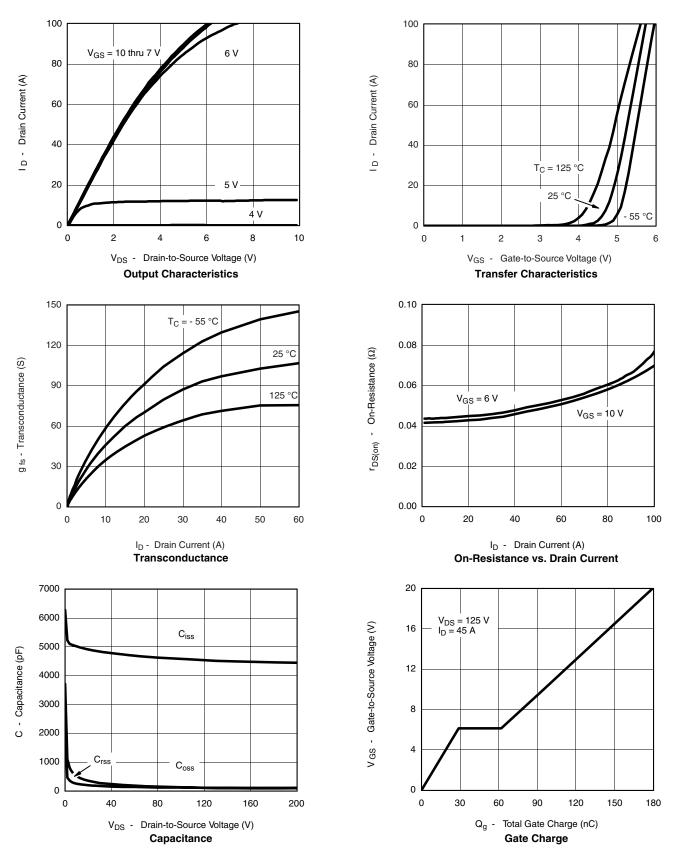
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



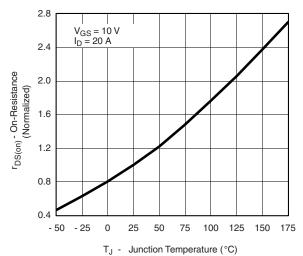
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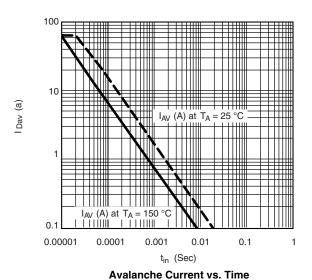
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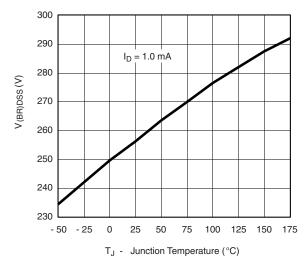
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

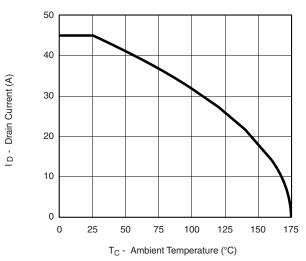


Drain Source Breakdown vs. Junction Temperature



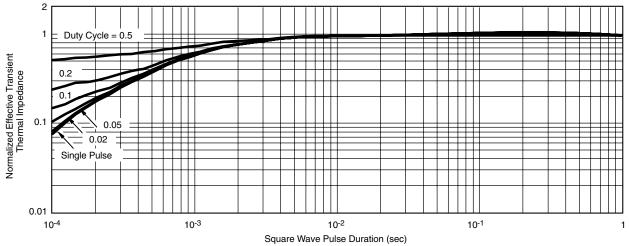
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THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature

Safe Operating Area, Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case

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