Absolute Maximum Ratings

Symbol	Parameter	Value	Units
P_{Pk}	Peak Pulse Power (t_p=8/20µs)	600	W
I_{PP}	Peak Pulse Current (t_p=8/20µs)	19	A
T_{OP}	Operating Temperature	-40 to 125	°C
T_{STOR}	Storage Temperature	-55 to 150	°C

Notes:

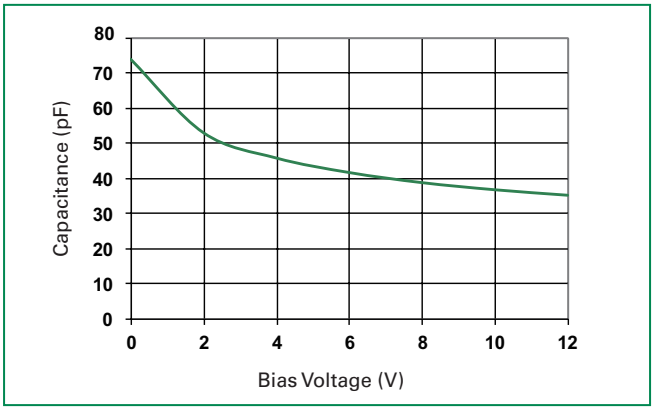
CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

SM712 Electrical Characteristics (T_{OP}=25°C)

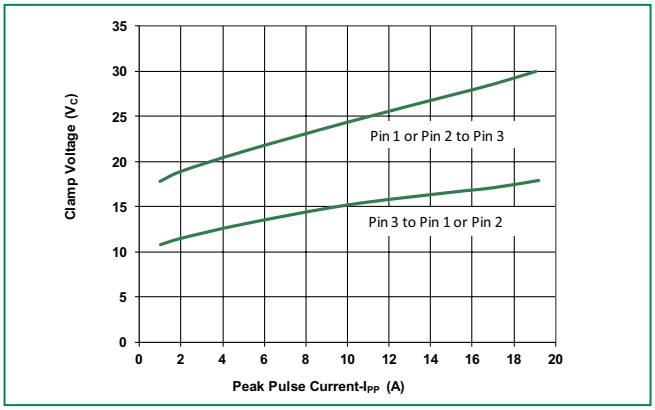
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Reverse Standoff Voltage	V_{RWM}	I_R≤1µA, Pin 3 to Pin 1 or Pin 2			7.0	V
		I_R≤1µA, Pin 1 or Pin 2 to Pin 3			12.0	V
Reverse Breakdown Voltage	V_R	I_R=1mA, Pin 3 to Pin 1 or Pin 2	7.5			V
		I_R=1mA, Pin 1 or Pin 2 to Pin 3	13.3			V
Leakage Current	I_{LEAK}	V_R=7V			20	µA
		V_R=12V			1	µA
Clamp Voltage[1]	V_C	I_{PP}=1A, t_p=8/20µs, Pin 1 or Pin 2 to Pin 3			19	V
		I_{PP}=1A, t_p=8/20µs, Pin 3 to Pin 1 or Pin 2			11	V
		I_{PP}=19A, t_p=8/20µs, Pin 1 or Pin 2 to Pin 3			31	V
		I_{PP}=19A, t_p=8/20µs, Pin 3 to Pin 1 or Pin 2			19	V
Dynamic Resistance[1]	R_{DYN}	$(V_{C2} - V_{C1}) / (I_{PP2} - I_{PP1})$		0.5		Ω
ESD Withstand Voltage[1]	V_{ESD}	IEC 61000-4-2 (Contact Discharge)	±30			kV
		IEC 61000-4-2 (Air Discharge)	±30			kV
Diode Capacitance[1]	$C_{I/O-GND}$	Reverse Bias=0V, f=1MHz; Pin 1 or Pin 2 to Pin 3			75	pF

Notes : 1. Parameter is guaranteed by design and/or device characterization.

Capacitance vs. Reverse Bias

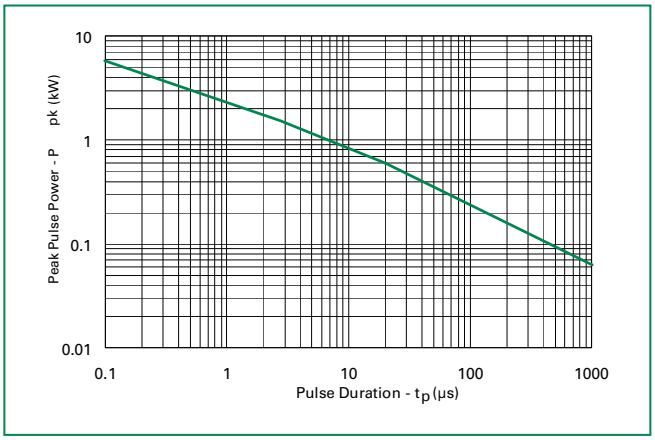


Clamping Voltage vs. I_{PP}

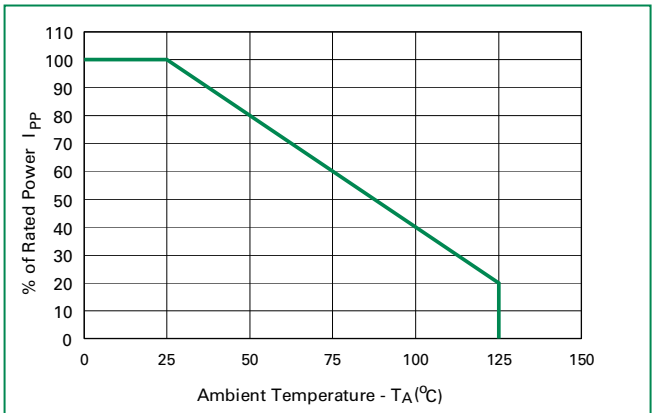


Revised: 08/22/19

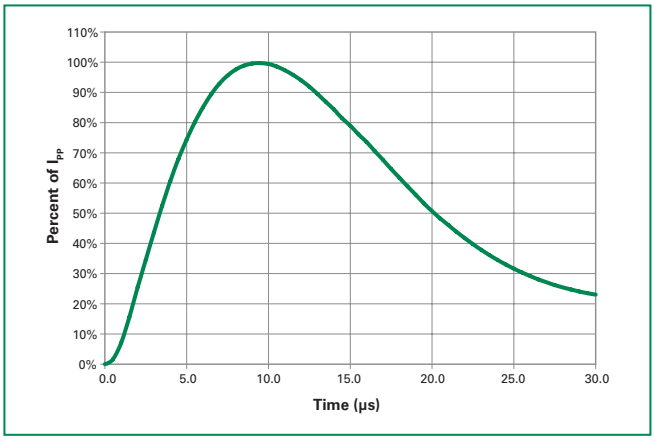
Non-Repetitive Peak Pulse Power vs. Pulse Time



Power Derating Curve

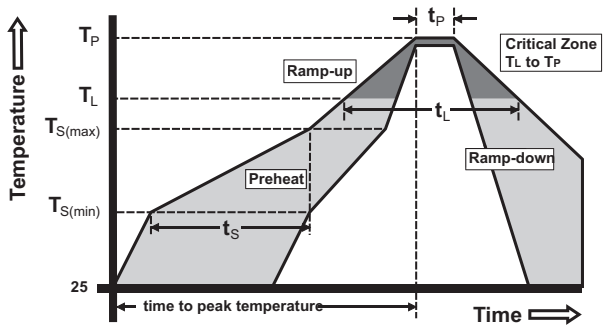


Pulse Waveform



Soldering Parameters

Reflow Condition		Pb – Free assembly
Pre Heat	- Temperature Min ($T_{s(min)}$)	150°C
	- Temperature Max ($T_{s(max)}$)	200°C
	- Time (min to max) (t_s)	60 – 180 secs
Average ramp up rate (Liquidus) Temp (T_L) to peak		3°C/second max
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C/second max
Reflow	- Temperature (T_L) (Liquidus)	217°C
	- Temperature (t_L)	60 – 150 seconds
Peak Temperature (T_P)		$260^{+0/-5}$ °C
Time within 5°C of actual peak Temperature (t_p)		20 – 40 seconds
Ramp-down Rate		6°C/second max
Time 25°C to peak Temperature (T_P)		8 minutes Max.
Do not exceed		260°C

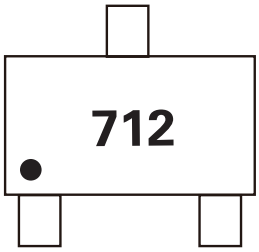


Revised: 08/22/19

TVS Diode Arrays (SPA® Diodes)

General Purpose ESD Protection - SM712

Part Marking System



Part Numbering System

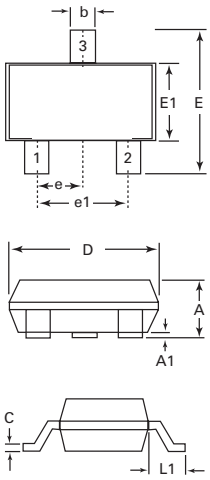
SM 712 - 02 H T G

- SM: TVS Diode Arrays (SPA® Diodes)
- 712: Series
- 02: Working Voltage
- H: Package, H: SOT23-3
- T: T= Tape & Reel
- G: G= Green
- 02: Number of Channels

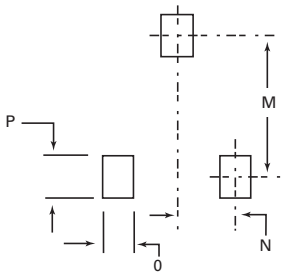
Ordering Information

Part Number	Package	Marking	Min. Order Qty.
SM712-02HTG	SOT23-3	712	3000

Package Dimensions — SOT23-3



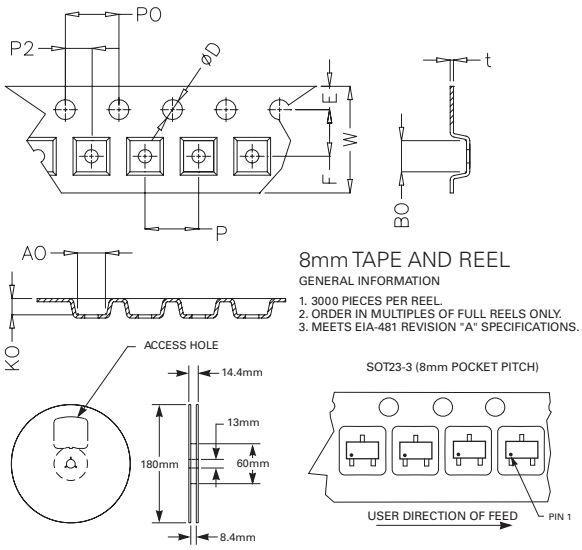
Recommended Pad Layout



Package	SOT23-3			
Pins	3			
JEDEC	TO-236			
	Millimeters		Inches	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A1	0.01	0.1	0.0004	0.004
b	0.3	0.5	0.012	0.020
c	0.08	0.2	0.003	0.008
D	2.8	3.04	0.110	0.120
E	2.1	2.64	0.083	0.104
E1	1.2	1.4	0.047	0.055
e	0.95 BSC		0.038 BSC	
e1	1.90 BSC		0.075 BSC	
L1	0.54 REF		0.021 REF	
M	–	2.29	–	.090
N	–	0.95	–	0.038
O	–	0.78	–	.030TYP
P	–	0.78	–	.030TYP

Revised: 08/22/19

Embossed Carrier Tape & Reel Specification — SOT23-3



Symbol	Millimetres		Inches	
	Min	Max	Min	Max
E	1.65	1.85	0.065	0.073
F	3.40	3.60	0.134	0.142
P2	1.90	2.10	0.075	0.083
D	1.40	1.60	0.055	0.063
P0	3.90	4.10	0.154	0.161
W	7.70	8.30	0.303	0.327
P	3.90	4.10	0.154	0.161
A0	3.05	3.25	0.120	0.128
B0	2.67	2.87	0.105	0.113
K0	1.12	1.32	0.044	0.052
t	0.22	0.24	0.009	0.009