40 V, 600 mA, PNP switching transistor

6. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PMBT4403	TO-236AB	plastic surface-mounted package; 3 leads	SOT23	

7. Marking

Table 4. Marking codes

Type number	Marking code [1]
PMBT4403	%2T

[1] % = placeholder for manufacturing site code

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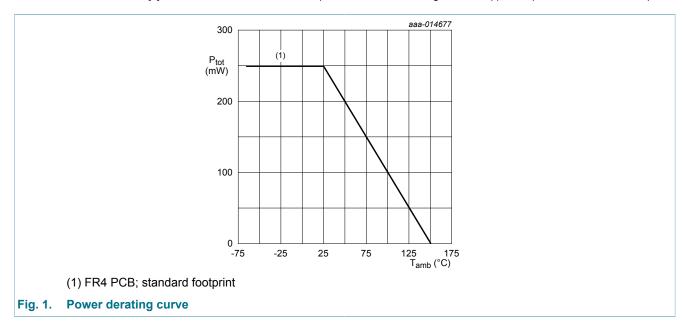
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-40	V
V_{CEO}	collector-emitter voltage	open base		-	-40	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-600	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-800	mA
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
T _j	junction temperature			_	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

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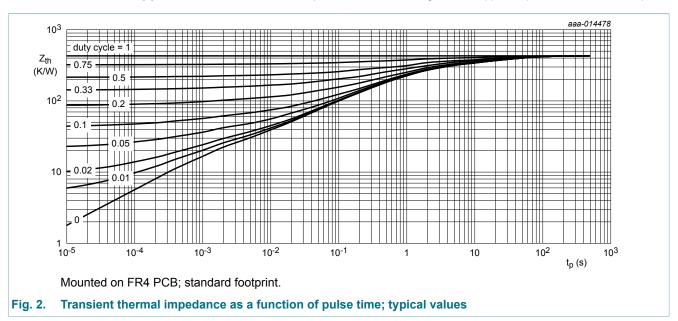
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[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



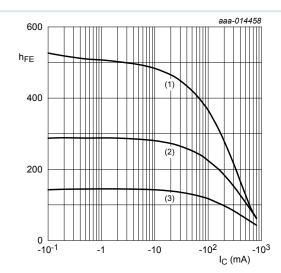
40 V, 600 mA, PNP switching transistor

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = -40 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-50	nA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -0.1 mA; T_{amb} = 25 °C	30	-	-	
		V_{CE} = -1 V; I_{C} = -1 mA; T_{amb} = 25 °C	60	-	-	
		V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	-	
		V_{CE} = -2 V; I_{C} = -150 mA; T_{amb} = 25 °C	100	-	300	
		V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	20	-	-	
GEGGE	collector-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; T_{amb} = 25 °C	-	-	-400	mV
		I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-	-750	mV
V _{BEsat}	base-emitter saturation voltage	I_C = -150 mA; I_B = -15 mA; T_{amb} = 25 °C	-	-	-950	mV
		I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-	-1.3	V
t _d	delay time	I _C = -150 mA; I _{Bon} = -15 mA;	-	-	15	ns
t _r	rise time	I _{Boff} = 15 mA; T _{amb} = 25 °C	-	-	30	ns
t _{on}	turn-on time		-	-	40	ns
t _s	storage time		-	-	300	ns
t _f	fall time		-	-	50	ns
t _{off}	turn-off time		-	-	350	ns
C _C	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	8.5	pF
C _E	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; f_{amb} = 25 °C	-	-	35	pF
f _T	transition frequency	V_{CE} = -10 V; I_{C} = -20 mA; f = 100 MHz; T_{amb} = 25 °C	200	-	-	MHz

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$$V_{CE} = -2 V$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 3. DC current gain as a function of collector current; typical values

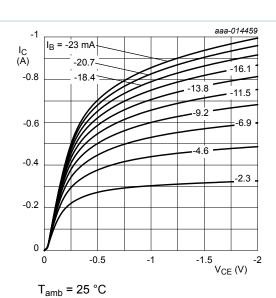
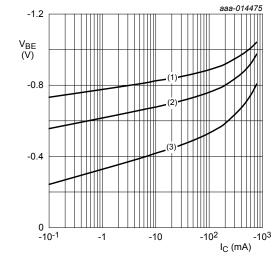


Fig. 4. Collector current as a function of collectoremitter voltage; typical values



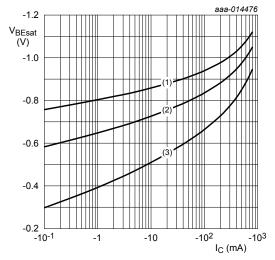
$$V_{CE} = -2 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 5. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

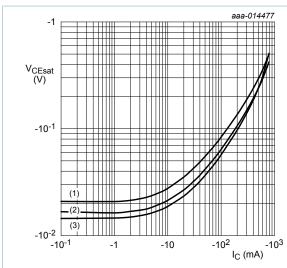
(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values

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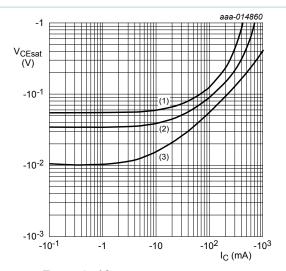
$$I_C/I_B = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values



$$T_{amb}$$
 = 25 °C

(1)
$$I_C/I_B = 100$$

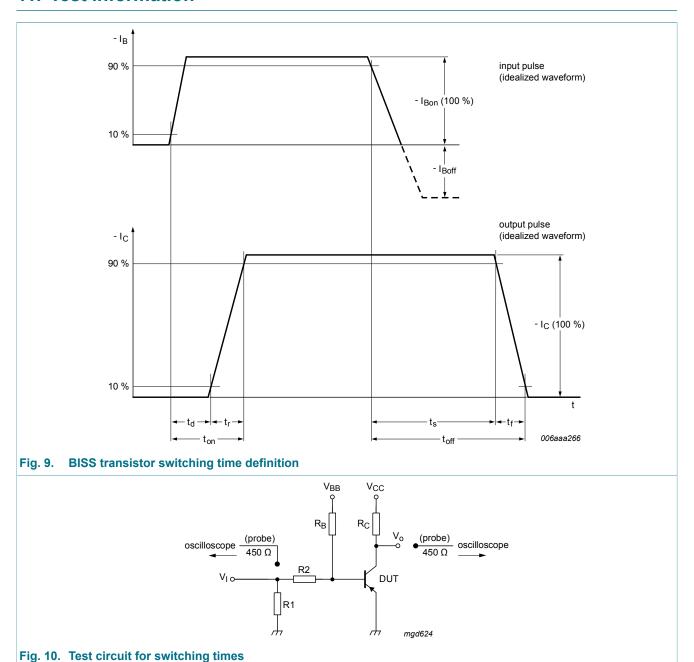
(2)
$$I_{\rm C}/I_{\rm B} = 50$$

(3)
$$I_C/I_B = 10$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

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11. Test information



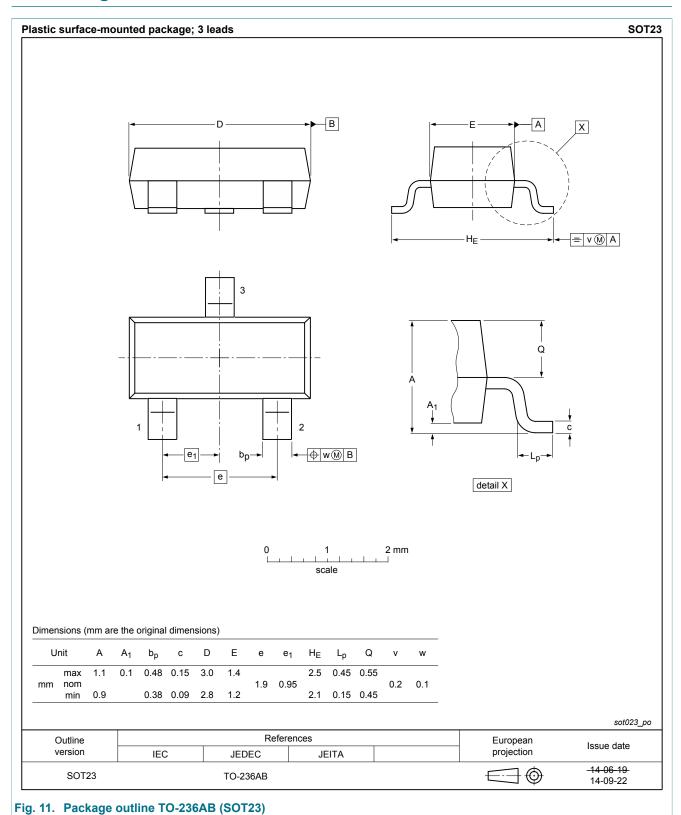
11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline



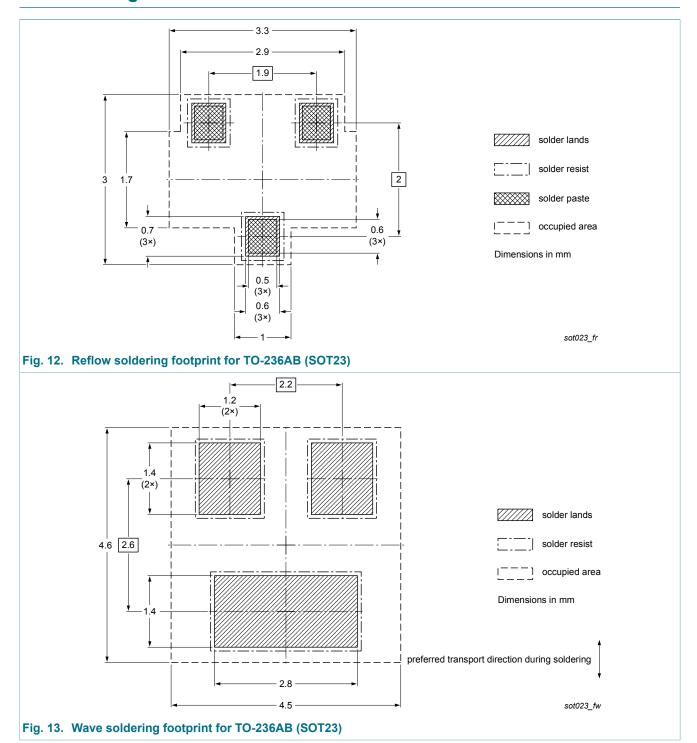
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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMBT4403 v.5	20150305	Product data sheet	-	PMBT4403 v.4		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors Legal texts have been adapted to the new company name where appropriate 					
PMBT4403 v.4	20040121	Product data sheet	-	PMBT4403 v.3		
PMBT4403 v.3	19990415	Product specification	-	PMBT4403 v.2		
PMBT4403 v.2	19970505	Product specification	-	PMBT4403 v.1		
PMBT4403 v.1	19940901	Product specification	-	-		

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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