

10GbE and 10GFC Compliant Transceiver

TEST FEATURES

- Extensive control of loopback, BIST, and operating modes via an 802.3 compliant MDC/MDIO serial interface (1.2 V).
- On-chip packet generator/checker provides at-speed diagnostics.
- Extensive built-in error counters per channel.
- Support for IEEE 1149.1 JTAG testing on all pins.
- Comprehensive evaluation platform for easy customer qualification.

PHYSICAL CHARACTERISTICS

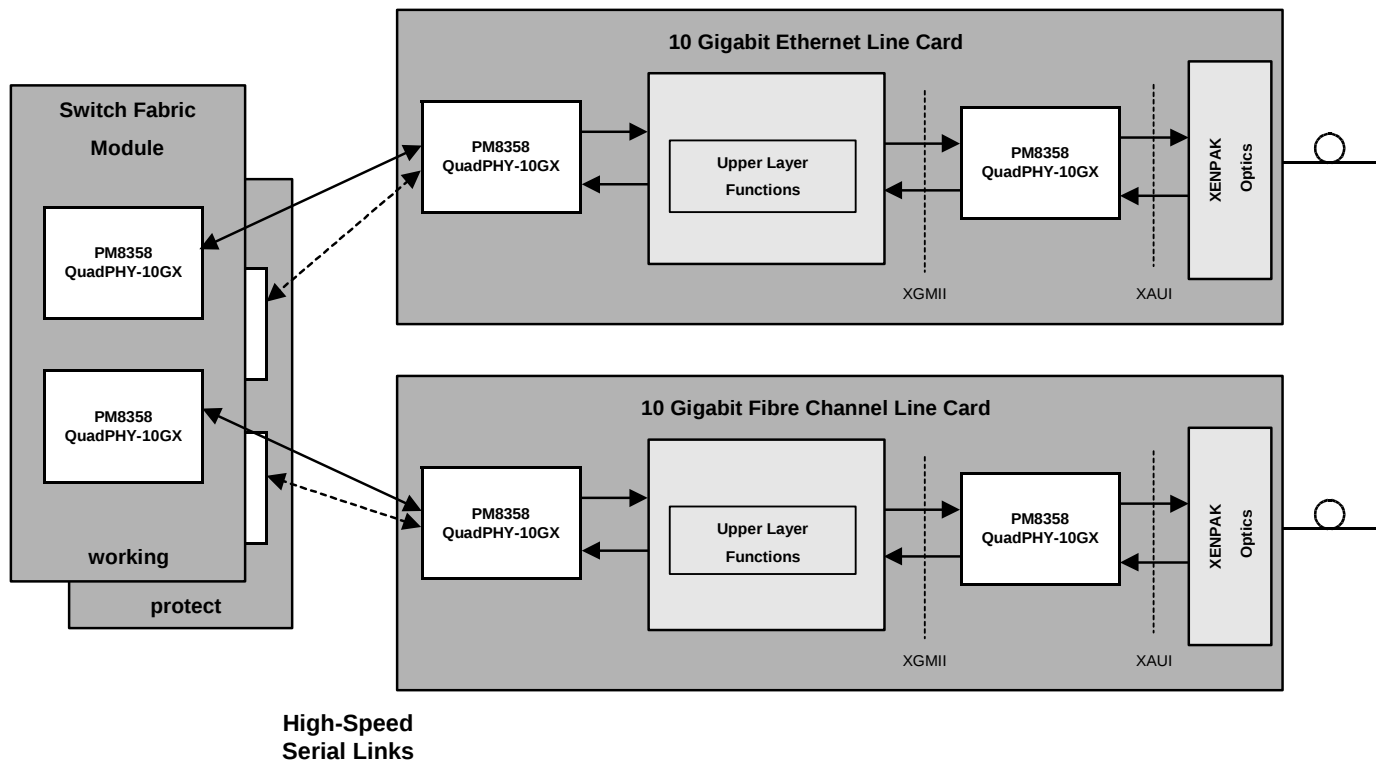
- 1.2/2.5 V, 0.13 μm CMOS technology.
- Small footprint 289-ball CABGA (19 mm x 19 mm).
- 1.3 W typical power at 3.2 Gbit/s (non redundant mode).

APPLICATIONS

- High-speed serial backplanes.
- 10GbE (XAUI) and 1GbE line cards.
- 10GFC and 2GFC line cards.
- Infiniband transceivers.
- OC-48 Serializer/Deserializer.
- PCI Express
- Serial Rapid I/O
- OBSAI RP3
- XAUI retimer.

TYPICAL APPLICATION

10GE AND 10GFC BACKPLANE DRIVER



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