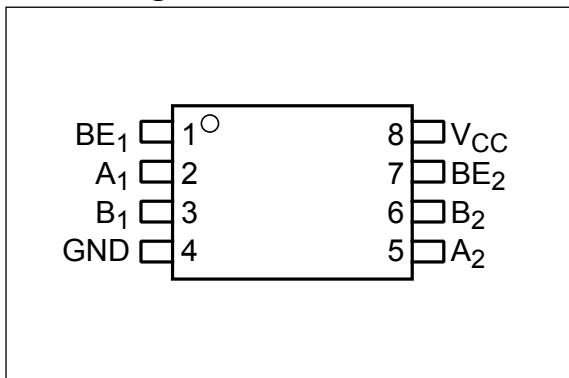


Pin Configuration



Pin Description

Pin Name	Description
BE _n	Switch Enable
A ₂ -A ₁	Bus A
B ₂ -B ₁	Bus B
V _{CC}	Power
GND	Ground

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current.....	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 4\text{V}$ to 5.5V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0		V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		–0.5	0.8	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}, V_{IN} = V_{CC}$			± 1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}, V_{IN} = \text{GND}$			± 1	
I_{OZ}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$			± 1	
I_{ON}	Low Impedance Output Current	$0 \leq A, B \leq V_{CC}$			± 1	
V_H	Input Hysteresis at Control Pins			250		mV
R_{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = 4.5\text{V}, V_{IN} = 0.0\text{V}, I_{ON} = 30\text{mA}$ or 64mA		4	7	Ω
		$V_{CC} = 4.5\text{V}, V_{IN} = 2.4\text{V}, I_{ON} = -15\text{mA}$		8	15	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 5.0\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
- Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (A, B) pins.

Capacitance ($T_A = 25^{\circ}\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽¹⁾	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3	pF
C_{OFF}	A/B Capacitance, Switch Off		5	
C_{ON}	A/B Capacitance, Switch On		10	

Notes:

- This parameter is determined by device characterization but is not production tested.

PI5C3305
Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		0.1	3.0	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH		V _{IN} = 3.4V ⁽³⁾			2.5	mA

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V_{CC} = 5.0V, +25°C ambient.
- Per TTL driven input (V_{IN} = 3.4V, control inputs only); A and B pins do not contribute to I_{CC}.

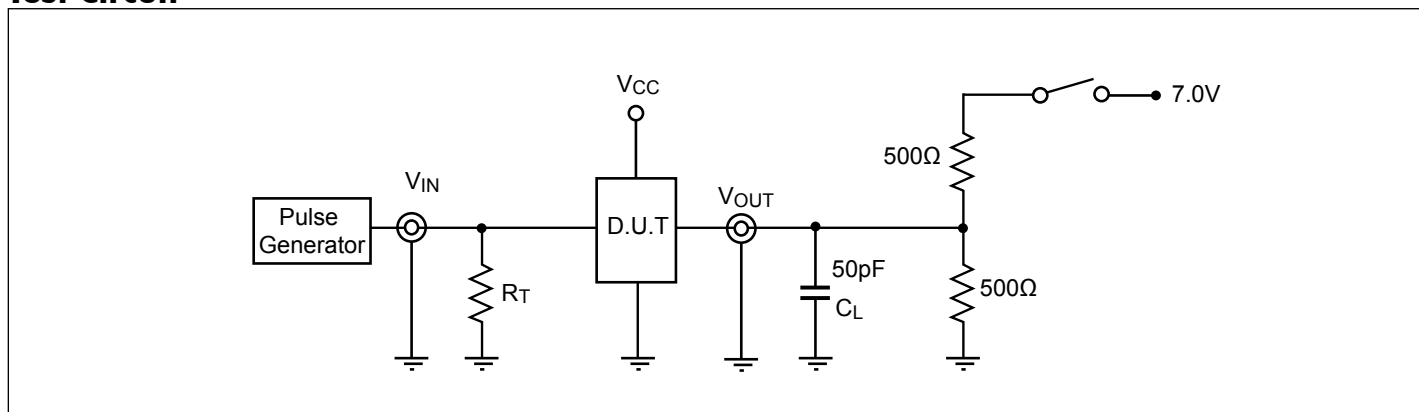
Switching Characteristics Over Operating Range

Parameters	Description	Test Conditions ⁽¹⁾	V _{CC} = 5V ± 10%		V _{CC} = 4V		Units
			Min.	Max.	Min.	Max.	
t _{PLH} t _{PHL}	Propagation Delay ^(2, 3) A to B, B to A	C _L = 50pF R _L = 500Ω		0.25		0.25	ns
t _{PZH} t _{PZL}	Bus Enable Time		1.0	4.9		5.5	
t _{PHZ} t _{PLZ}	Bus Disable Time		1.0	4.2		4.5	

Notes:

- See test circuit and waveforms.
- This parameter is guaranteed but not tested on Propagation Delays.
- The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Test Circuit



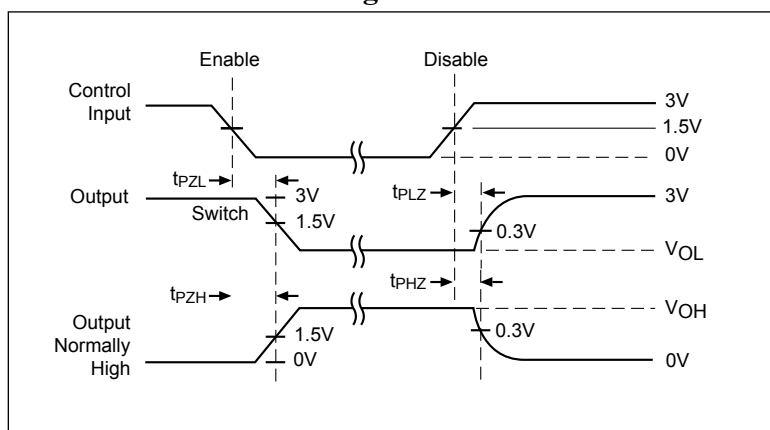
Switch Positions⁽¹⁾

Test	Switch
Open Drain Disable LOW Enable LOW	7V
All Other Inputs	Open

Notes:

- C_L = Load Capacitance: includes jig and probe capacitance.
 R_T = Termination Resistance: should be equal to the Z_{OUT} of the Pulse Generator.

Enable and Disable Timing

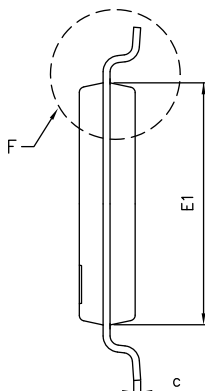
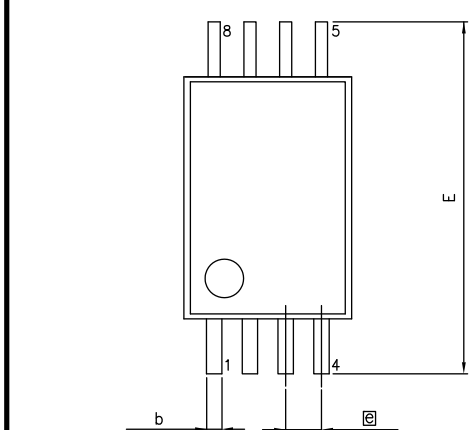


Notes:

- Input Control Enable = Low; Input Control Disable = High
- Pulse Generator for All Pulses: Rate ≤ 1.0 MHz; $Z_{OUT} \leq 50\Omega$; $t_F, t_R \leq 2.5$ ns

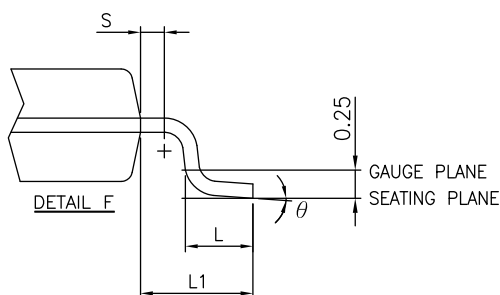
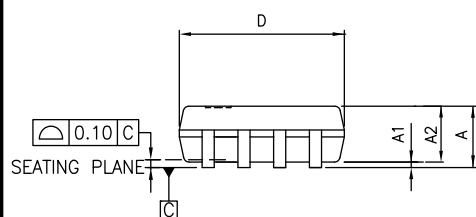
Part Marking

Top mark not available at this time. To obtain advance information regarding the top mark, please contact your local sales representative.

PI5C3305
Packaging Mechanical: 8-TSSOP (L)


SYMBOLS	MIN.	NOM.	MAX.
A	—	—	1.20
A1	0.05	—	0.15
A2	0.80	1.00	1.05
b	0.19	—	0.30
c	0.09	—	0.20
D	2.90	3.00	3.10
E	6.20	6.40	6.60
e	0.65 BSC		
E1	4.30	4.40	4.50
L	0.45	0.60	0.75
L1	1.00 REF		
S	0.20	—	—
θ°	0	—	8

UNIT : MM


NOTES:

1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. JEDEC MO-153F/AA
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

16-0062



DATE: 03/24/16

DESCRIPTION: 8-Pin, 173mil Wide TSSOP

PACKAGE CODE: L (L8)

DOCUMENT CONTROL #: PD-1308

REVISION: G

For latest package info.

 please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>
Ordering Information

Ordering Code	Package Code	Package Description
PI5C3305LEX	L	8-Pin, 173mil Wide (TSSOP)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

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