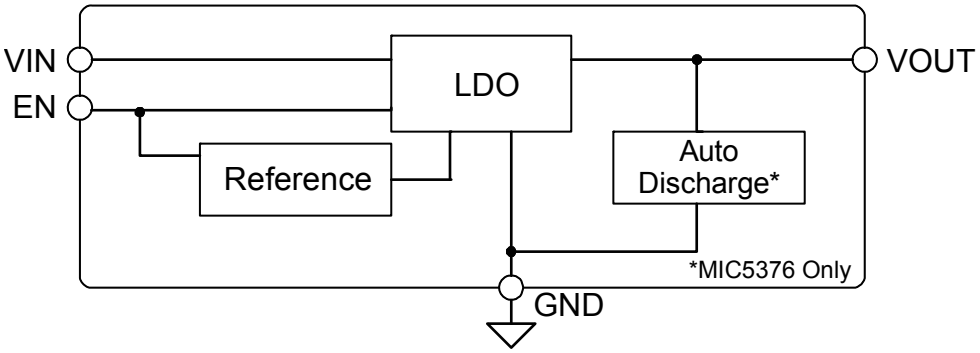
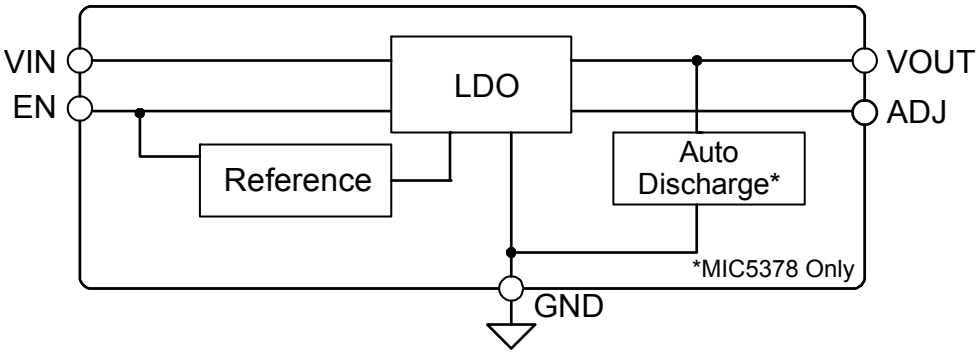
Block Diagram



MIC5375/6 Block Diagram



MIC5377/8 Block Diagram

Ordering Information

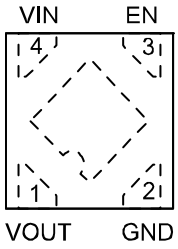
Part Number	Marking Code	Output Voltage	Temperature Range	Package	Lead Finish
MIC5375-2.8YMT	J7	2.8V	–40°C to +125°C	4-Pin 1mm x 1mm Thin MLF®	Pb-Free
MIC5375-2.8YC5	G7J	2.8V	–40°C to +125°C	5-Pin SC-70	Pb-Free
MIC5376-2.8YMT*	TT	2.8V	–40°C to +125°C	4-Pin 1mm x 1mm Thin MLF®	Pb-Free
MIC5376-2.8YC5*	2T8	2.8V	–40°C to +125°C	5-Pin SC-70	Pb-Free
MIC5377YMT	AH	ADJ	–40°C to +125°C	8-Pin 1.2mm x 1.2mm Thin MLF®	Pb-Free
MIC5377YC5	AHA	ADJ	–40°C to +125°C	5-Pin SC-70	Pb-Free
MIC5378YMT*	67A	ADJ	–40°C to +125°C	8-Pin 1.2mm x 1.2mm Thin MLF®	Pb-Free
MIC5378YC5*	A67	ADJ	–40°C to +125°C	5-Pin SC-70	Pb-Free

Notes:

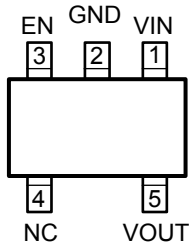
1. Other voltages available. Contact Micrel for details.
2. Under bar symbol (_) may not be to scale.
3. Thin MLF® ▲ = Pin 1 identifier.
4. Thin MLF® is a GREEN RoHS compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

* MIC5376/8 offers Auto-Discharge function.

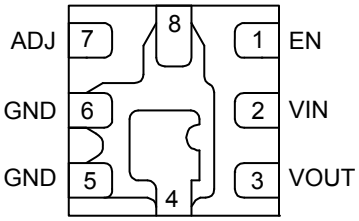
Pin Configuration



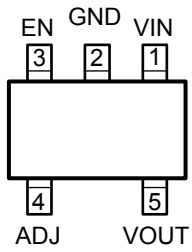
4-Pin 1mm x 1mm Thin MLF® (MT)
MIC5375/6 Fixed Output
(Top View)



5-Pin SC-70 (C5)
MIC5375/6 Fixed Output



8-Pin 1.2mm x 1.2mm Thin MLF® (MT)
MIC5377/8 Adjustable Output
(Bottom View)



5-Pin SC-70 (C5)
MIC5377/8 Adjustable Output

Pin Description

Pin No. MIC5375/6 TMLF 1x1	Pin No. MIC5375/6 SC-70-5	Pin No. MIC5377/8 TMLF 1.2x1.2	Pin No. MIC5377/8 SC-70-5	Pin Name	Pin Function
3	3	1	3	EN	Enable Input. Active High. High = on, low = off. Do not leave floating.
4	1	2	1	VIN	Supply Input.
1	5	3	5	VOUT	Output Voltage.
2	2	4,5,6,8	2	GND	Ground.
–	–	7	4	ADJ	Adjust Pin: Feedback input from external divider.
–	4	–	–	NC	No connection.
HS Pad		–	–	EPAD	Exposed Heatsink Pad connected to ground internally.

Absolute Maximum Ratings[1]

Supply Voltage (V_{IN}) ..... 0V to 6V
Enable Voltage (V_{EN}) ..... 0V to V_{IN}
Power Dissipation (P_D) ..... Internally Limited[3]
Lead Temperature (soldering, 5 sec) ..... 260°C
Junction Temperature (T_J) ..... –40°C to +125°C
Storage Temperature (T_s) ..... –65°C to +150°C
ESD Rating[4] 2kV

Operating Ratings[2]

Supply Voltage (V_{IN}) ..... 2.5V to 5.5V
Enable Voltage (V_{EN}) ..... 0V to V_{IN}
Junction Temperature (T_J) ..... –40°C to +125°C
Junction Thermal Resistance
1mm x 1mm Thin MLF-4 (θ_{JA}) ..... 250°C/W
1.2mm x 1.2mm Thin MLF-8(θ_{JA}) ..... 250°C/W
SC-70-5 (θ_{JA}) ..... 256.5°C/W

Electrical Characteristics[5]

$V_{IN} = V_{EN} = V_{OUT} + 1V$; $C_{IN} = C_{OUT} = 1\mu F$ for Vout ≥ 2.5V, $C_{IN} = C_{OUT} = 2.2\mu F$ for $V_{OUT} < 2.5V$; $I_{OUT} = 100\mu A$; $T_J = 25°C$, **bold** values indicate –40°C to +125°C, unless noted.

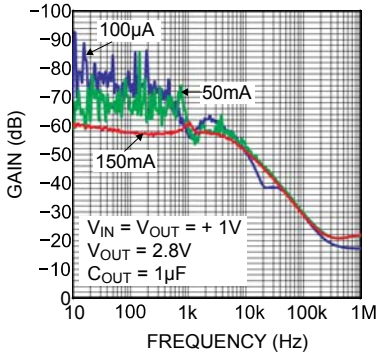
Parameter	Condition	Min	Typ	Max	Units
Output Voltage Accuracy	Variation from nominal V_{OUT}	–2.0		+2.0	%
	Variation from nominal V_{OUT}	**–3.0**		**+3.0**	%
Line Regulation	$V_{IN} = V_{OUT}$ +1V to 5.5V; I_{OUT} = 100µA		0.02	**0.3**	%
Load Regulation[6]	I_{OUT} = 100µA to 150mA		0.3	1.0	%
Dropout Voltage[7]	I_{OUT} = 50mA		45	**100**	mV
	I_{OUT} = 150mA		120	**200**	mV
Ground Pin Current[8]	I_{OUT} = 0mA		29	**45**	µA
Ground Pin Current in Shutdown	V_{EN} ≤ 0.2V		0.05	1	µA
Ripple Rejection	f = 1kHz; C_{OUT} = 1µF		60		dB
	f = 10kHz; C_{OUT} = 1µF		50		dB
Current Limit	V_{OUT} = 0V	200	370	550	mA
Output Voltage Noise	C_{OUT} = 1µF, 10Hz to 100kHz		200		μV_{RMS}
Auto-Discharge NFET Resistance	V_{EN} = 0V; V_{IN} = 3.6V		30		Ω
Reference Voltage (MIC5377/8)					
Reference Voltage Accuracy		**0.97**	1	**1.03**	V
Adjust Pin Input Current			0.01		µA
Enable Input					
Enable Input Voltage	Logic Low			**0.2**	V
	Logic High	**1.2**			V
Enable Input Current	V_{IL} ≤ 0.2V		0.01	**1**	µA
	V_{IH} ≥ 1.2V		0.01	**1**	µA
Turn-on Time	C_{OUT} = 1µF; I_{OUT} = 150mA		45	**100**	µs

Notes:

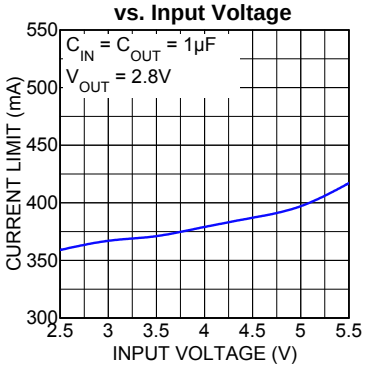
1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. The maximum allowable power dissipation of any T_A (ambient temperature) is $P_{D(max)} = (T_{J(max)} – T_A) / \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
4. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5kΩ in series with 100pF.
5. Specification for packaged product only.
6. Regulation is measured at constant junction temperature using low duty cycle pulse testing.
7. Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 2.5V, dropout voltage is the input-to-output differential with the minimum input voltage 2.5V.
8. Ground pin current is the regulator quiescent current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

Typical Characteristics

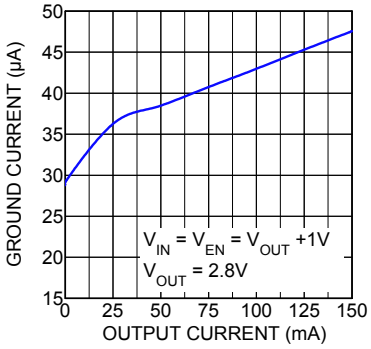
PSRR



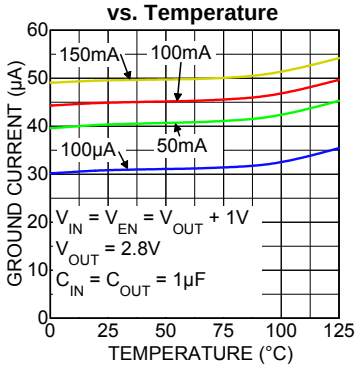
Current Limit vs. Input Voltage



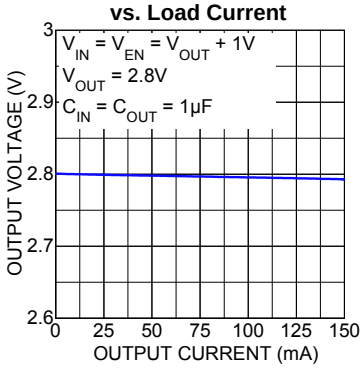
Ground Current vs. Output Current



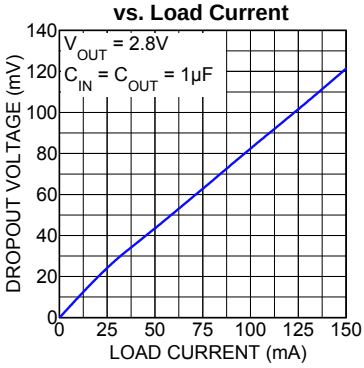
Ground Current vs. Temperature



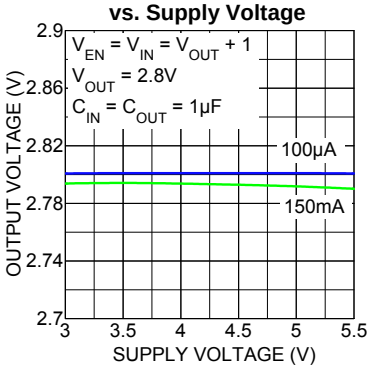
Output Voltage vs. Load Current



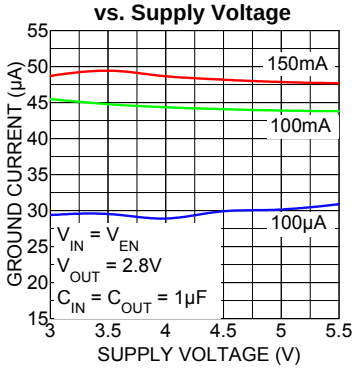
Dropout Voltage vs. Load Current



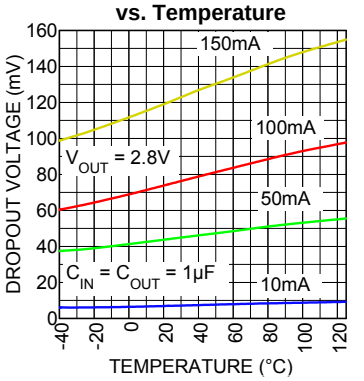
Output Voltage vs. Supply Voltage



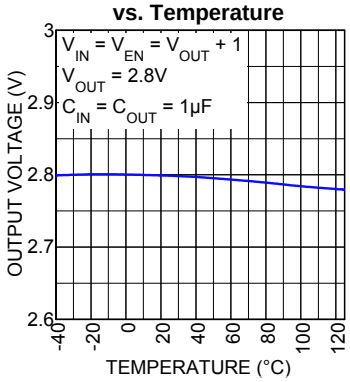
Ground Current vs. Supply Voltage



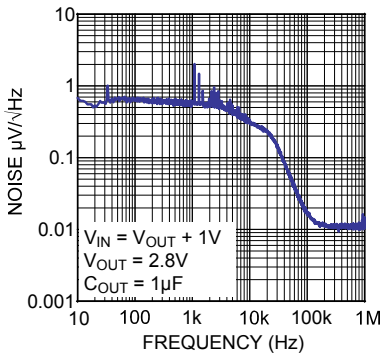
Dropout Voltage vs. Temperature



Output Voltage vs. Temperature

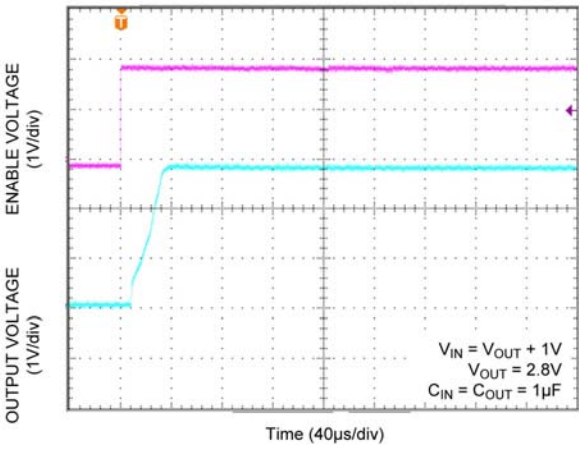


Output Noise Spectral Density

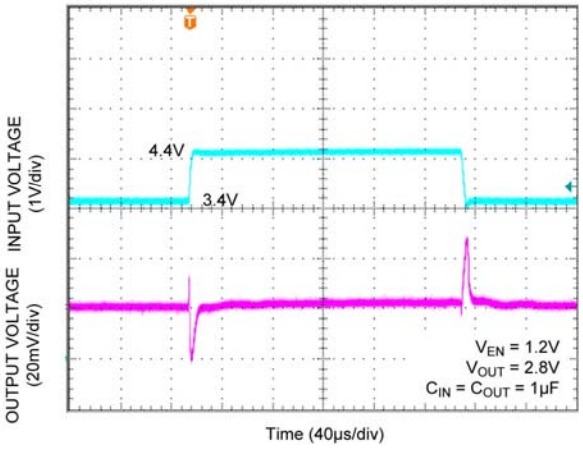


Functional Characteristics

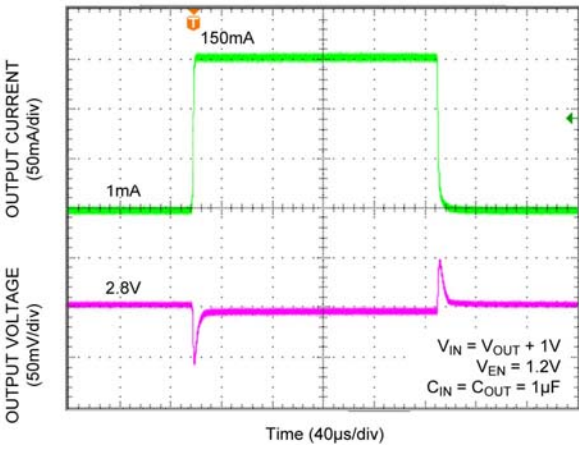
Enable Turn-On



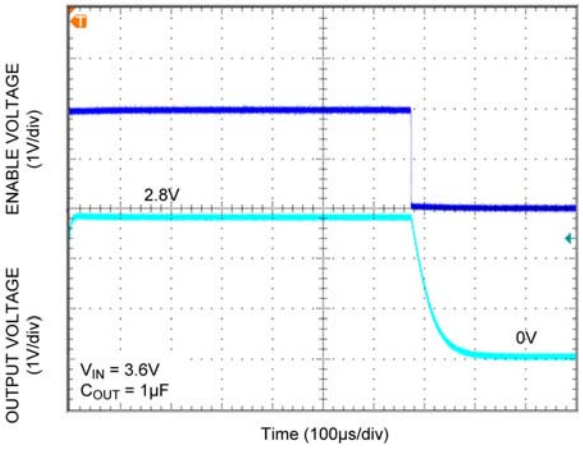
Line Transient 2.8V (3.4V to 4.4V)



Load Transient 2.8V (1 to 150mA)



MIC5376 Auto Discharge (No Load)



Application Information

MIC5375/6/7/8 is Low noise 150mA LDO. The MIC5376/8 includes an auto-discharge circuit that is switched on when the regulator is disabled through the enable pin. The MIC5375/6/7/8 regulator is protected from damage due to fault conditions, offering linear current limiting and thermal shutdown.

Input Capacitor

The MIC5375/6/7/8 is a high-performance, high bandwidth device. An input capacitor of 1µF is required from the input to ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small-valued NPO dielectric-type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

Output Capacitor

For output voltages ≥ 2.5V, the MIC5375/6/7/8 requires a minimum 1µF output capacitor. For output voltages below 2.5V a 2.2µF minimum output capacitor is required. The design is optimized for use with low-ESR ceramic chip capacitors. High ESR capacitors are not recommended because they may cause high frequency oscillation. The output capacitor can be increased, but performance does not improve significantly with larger capacitance.

X7R/X5R dielectric-type ceramic capacitors are recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

No-Load Stability

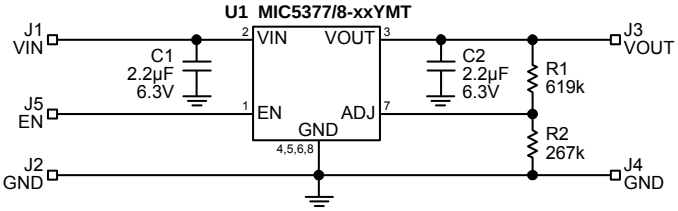
Unlike many other voltage regulators, the MIC5375/6/7/8 will remain stable and in regulation with no load. This is especially important in CMOS RAM keep-alive applications.

Enable/Shutdown

The MIC5375/6/7/8 is provided with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a “zero” off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

Adjustable Regulator Design

The MIC5377/8 adjustable version allows setting the output voltage down to 1V with the use of two external feedback resistors.



$$V_{OUT} = V_{REF}\left(1 + \frac{R_1}{R_2}\right)$$

Thermal Considerations

The MIC5375/6/7/8 is designed to provide 150mA of continuous current in a very small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. For example if the input voltage is 3.6V, the output voltage is 2.8V, and the output current = 150mA. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} – V_{OUT1})\, I_{OUT} + V_{IN}\, I_{GND}$$

Because this device is CMOS and the ground current is typically <100µA over the load range, the power dissipation contributed by the ground current is < 1% and can be ignored for this calculation.

$$P_D = (3.6V – 2.8V) \times 150mA$$

$$P_D = 0.12W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(max)} = \left(\frac{T_{J(max)} - T_A}{\theta_{JA}}\right)$$

$T_{J(max)}$ = 125°C, the maximum junction temperature of the die, θ_{JA} thermal resistance = 250°C/W for the YMT package and 256.5°C/W for the SC-70-5 package.

Substituting P_D for $P_{D(max)}$ and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is 250°C/W.

The maximum power dissipation must not be exceeded for proper operation.

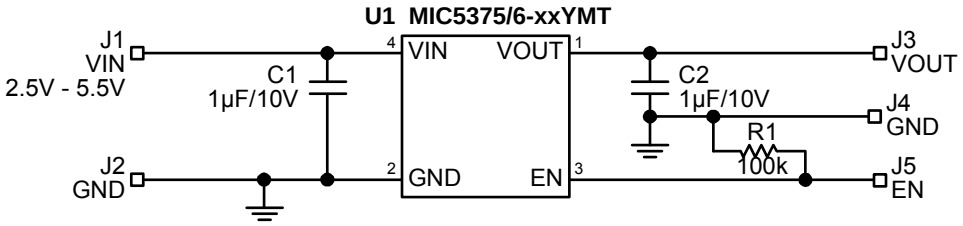
For example, when operating the MIC5375-2.8YMT at an input voltage of 3.6V and 150mA load with a minimum footprint layout, the maximum ambient operating temperature T_A can be determined as follows:

$$0.12W = (125°C – T_A)/(250°C/W)$$

$$T_A = 95°C$$

Therefore the maximum ambient operating temperature of 95°C is allowed in a 1mm x 1mm TMLF® package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the “Regulator Thermals” section of *Micrel’s Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

http://www.micrel.com/_PDF/other/LDOBk_ds.pdf

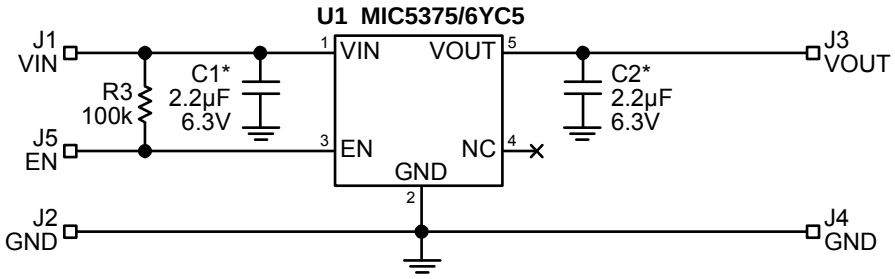


Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1, C2	GRM155R61A105KE15D	Murata[1]	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
	C1005X5R1A105K	TDK[2]		
	0402ZD105MAT	AVX[3]		
R1	CRCW0603100KFKEA	Vishay[4]	Resistor, 100k, 1%, 1/16W, Size 0603	1
U1	**MIC5375/6-xxYMT**	**Micrel, Inc.[5]**	**High Performance 150mA LDO, 4 Pin 1mm x 1mm Thin MLF®**	**1**

Notes:

1. Murata: www.murata.com
2. TDK: www.tdk.com
3. AVX: www.avx.com
4. Vishay: www.vishay.com
5. **Micrel, Inc.: www.micrel.com**



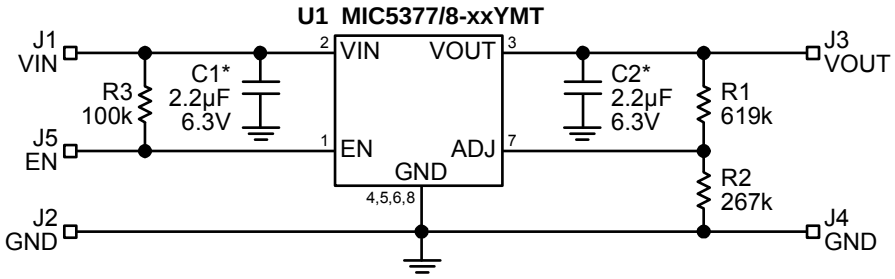
Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1, C2	JMK105BJ225MV-F	Taiyo Yuden(1)	Capacitor, 2.2µF, 6.3V, X5R, Size 0402	2
	GRM155R60G225ME15D	Murata(2)		
	CV05X5R225K10AB	AVX/Kyocera(3)		
C1, C2	GRM155R61A105KE15D	Murata(1)	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
	C1005X5R1A105KT	TDK(2)		
	CV05X5R105K10AH	AVX/Kyocera(3)		
R1	CRCW0402619KFKEA	Vishay(4)	Resistor, 619kΩ, 1%, 1/16W, Size 0402	1
R2	CRCW04022673KFKEA	Vishay(4)	Resistor, 267kΩ, 1%, 1/16W, Size 0402	1
R3	CRCW04021003KFKEA	Vishay(4)	Resistor, 100kΩ, 1%, 1/16W, Size 0402	1
U1	**MIC5375/6YC5**	**Micrel, Inc.(5)**	**High Performance 150mA LDO, 5-Pin SC-70**	**1**

Notes:

1. Taiyo Yuden:
2. Murata: www.murata.com
3. AVX/Kyocera: www.avx.com
4. Vishay: www.vishay.com
5. **Micrel, Inc.: www.micrel.com**

* $C_{IN} = C_{OUT} = 1\mu F$ for Vout ≥ 2.5V, $C_{IN} = C_{OUT} = 2.2\mu F$ for $V_{OUT} < 2.5V$



Bill of Materials

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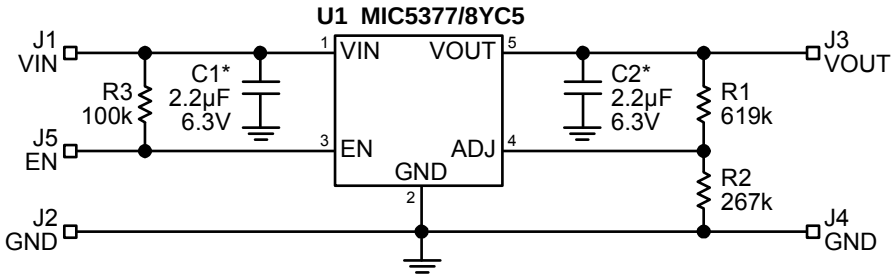
	GRM155R60G225ME15D	Murata[2]		
	CV05X5R225K10AB	AVX/Kyocera[3]		
C1, C2	GRM155R61A105KE15D	Murata[1]	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
	C1005X5R1A105KT	TDK[2]		
	CV05X5R105K10AH	AVX/Kyocera[3]		
R1	CRCW0402619KFKEA	Vishay[4]	Resistor, 619kΩ, 1%, 1/16W, Size 0402	1
R2	CRCW04022673KFKEA	Vishay[4]	Resistor, 267kΩ, 1%, 1/16W, Size 0402	1
R3	CRCW04021003KFKEA	Vishay[4]	Resistor, 100kΩ, 1%, 1/16W, Size 0402	1
U1	**MIC5377/8-xxYMT**	**Micrel, Inc.[5]**	**High Performance 150mA LDO, 8 Pin 1.2mm x 1.2mm Thin MLF®**	**1**

Notes:

1. Taiyo Yuden:
2. Murata: www.murata.com
3. AVX/Kyocera: www.avx.com
4. Vishay: www.vishay.com
5. **Micrel, Inc.: www.micrel.com**

* $C_{IN} = C_{OUT} = 1\mu F$ for Vout ≥ 2.5V, $C_{IN} = C_{OUT} = 2.2\mu F$ for $V_{OUT} < 2.5V$



Bill of Materials

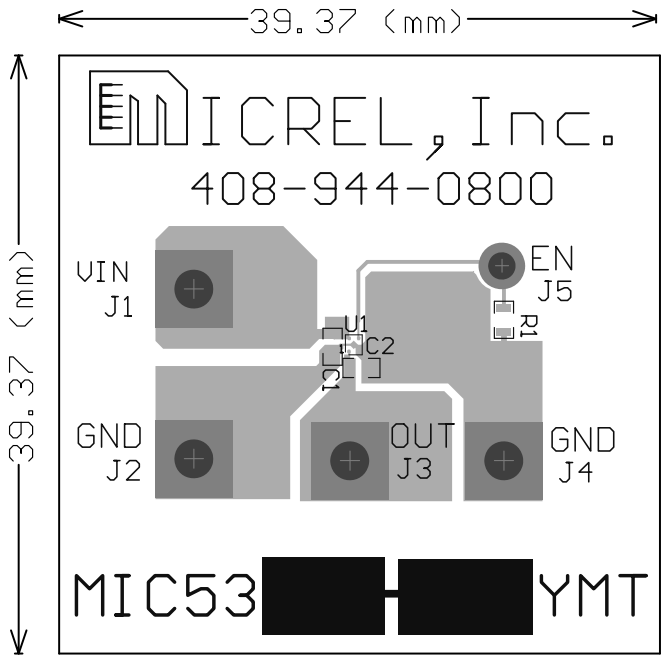
Item	Part Number	Manufacturer	Description	Qty.
C1, C2	JMK105BJ225MV-F	Taiyo Yuden(1)	Capacitor, 2.2µF, 6.3V, X5R, Size 0402	2
	GRM155R60G225ME15D	Murata(2)		
	CV05X5R225K10AB	AVX/Kyocera(3)		
C1, C2	GRM155R61A105KE15D	Murata(1)	Capacitor, 1µF Ceramic, 10V, X5R, Size 0402	2
	C1005X5R1A105KT	TDK(2)		
	CV05X5R105K10AH	AVX/Kyocera(3)		
R1	CRCW0402619KFKEA	Vishay(4)	Resistor, 619kΩ, 1%, 1/16W, Size 0402	1
R2	CRCW04022673KFKEA	Vishay(4)	Resistor, 267kΩ, 1%, 1/16W, Size 0402	1
R3	CRCW04021003KFKEA	Vishay(4)	Resistor, 100kΩ, 1%, 1/16W, Size 0402	1
U1	**MIC5377/8YC5**	**Micrel, Inc.(5)**	**High Performance 150mA LDO, 5-Pin SC-70**	**1**

Notes:

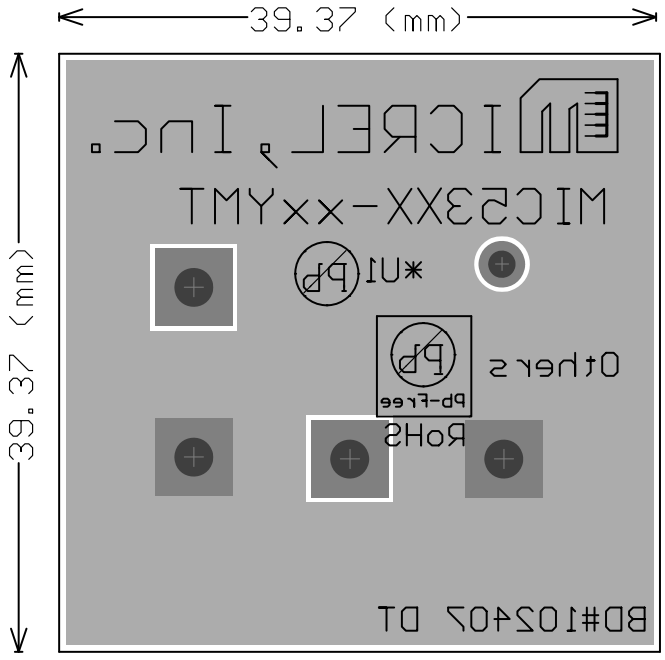
1. Taiyo Yuden:
2. Murata: www.murata.com
3. AVX/Kyocera: www.avx.com
4. Vishay: www.vishay.com
5. **Micrel, Inc.: www.micrel.com**

* $C_{IN} = C_{OUT} = 1\mu F$ for Vout ≥ 2.5V, $C_{IN} = C_{OUT} = 2.2\mu F$ for $V_{OUT} < 2.5V$

PCB Layout Recommendations (1mm x 1mm 4-Pin Thin MLF®) Fixed

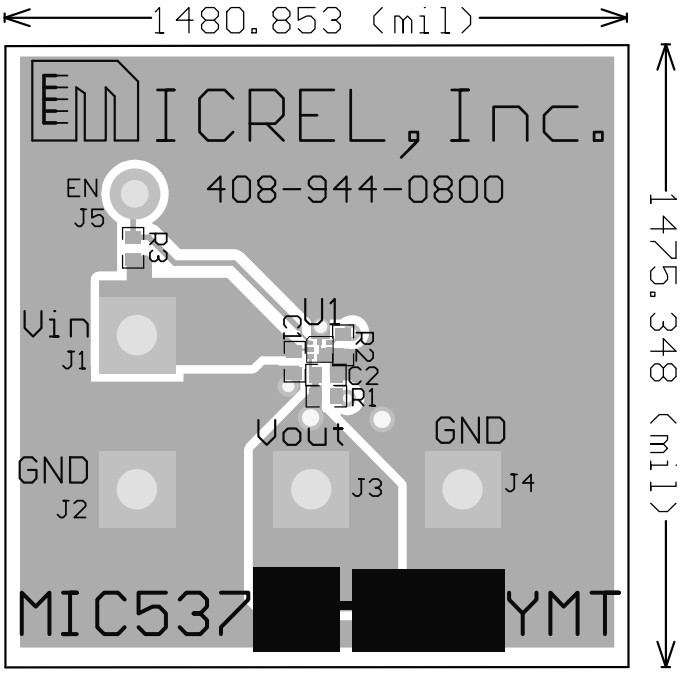


Top Layer



Bottom Layer

PCB Layout Recommendations (1.2mm x 1.2mm 8-Pin Thin MLF®) Adjustable



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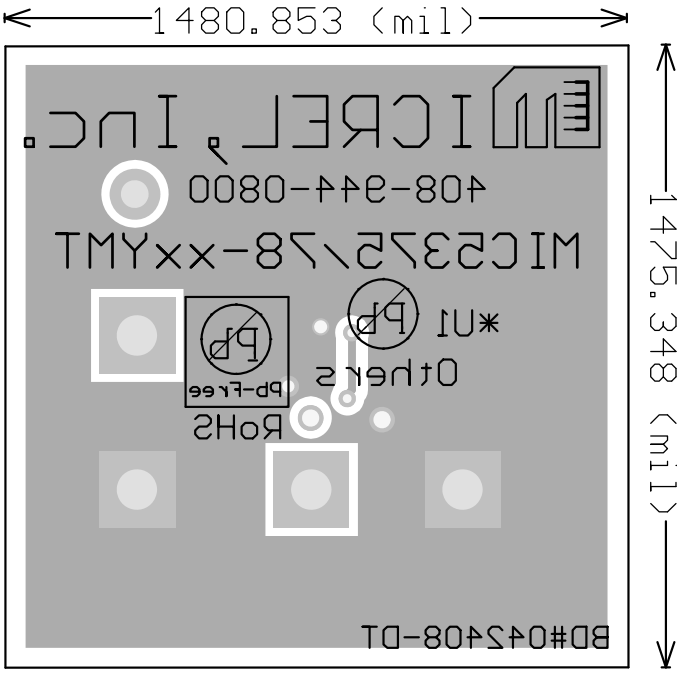
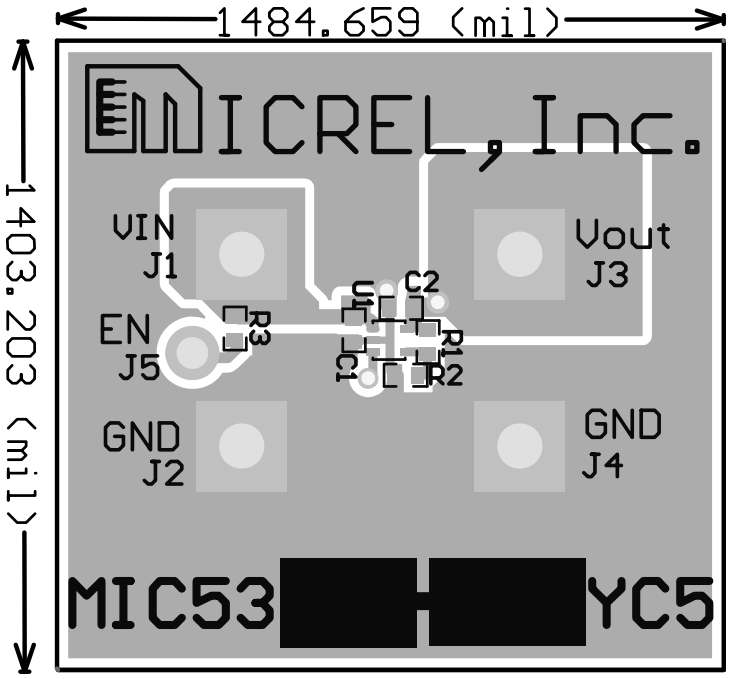
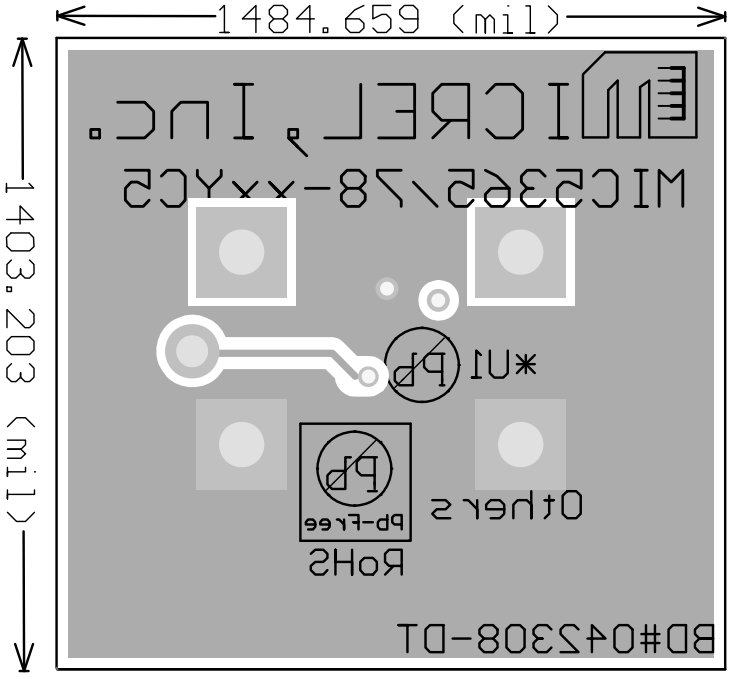


Bottom Layer

PCB Layout Recommendations (SC-70-5)

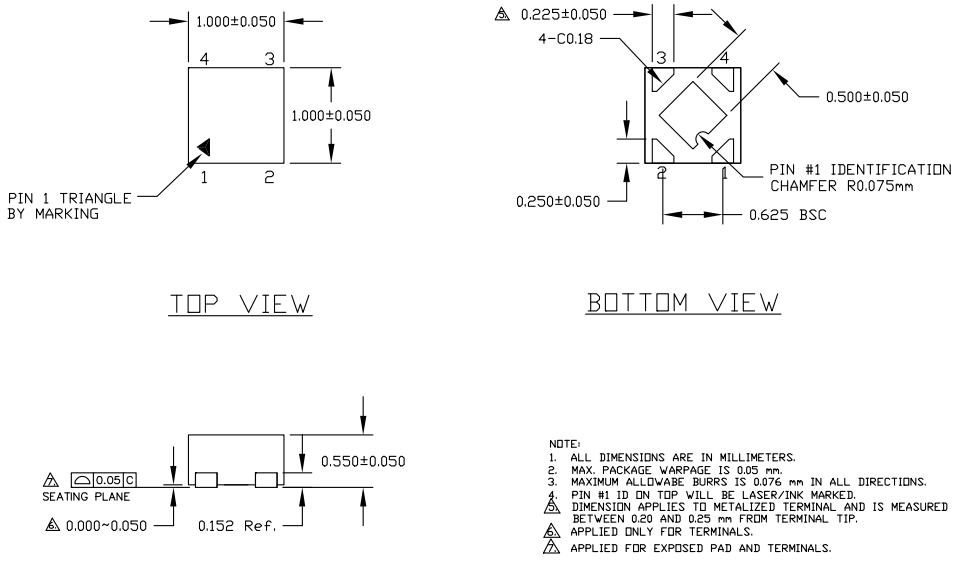


Top Layer

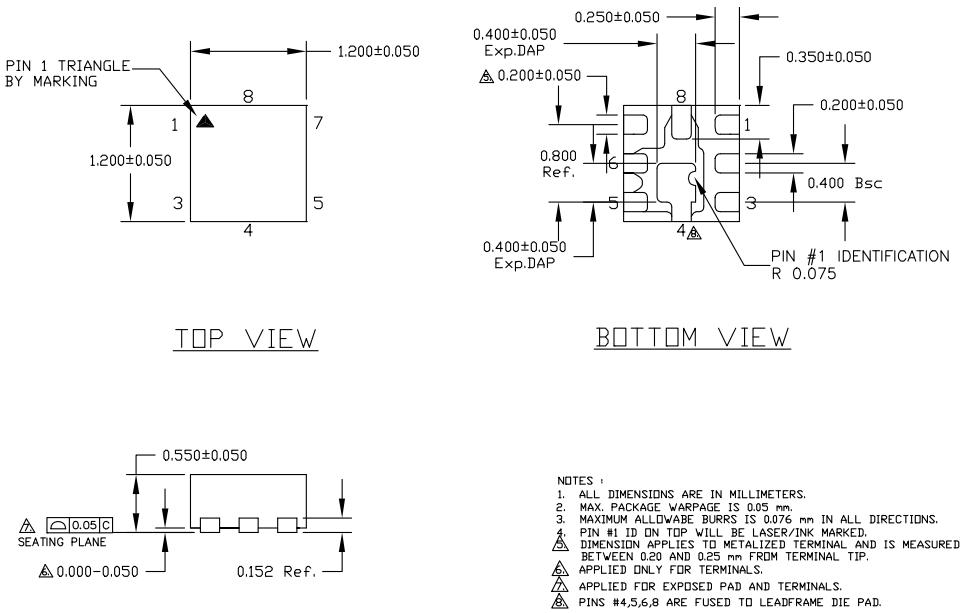


Bottom Layer

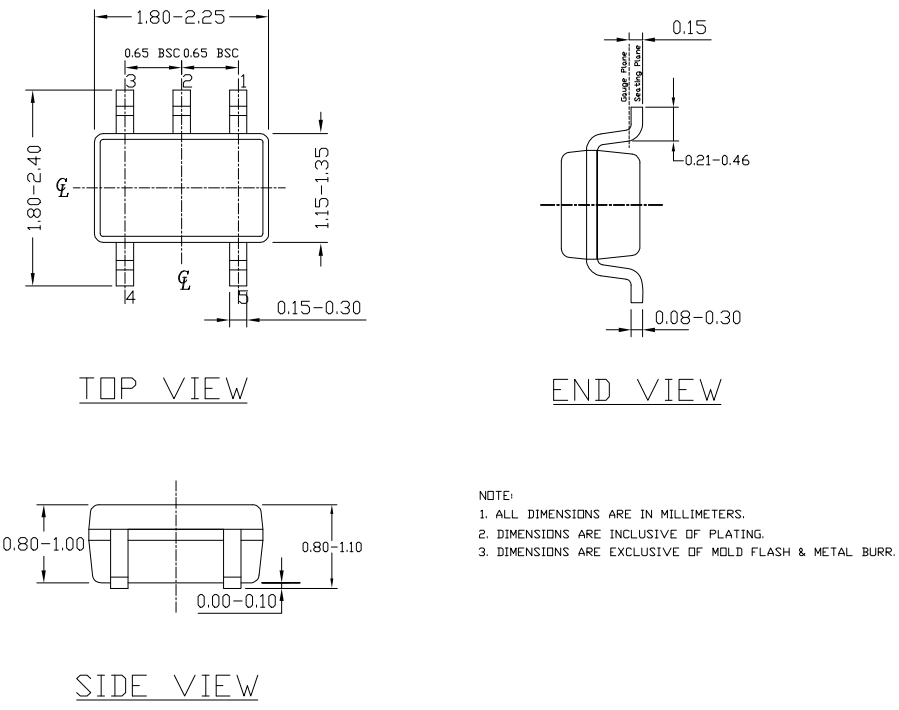
Package Information



4-Pin 1mm x 1mm Thin MLF® (MT)



8-Pin 1.2mm x 1.2mm Thin MLF® (MT)



5-Pin SC-70 (C5)

MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USA
TEL +1 (408) 944-0800 FAX +1 (408) 474-1000 WEB http://www.micrel.com