

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)  
 Input Voltages (IN<sub>-</sub>) (except -5V) .....-0.3V to +6V  
 $\overline{\text{RESET}}$  .....-0.3V to +6V  
 Input Voltage (-5V Input) .....-6V to +0.3V  
 Continuous  $\overline{\text{RESET}}$  Current .....20mA  
 Continuous Power Dissipation ( $T_A = +70^\circ\text{C}$ )  
 6-pin SOT23 (derate 8.7mW/ $^\circ\text{C}$  above  $+70^\circ\text{C}$ ) .....695.7mW

Operating Temperature Range .....-40 $^\circ\text{C}$  to +85 $^\circ\text{C}$   
 Storage Temperature Range .....-65 $^\circ\text{C}$  to +150 $^\circ\text{C}$   
 Junction Temperature .....+150 $^\circ\text{C}$   
 Lead Temperature (soldering, 10s) .....+300 $^\circ\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $V_{IN2} = +1\text{V}$  to  $+5.5\text{V}$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted. Typical values are at  $V_{IN2} = +3.0\text{V}$  to  $+3.3\text{V}$ ,  $T_A = +25^\circ\text{C}$ , unless otherwise noted.) (Note 1)

| PARAMETER                                    | SYMBOL     | CONDITIONS                                                                                                                                                          | MIN                    | TYP   | MAX   | UNITS                 |
|----------------------------------------------|------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------|-------|-------|-----------------------|
| Supply Voltage Range<br>(Notes 2, 3)         | $V_{IN2}$  |                                                                                                                                                                     | 1.0                    |       | 5.5   | V                     |
| Input Current                                | $I_{IN-}$  | $V_{IN-}$ = input threshold voltage (input threshold voltage of 1.8V, 2.5V, 5V)                                                                                     |                        | 25    | 40    | $\mu\text{A}$         |
|                                              |            | $V_{IN2}$ = input threshold voltage (input threshold voltage of 3.0V, 3.3V) (Note 4)                                                                                |                        | 55    | 115   |                       |
|                                              |            | $V_{IN-} = 0$ to $V_{IN2}$<br>(input threshold voltage = $+1.23\text{V}$ )                                                                                          | -0.1                   |       | +0.1  |                       |
|                                              |            | $V_{IN1} = 1.5\text{V}$ (MAX6339 K/L only)                                                                                                                          |                        | 0.4   | 1.5   |                       |
|                                              |            | $V_{IN-} = -5\text{V}$ (input threshold voltage = $-5\text{V}$ )                                                                                                    |                        | -15   | -20   |                       |
| Reset<br>Threshold Voltage                   | $V_{TH}$   | $V_{IN-}$<br>decreasing                                                                                                                                             | +5V (-5%) threshold    | 4.50  | 4.63  | V                     |
|                                              |            |                                                                                                                                                                     | +5V (-10%) threshold   | 4.25  | 4.38  |                       |
|                                              |            |                                                                                                                                                                     | +3.3V (-5%) threshold  | 3.00  | 3.08  |                       |
|                                              |            |                                                                                                                                                                     | +3.3V (-10%) threshold | 2.85  | 2.93  |                       |
|                                              |            |                                                                                                                                                                     | +3.0V (-5%) threshold  | 2.70  | 2.78  |                       |
|                                              |            |                                                                                                                                                                     | +3.0V (-10%) threshold | 2.55  | 2.63  |                       |
|                                              |            |                                                                                                                                                                     | +2.5V (-10%) threshold | 2.13  | 2.19  |                       |
|                                              |            |                                                                                                                                                                     | +1.8V (-10%) threshold | 1.53  | 1.58  |                       |
|                                              |            | $V_{IN-}$<br>increasing                                                                                                                                             | -5V (+5%) threshold    | -4.75 | -4.63 |                       |
|                                              |            |                                                                                                                                                                     | -5V (+10%) threshold   | -4.50 | -4.38 |                       |
| Adjustable Threshold                         | $V_{TH}$   | $V_{IN}$ decreasing                                                                                                                                                 | 1.20                   | 1.23  | 1.26  | V                     |
| Threshold Voltage<br>Temperature Coefficient |            |                                                                                                                                                                     |                        | 60    |       | ppm/ $^\circ\text{C}$ |
| Threshold Hysteresis                         | $V_{HYST}$ |                                                                                                                                                                     |                        | 0.3   |       | %                     |
| Reset Delay                                  | $t_{RD}$   | $V_{IN} = V_{TH}$ to ( $V_{TH} - 50\text{mV}$ ) (all inputs except $-5\text{V}$ )<br>or $V_{IN-} = V_{TH}$ to ( $V_{TH} + 50\text{mV}$ ) ( $-5\text{V}$ input only) |                        | 20    |       | $\mu\text{s}$         |
| Reset Active Timeout<br>Period               | $t_{RP}$   |                                                                                                                                                                     | 140                    | 200   | 280   | ms                    |

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## ELECTRICAL CHARACTERISTICS (continued)

( $V_{IN2} = +1V$  to  $+5.5V$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted. Typical values are at  $V_{IN2} = +3.0V$  to  $+3.3V$ ,  $T_A = +25^\circ\text{C}$ , unless otherwise noted.) (Note 1)

| PARAMETER                  | SYMBOL   | CONDITIONS                                                                    | MIN                  | TYP | MAX | UNITS   |
|----------------------------|----------|-------------------------------------------------------------------------------|----------------------|-----|-----|---------|
| Output Low Voltage         | $V_{OL}$ | $V_{IN2} = 5V$ , $I_{SINK} = 2mA$                                             |                      |     | 0.4 | V       |
|                            |          | $V_{IN2} = 2.5V$ , $I_{SINK} = 1.2mA$                                         |                      |     | 0.4 |         |
|                            |          | $V_{IN1} = V_{IN2} = 1V$ , $I_{SINK} = 50\mu A$                               |                      |     | 0.4 |         |
| Output High Voltage        | $V_{OH}$ | $V_{IN2} > 2.55V$ , $I_{SOURCE} = 6\mu A$<br>( $\overline{RESET}$ unasserted) | $0.8 \times V_{IN2}$ |     |     | V       |
| Output High Source Current | $I_{OH}$ | $V_{IN2} > 2.55V$ ( $\overline{RESET}$ unasserted)                            | 6                    | 10  |     | $\mu A$ |

**Note 1:** 100% production tested at  $T_A = +25^\circ\text{C}$ . Limits over temperature guaranteed by design.

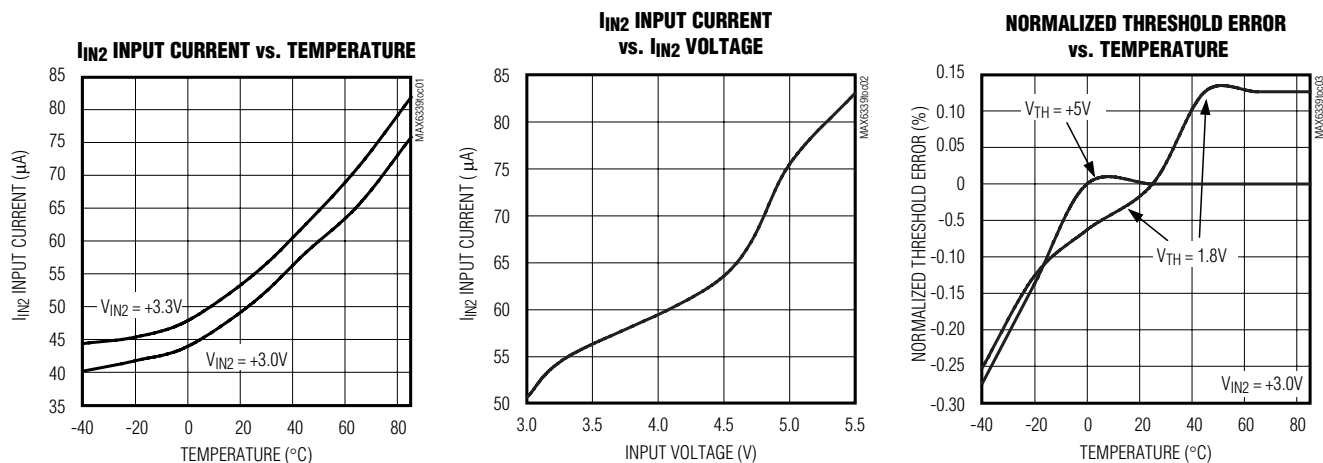
**Note 2:** The device is powered from input  $IN2$ .

**Note 3:** The  $\overline{RESET}$  output is guaranteed to be in the correct state for  $IN1$  or  $IN2$  down to  $1V$ .

**Note 4:** Monitored voltage ( $+3.3V$ ,  $+3.0V$ ) is also the device power supply. Supply current splits as follows:  $25\mu A$  for the resistor-divider (for the monitored voltage) and  $30\mu A$  for other circuits.

## Typical Operating Characteristics

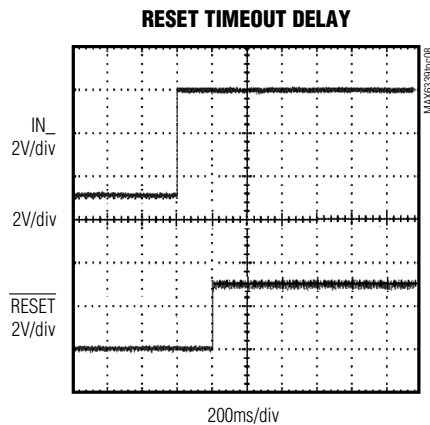
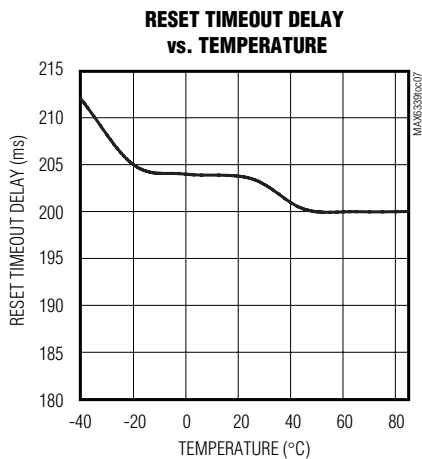
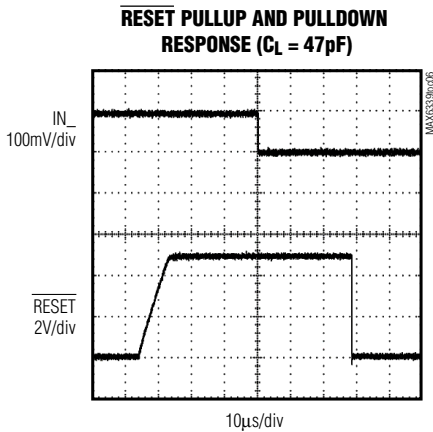
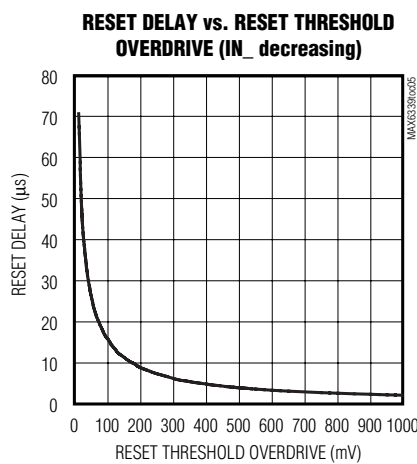
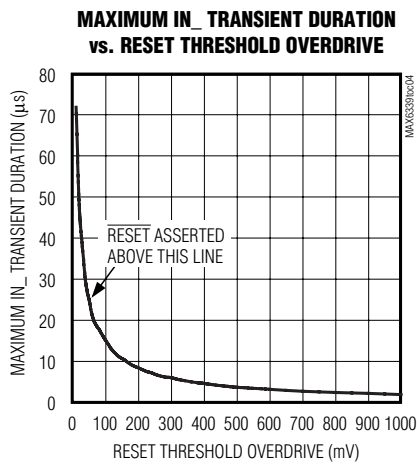
( $V_{IN2} = +3.0V$ ,  $T_A = +25^\circ\text{C}$ )



# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## Typical Operating Characteristics (continued)

( $V_{IN2} = +3.0V$ ,  $T_A = +25^\circ C$ )



## Pin Description

| PIN | NAME  | FUNCTION                                                                                                                                                                                                                                                                         |
|-----|-------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1   | IN1   | Input Voltage 1. See the <i>Selector Guide</i> for monitored voltages.                                                                                                                                                                                                           |
| 2   | IN2   | Input Voltage 2. See the <i>Selector Guide</i> for monitored voltages. IN2 is the power-supply input for the device.                                                                                                                                                             |
| 3   | IN3   | Input Voltage 3. See the <i>Selector Guide</i> for monitored voltages.                                                                                                                                                                                                           |
| 4   | IN4   | Input Voltage 4. See the <i>Selector Guide</i> for monitored voltages.                                                                                                                                                                                                           |
| 5   | GND   | Ground                                                                                                                                                                                                                                                                           |
| 6   | RESET | Reset Output, Active Low. RESET goes low when any input goes below its specified threshold. After all inputs are above their threshold voltage, RESET remains low for at least 140ms (minimum) before going high. RESET output is open drain with a weak internal pullup to IN2. |

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## Detailed Description

The MAX6339 is a very small, low-power, quad voltage  $\mu$ P supervisory circuit designed to maintain system integrity in multi-supply systems (Figure 1). The device offers several internally trimmed undervoltage threshold options that minimize or eliminate the need for external components. Preset voltage monitoring options for +5.0V, +3.3V, +3.0V, +2.5V, +1.8V, and -5.0V make it ideal for telecommunications, desktop and notebook computers, high-end printers, data storage equipment, and networking equipment applications.

The quad monitor/reset includes an accurate bandgap reference, four precision comparators, and a series of internal trimmed resistor-divider networks to set the factory-fixed reset threshold options. The resistor networks scale the specified  $IN\_$  reset voltages to match the internal bandgap reference/comparator voltage. User-adjustable threshold options bypass the internal resistor networks and connect directly to one of the comparator inputs (an external resistor-divider network is required for threshold matching). All threshold voltage options, fixed and adjustable, are indicated through a single-letter code in the product number (see the *Selector Guide*).

Each of the internal comparators has a typical hysteresis of 0.3% with respect to its reset threshold. This built-in hysteresis improves the monitor's immunity to ambient noise without significantly reducing threshold accuracy when an input sits at its specified reset voltage. The MAX6339 is also designed to ignore short  $IN\_$  transients. See the *Typical Operating Characteristics* for a glitch immunity graph.

## Applications Information

### Reset Output

The MAX6339  $\overline{RESET}$  output is asserted low when any of the monitored  $IN\_$  voltages drop below its specified reset threshold (or above for -5V option) and remain low for the reset timeout period (140ms minimum) after all inputs exceed their thresholds (Figure 2). The output is open drain with a weak internal pullup to the monitored  $IN2$  supply (10 $\mu$ A typ). For many applications no external pullup resistor is required to interface with other logic devices. An external pullup resistor to any voltage from 0 to +5.5V can overdrive the internal pullup if interfacing to different logic supply voltages (Figure 3). Internal circuitry prevents reverse current flow from the external pullup voltage to  $IN2$ .

The MAX6339 is normally powered from the monitored  $IN2$  supply when all input voltages are above their specified thresholds. When any supply drops below its

threshold, the reset output is asserted and guaranteed to remain low while either  $IN1$  or  $IN2$  is above +1.0V.

### User-Adjustable Thresholds

The MAX6339 offers several monitor options with user-adjustable reset thresholds. The threshold voltage at each adjustable  $IN\_$  input is typically 1.23V. To monitor a voltage > 1.23V, connect a resistor-divider network to the circuit as shown in Figure 4.

$$V_{INTH} = 1.23V \times (R1 + R2) / R2$$

or, solved in terms of  $R1$ :

$$R1 = R2 \left( (V_{INTH} / 1.23V) - 1 \right)$$

Because the MAX6339 has a guaranteed input current of  $\pm 0.1\mu$ A on its adjustable inputs, resistor values up to 100k $\Omega$  can be used for  $R2$  with < 1% error.

### Unused Inputs

If some monitor inputs are to be unused, they must be tied to a supply voltage greater in magnitude than their specified threshold voltages. For unused  $IN3$  or  $IN4$  options with positive thresholds (fixed or adjustable), the inputs can be connected directly to the  $IN2$  supply. For unused  $IN4$  options with negative thresholds, the input must be tied to a more negative supply. The  $IN2$  input must always be used for normal operation (device power-supply pin). Unused pins cannot be connected to ground or allowed to float.

### Negative Voltage Monitoring Beyond -5V

The MAX6339 is offered with options to monitor -5V supplies with internally fixed thresholds. To monitor supplies more negative than -5V, a low-impedance resistor-divider network can be used external to the MAX6339 as shown in Figure 5. The current through the external resistor-divider should be greater than the input current for the -5V monitor options. For an input monitor current error of < 1%, the resistor-divider current should  $\geq 2$ mA (for  $I_{IN4} = 20\mu$ A max). Set  $R2 = 2.5k\Omega$ . Calculate  $R1$  based on the desired  $V_{IN\_}$  reset threshold voltage, using the following formula:

$$R1 = R2 \times [(V_{INTH} / V_{TH}) - 1]$$

where  $R2 \leq 2.49k\Omega$ ,  $V_{INTH}$  = desired threshold voltage and  $V_{TH}$  is the internal threshold voltage.

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

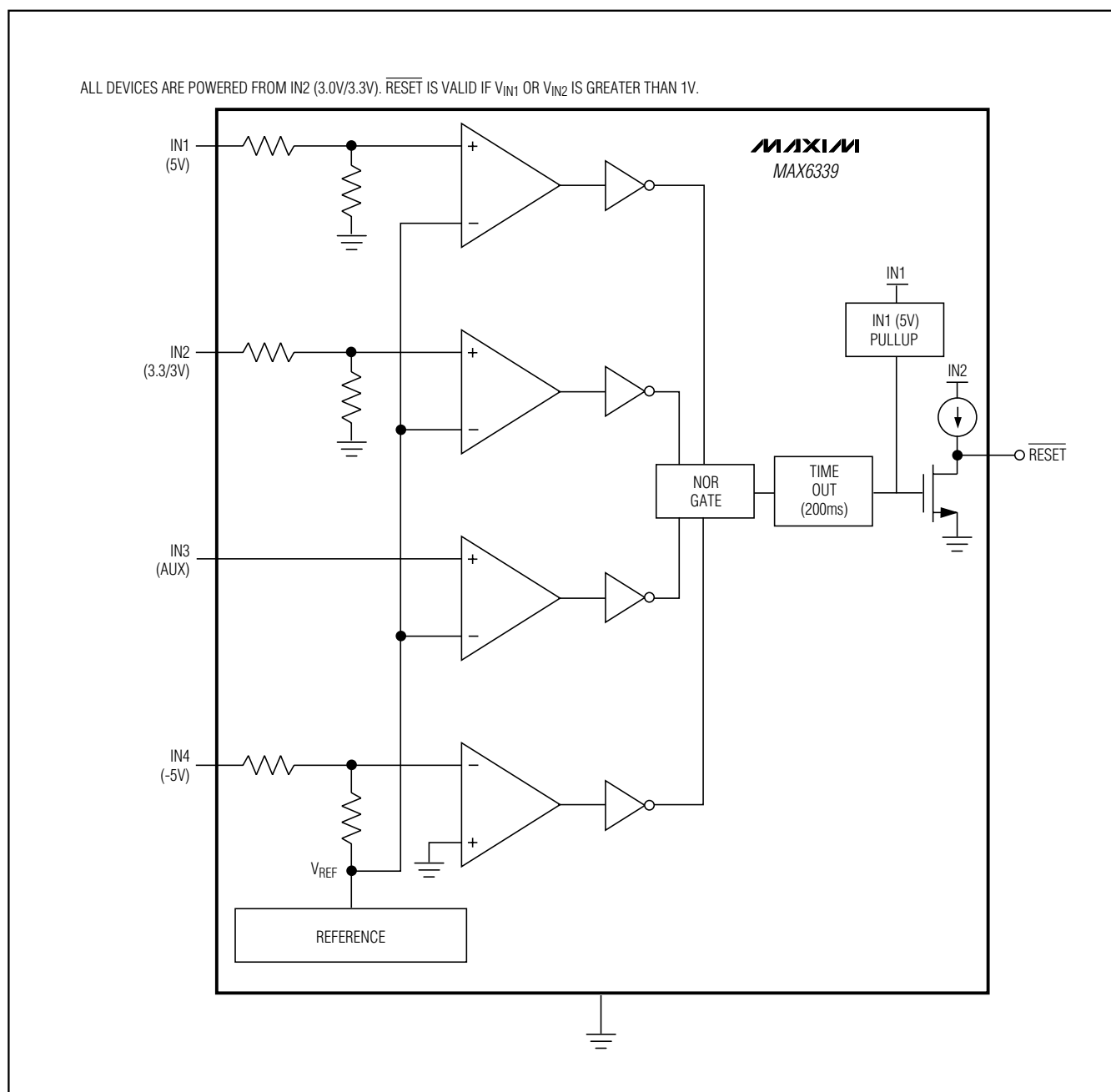


Figure 1. Functional Diagram

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

MAX6339

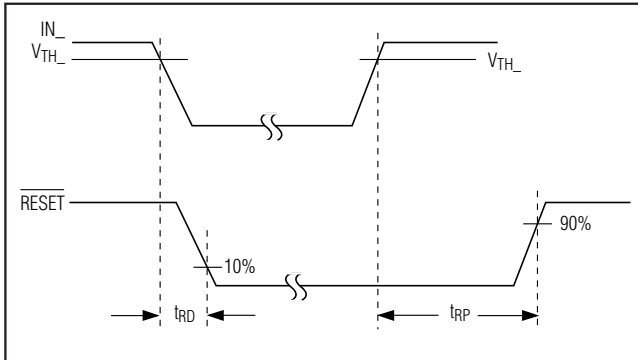


Figure 2.  $\overline{\text{RESET}}$  Output Timing Diagram

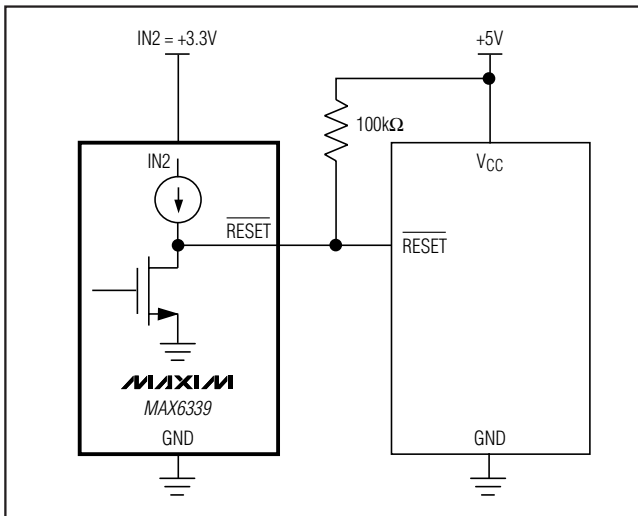


Figure 3. Interfacing to Different Logic Supply Voltage

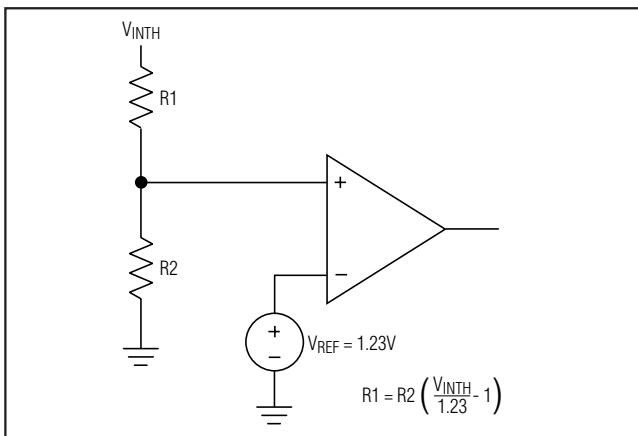


Figure 4. Setting the Auxiliary Monitor

For  $-V_{IN} = -12\text{V}$  nominal,  $V_{INTH} = -11.1\text{V}$ ,  $V_{TH} = -4.63\text{V}$ , and  $R2 = 2.49\text{k}\Omega$ ,

$$R1 = 2.49\text{k}\Omega \times [(-11.1 / -4.63) - 1]$$

$$R1 = 3.48\text{k}\Omega$$

## Power-Supply Bypassing and Grounding

The MAX6339 is normally powered from the monitored IN2 supply input. All monitor inputs are immune to short supply transients. If higher immunity is desired in noisy applications, connect  $0.1\mu\text{F}$  bypass capacitors from the IN2 input to ground. Additionally, capacitance can be added to IN1, IN3, and IN4 to increase their noise immunity.

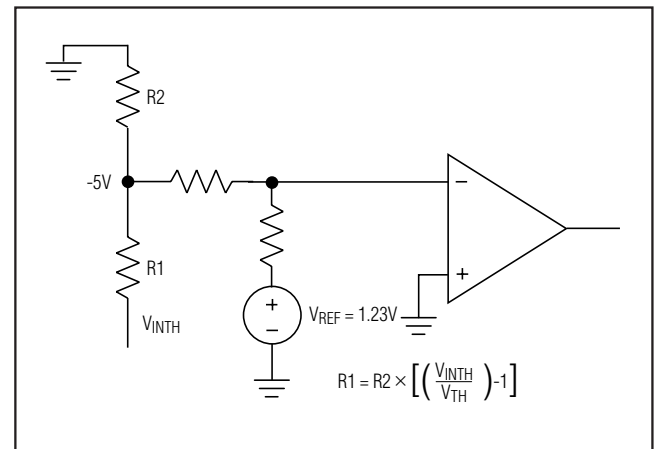
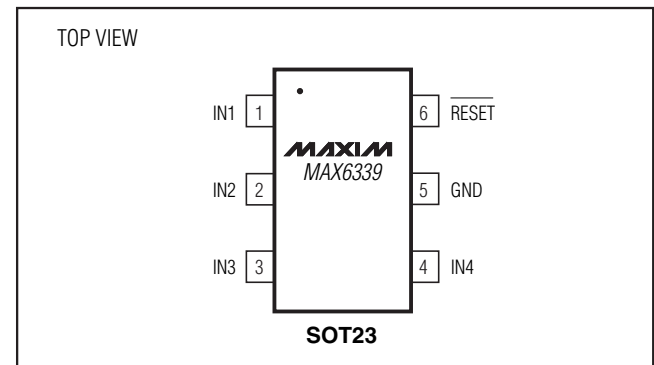


Figure 5. Negative Voltage Monitoring Beyond  $-5\text{V}$

## Pin Configuration



## Chip Information

TRANSISTOR COUNT: 896

PROCESS: BiCMOS

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## Selector Guide

| PART       | NOMINAL INPUT VOLTAGE |            |            |            |                            |             |
|------------|-----------------------|------------|------------|------------|----------------------------|-------------|
|            | IN1<br>(V)            | IN2<br>(V) | IN3<br>(V) | IN4<br>(V) | SUPPLY<br>TOLERANCE<br>(%) | TOP<br>MARK |
| MAX6339AUT | 5                     | 3.3        | 2.5        | Adj*       | 10                         | AAJU        |
| MAX6339BUT | 5                     | 3.3        | 2.5†       | Adj*       | 5                          | AAJV        |
| MAX6339CUT | 5                     | 3.3        | 1.8        | Adj*       | 10                         | AAJW        |
| MAX6339DUT | 5                     | 3.3        | 1.8†       | Adj*       | 5                          | AAJX        |
| MAX6339EUT | 5                     | 3.0        | 2.5        | Adj*       | 10                         | AAJY        |
| MAX6339FUT | 5                     | 3.0        | 2.5†       | Adj*       | 5                          | AAJZ        |
| MAX6339GUT | 5                     | 3.0        | 1.8        | Adj*       | 10                         | AAKA        |
| MAX6339HUT | 5                     | 3.0        | 1.8†       | Adj*       | 5                          | AAKB        |
| MAX6339IUT | 5                     | 3.3        | 2.5        | 1.8        | 10                         | AAKC        |
| MAX6339JUT | 5                     | 3.3        | 2.5†       | 1.8†       | 5                          | AAKD        |
| MAX6339KUT | Adj*                  | 3.3        | 2.5        | Adj*       | 10                         | AAKE        |
| MAX6339LUT | Adj*                  | 3.3        | 2.5        | Adj*       | 5                          | AAKF        |
| MAX6339MUT | 5                     | 3.0        | Adj*       | -5         | 10                         | AAKG        |
| MAX6339NUT | 5                     | 3.0        | Adj*       | -5         | 5                          | AAKH        |
| MAX6339OUT | 5                     | 3.3        | Adj*       | -5         | 10                         | AAKI        |
| MAX6339PUT | 5                     | 3.3        | Adj*       | -5         | 5                          | AAKJ        |

\*Adjustable voltage based on +1.23V internal threshold. External threshold voltage can be set using an external resistor-divider.

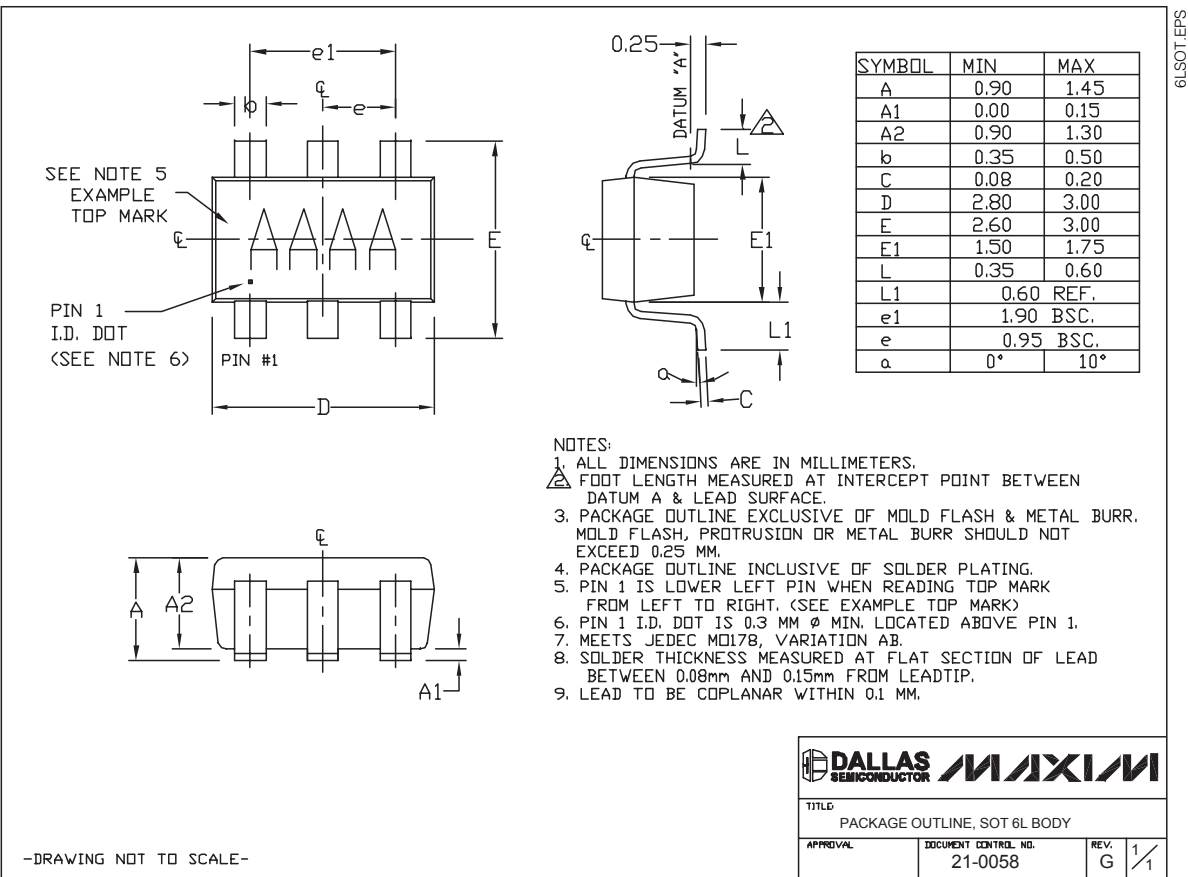
†Normal input voltages for 1.8V and 2.5V are specified for 10% tolerances.

# Quad Voltage $\mu$ P Supervisory Circuit in SOT Package

## Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)

MAX6339



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600

9