

Ordering Information

Part Number	Device Marking Package Packing Method		Packing Method
LL4148	Color Band Marking	SOD-80 2L	Tape and Reel, 7 inch Reel, 2500 pcs

Absolute Maximum Ratings^{(1), (2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter		Value	Unit	
V _{RRM}	Maximum Repetitive Reverse Voltage		100	V	
I _{F(AV)}	Average Rectified Forward Current		200	mA	
l _f	Recurrent Peak Forward Current		500	mA	
I _{FSM}	Non-Repetitive Peak Forward Surge Current	Pulse Width = 1.0 s	1.0	٨	
		Pulse Width = $1.0 \ \mu s$	2.0	A	
T _{STG}	Storage Temperature Range		-65 to +200	°C	
Τ _J	Operating Junction Temperature Range		-55 to +175	°C	

Notes:

- 1. These ratings are based on a maximum junction temperature of 200°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

© 2005 Fairchild Semiconductor Corporation LL4148 Rev. 1.8

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
PD	Power Dissipation	500	mW
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	300	°C/W

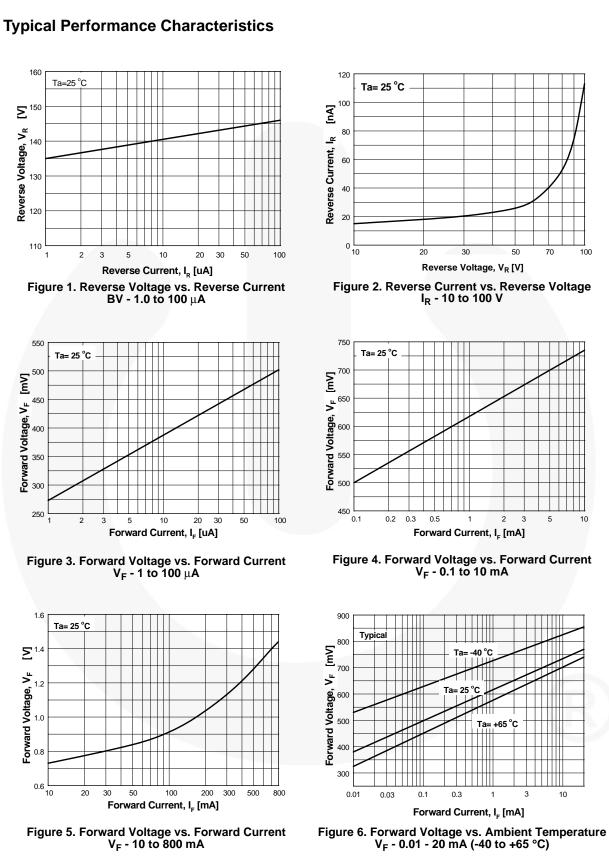
Note:

3. JEDEC Standard 51-3 method (PCB Board size 76 x 114 x 0.6Tmm³)

Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _R	Breakdown Voltage	I _R = 100 μA	100		v
		I _R = 5.0 μA	75		
V _F	Forward Voltage	I _F = 10 mA		1.0	V
I _R	Reverse Leakage	V _R = 20 V		25	nA
		$V_{R} = 20 \text{ V}, \text{ T}_{A} = 150^{\circ}\text{C}$		50	μΑ
CT	Total Capacitance	V _R = 0, f = 1.0 MHz		4.0	pF
t _{rr}	Reverse Recovery Time	$I_F = 10$ mA, $V_R = 6.0$ V (60 mA), $I_{rr} = 1.0$ mA, $R_L = 100$ Ω		4.0	ns

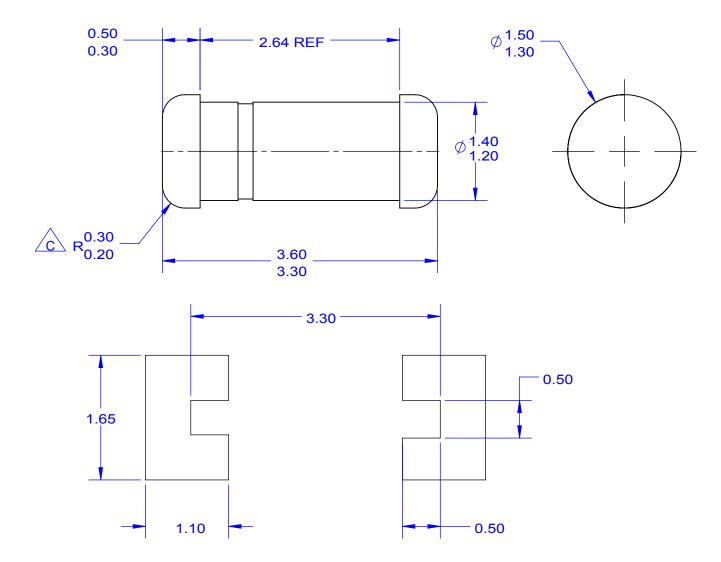


© 2005 Fairchild Semiconductor Corporation LL4148 Rev. 1.8 LL4148

— Small Signal Diode

Typical Performance Characteristics (Continued) 0.90 4.0 T_A = 25 °C T_a = 25°C **SU** 3.5 Total Capacitance (pF) 0800 0800 **Reverse Recovery Time, t**¹⁷ 3.0 5.2 7.0 1.5 1.0 ∟ 10 0.75 20 30 40 50 60 2 12 14 0 6 8 10 4 Reverse Recovery Current, I, [mA] **REVERSE VOLTAGE (V)** Figure 8. Reverse Recovery Time vs. Reverse Recovery Current Figure 7. Total Capacitance 500 500 400 **Power Dissipation, P_b [mW]** 200 Current (mA) 500 SOD80 Inaw - AVERAGE RECTIFIED CURRENT. mA 100 0 L 0 0 L 50 100 150 50 100 150 200 Ambient Temperature (°C) Temperature [°C] Figure 9. Average Rectified Current (I_{F(AV)}) vs. Ambient Temperature (T_A) Figure 10. Power Derating Curve

© 2005 Fairchild Semiconductor Corporation LL4148 Rev. 1.8 LL4148 — Small Signal Diode



LAND PATTERN RECOMMENDATION

NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC DO-213, VARIATION AC.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- CORNER RADIUS IS OPTIONAL.
- D) LAND PATTERN RECOMMENDATION PER IPC DIOMELF3414N
- FAIRCHILD

E) DRAWING FILE NAME: SOD80A REV3

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor has against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death ass

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

www.onsemi.com