

Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	60	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	100	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
ESD Rating, Human Body Model	8	kV
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

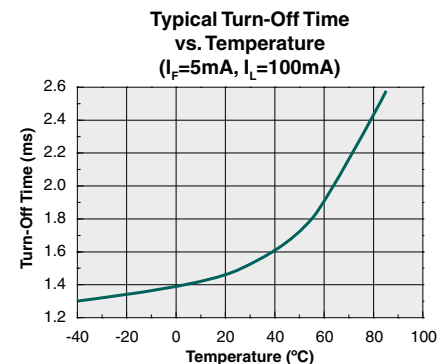
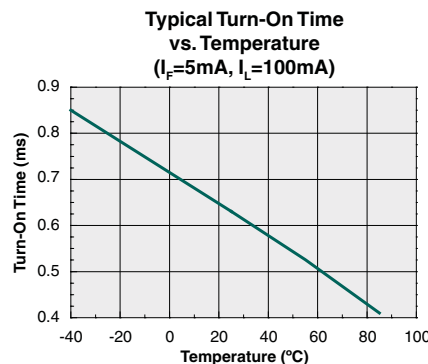
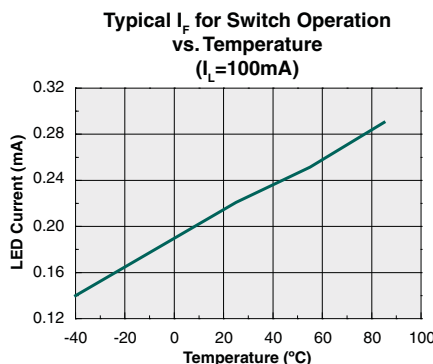
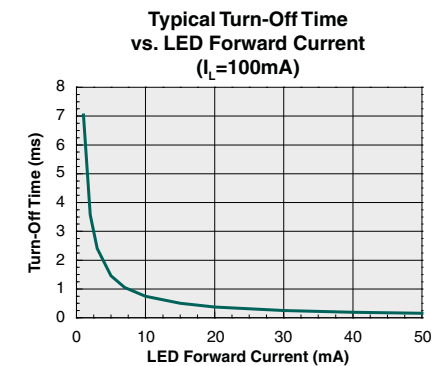
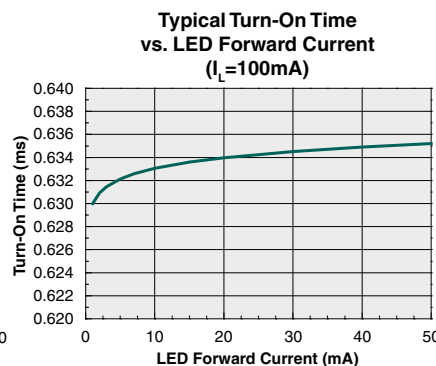
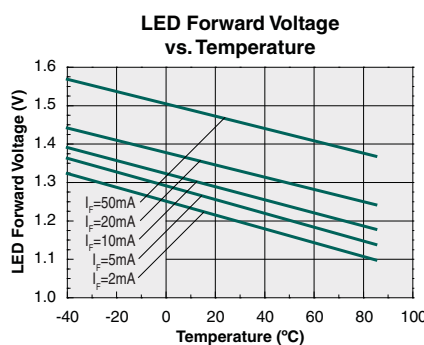
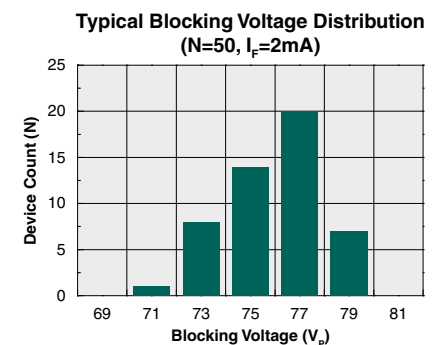
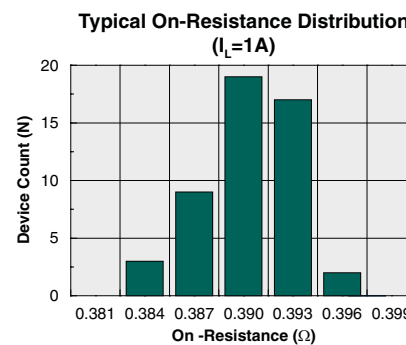
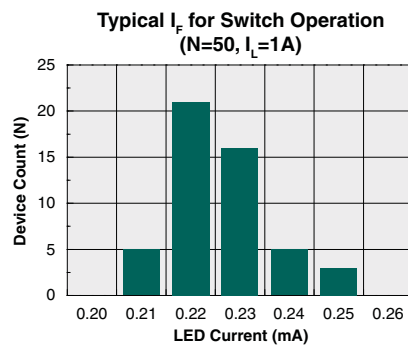
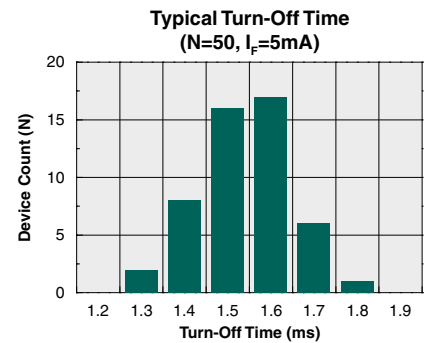
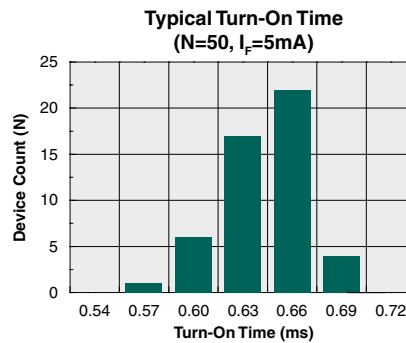
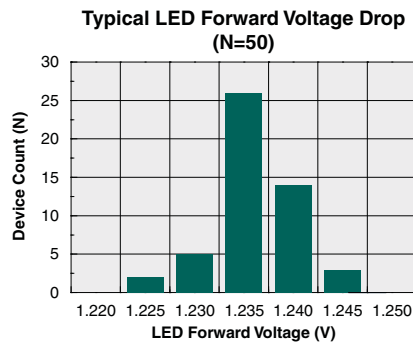
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current						
AC/DC Configuration, Continuous	I _F =0mA	I _L	-	-	1	A _{rms} / A _{DC}
DC Configuration, Continuous			-	-	2	A _{DC}
Peak	I _F =0mA, t ≤ 10ms	I _{LPK}	-	-	±5	A _P
On-Resistance ¹						
AC/DC Configuration	I _F =0mA, I _L =1A	R _{ON}	-	0.39	0.6	Ω
DC Configuration	I _F =0mA, I _L =2A		-	0.1	0.2	
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	0.63	3	ms
Turn-Off		t _{off}	-	1.5	3	
Off-State Leakage Current	I _F =2mA, V _L =60V	I _{LEAK}	-	-	1	μA
Output Capacitance	I _F =2mA, V _L =50V, f=1MHz	C _{OUT}	-	125	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =1A	I _F	-	0.22	2	mA
Input Control Current to Deactivate	-	I _F	0.1	0.21	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Capacitance, Input to Output	-	C _{I/O}	-	3	-	pF

¹ Measurement taken within 1 second of on-time.

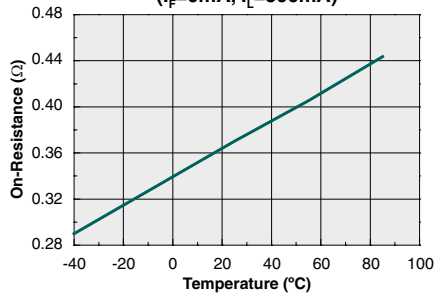
PERFORMANCE DATA* (@25°C Unless Otherwise Noted)



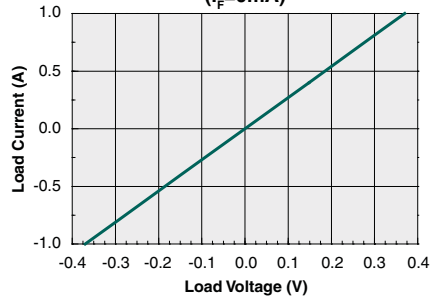
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA* (@25°C Unless Otherwise Noted)

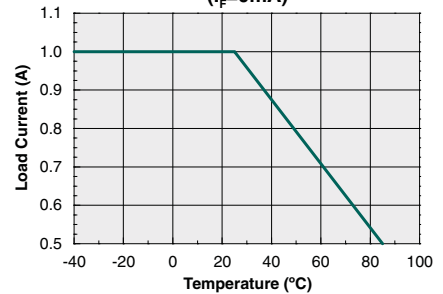
Typical On-Resistance vs. Temperature
AC/DC Configuration
($I_F=0\text{mA}$, $I_L=500\text{mA}$)



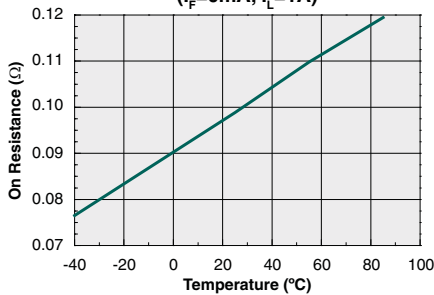
Typical Load Current vs. Load Voltage
AC/DC Configuration
($I_F=0\text{mA}$)



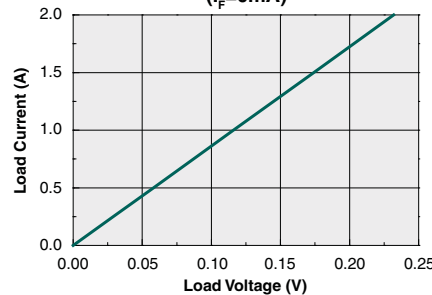
Maximum Load Current vs. Temperature
AC/DC Configuration
($I_F=0\text{mA}$)



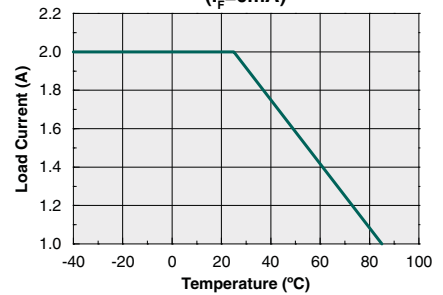
Typical On-Resistance vs. Temperature
DC-Only Configuration
($I_F=0\text{mA}$, $I_L=1\text{A}$)



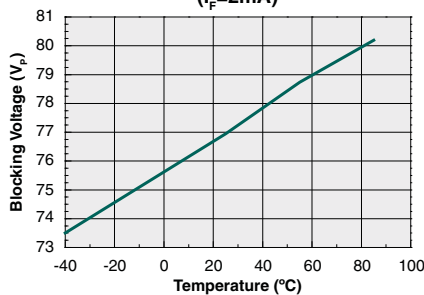
Load Current vs. Load Voltage
DC-Only Configuration
($I_F=0\text{mA}$)



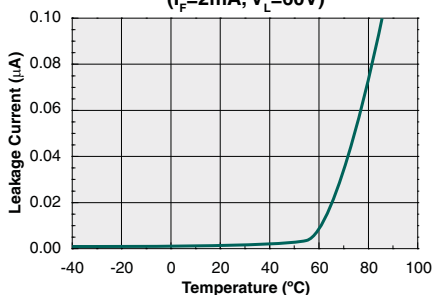
Maximum Load Current vs. Temperature
DC-Only Configuration
($I_F=0\text{mA}$)



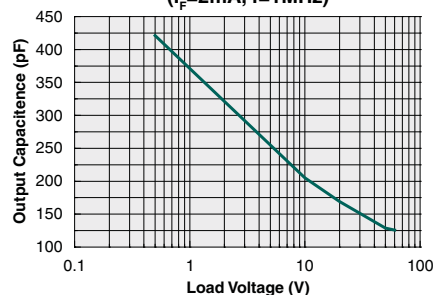
Typical Blocking Voltage vs. Temperature
($I_F=2\text{mA}$)



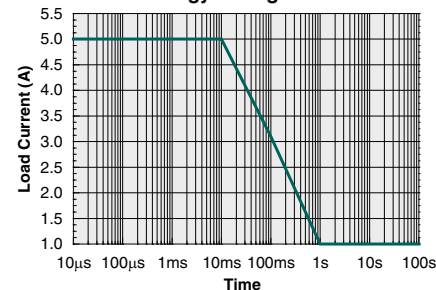
Typical Leakage Current vs. Temperature
($I_F=2\text{mA}$, $V_L=60\text{V}$)



Output Capacitance vs. Load Voltage
($I_F=2\text{mA}$, $f=1\text{MHz}$)



Energy Rating Curve



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
LCB710 / LCB710S	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
LCB710 / LCB710S	250°C for 30 seconds

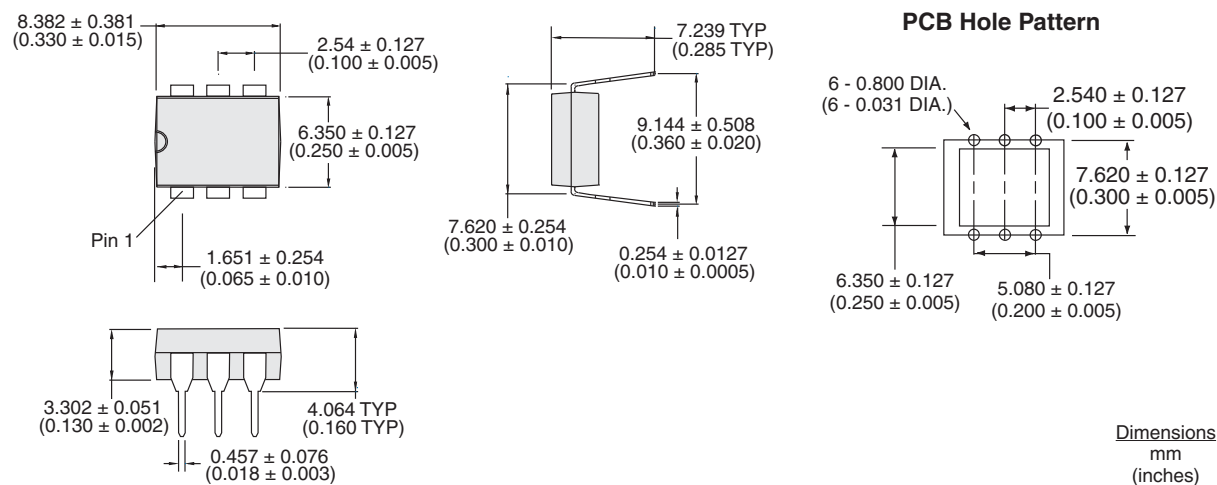
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

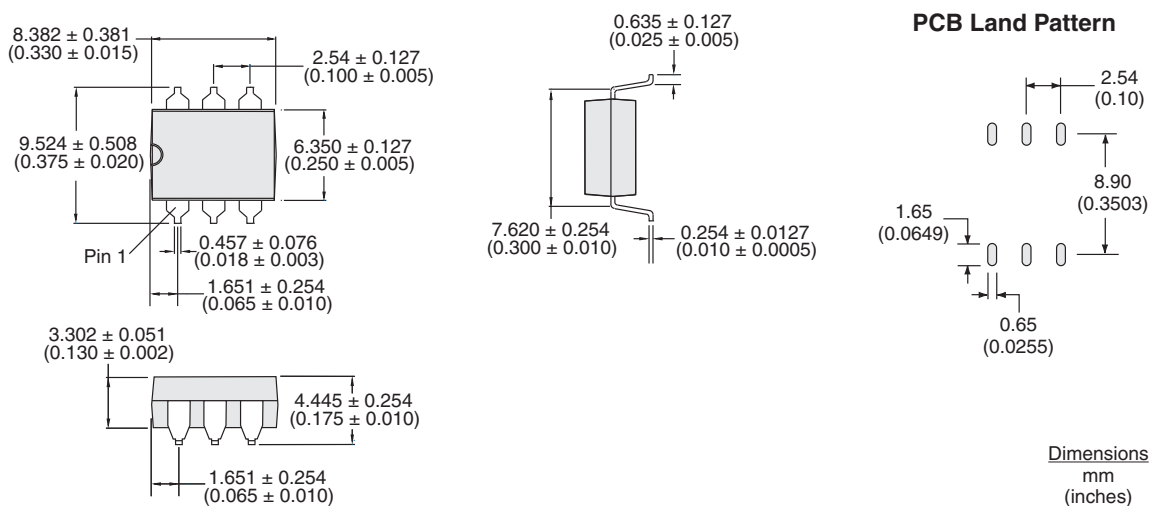


Mechanical Dimensions

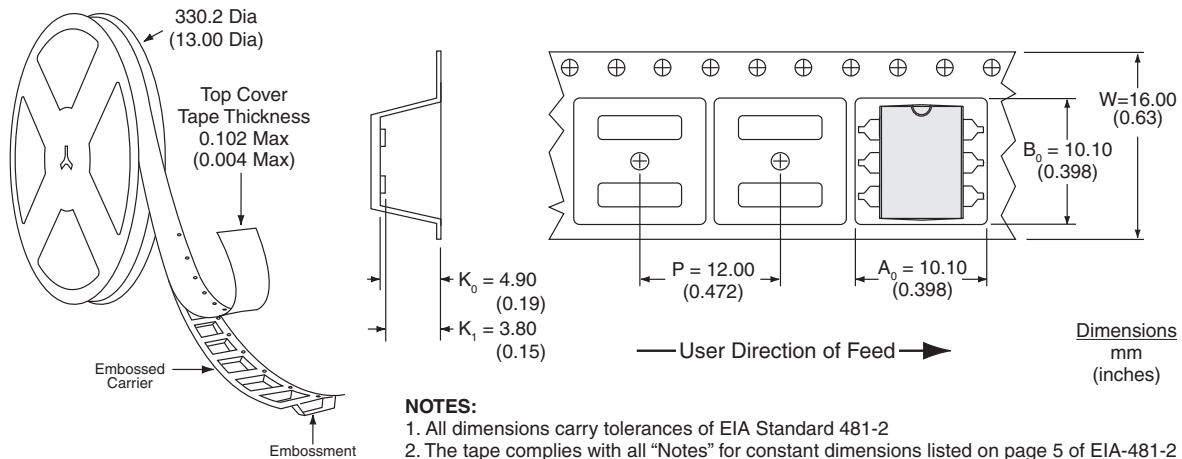
LCB710



LCB710S



LCB710STR Tape & Reel



For additional information please visit our website at: www.ixysic.com

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Specification: DS-LCB710-R02
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