

RATING AND CHARACTERISTIC CURVES L35ESD12VCB2

LITEON

FIG.1- 8/20us pulse waveform according to IEC 61000-4-5

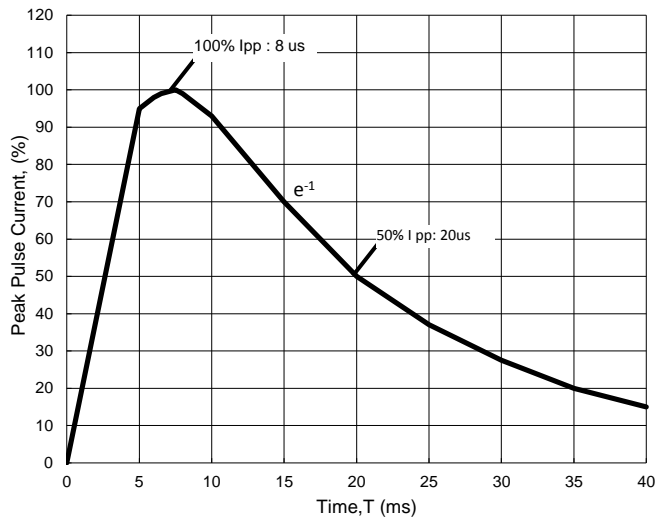


FIG.2- ESD pulse waveform according to IEC 61000-4-2

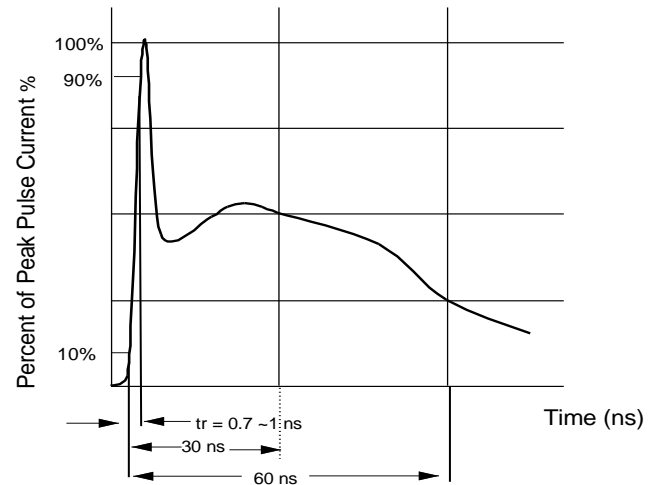


FIG.3- power dissipation versus pulse time

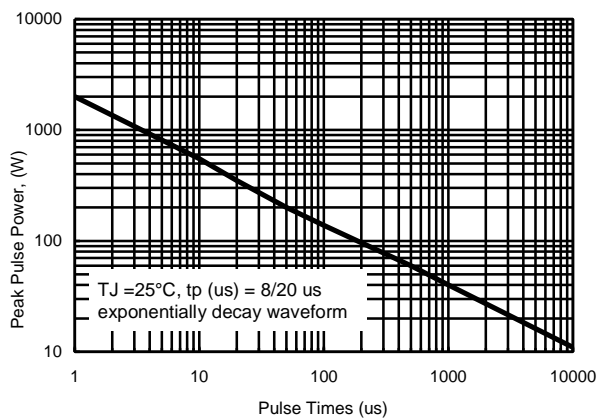


FIG.4- peak pulse power versus T_J

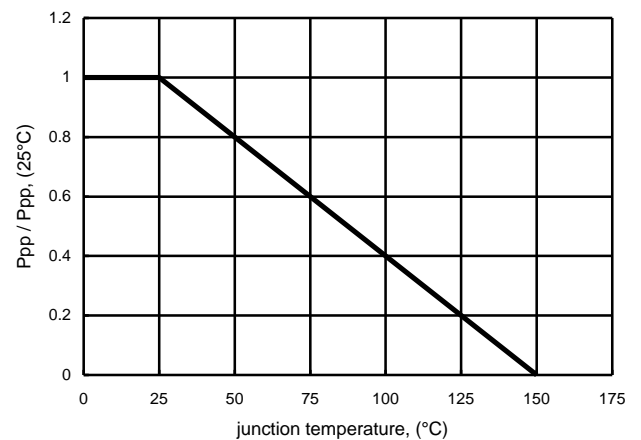


FIG.5- typical junction capacitance

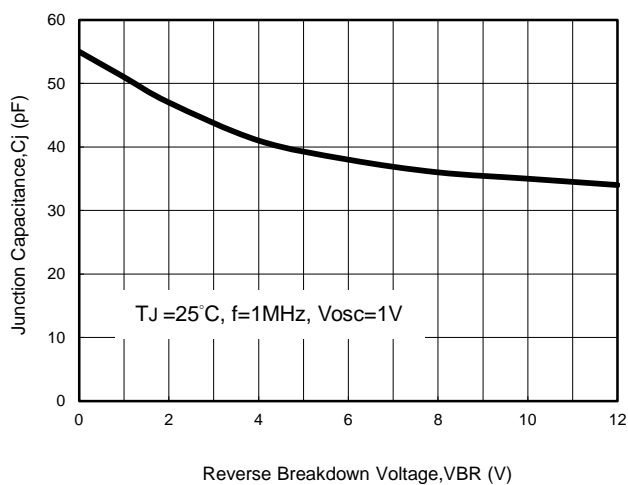
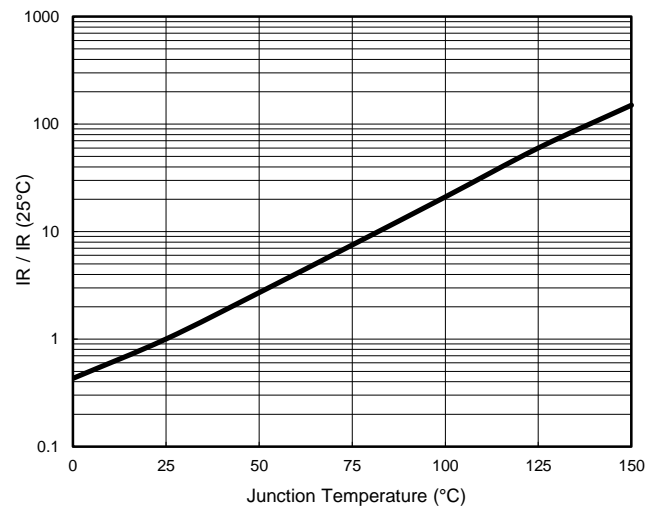


FIG.6- reverse leakage current versus T_J



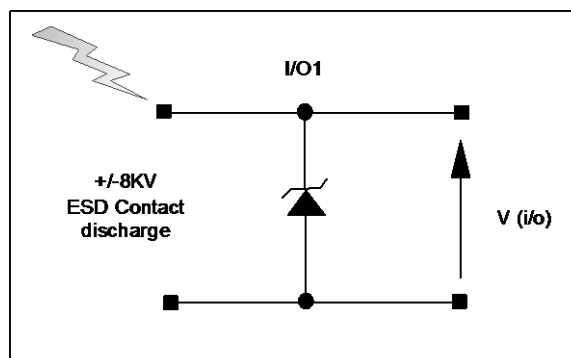


FIG.7- ESD Test Configuration

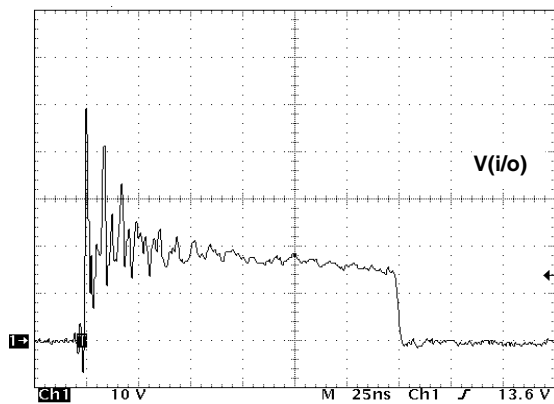


FIG.8- Clamped +8 kV ESD voltage waveform

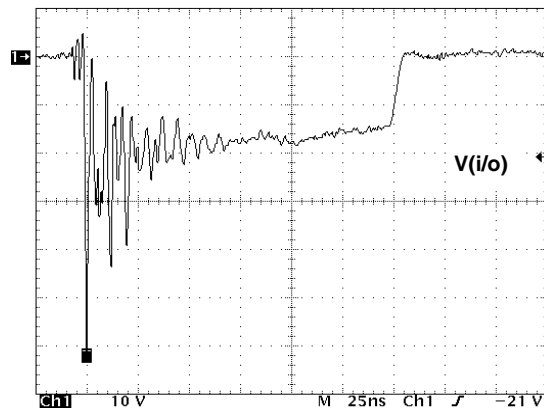
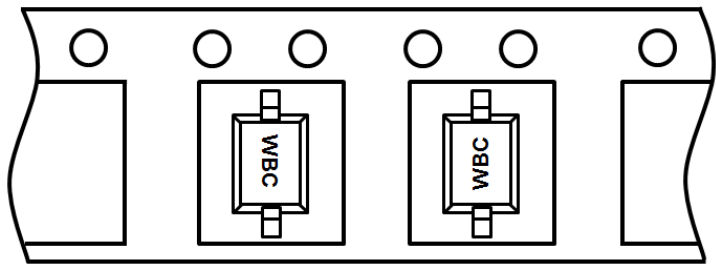


FIG.9- Clamped -8 kV ESD voltage waveform

Marking & Orientation

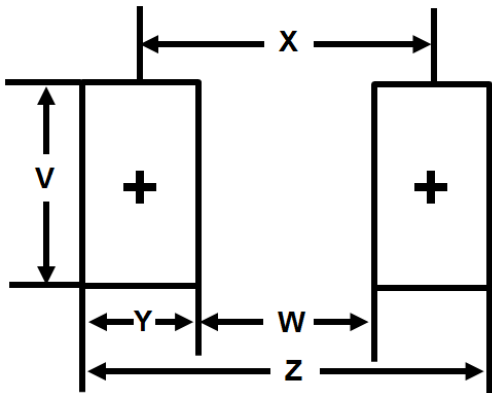


Note: Marking is none direction

Packaging Information

DEVICE	Q'TY/REEL (PCS)	REEL DIA. (INCH)	Q'TY/BOX (PCS)	Q'TY/CARTON (PCS)
L35ESD12VCB2	3000	7	45000	90K/180K

SOD-323 Soldering Pad Layout



Dim.	Millimeters	Inches
Z	3.05	0.120
X	2.15	0.084
W	1.25	0.049
Y	0.90	0.035
V	0.70	0.027

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