

IRG4PC50UD

International
IR Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage ^③	600	----	----	V	$V_{GE} = 0V, I_C = 250\mu A$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	----	0.60	----	V/ $^\circ\text{C}$	$V_{GE} = 0V, I_C = 1.0mA$
$V_{CE(on)}$	Collector-to-Emitter Saturation Voltage	----	1.65	2.0	V	$I_C = 27A$ $V_{GE} = 15V$
		----	2.0	----		$I_C = 55A$ See Fig. 2, 5
		----	1.6	----		$I_C = 27A, T_J = 150^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	3.0	----	6.0		$V_{CE} = V_{GE}, I_C = 250\mu A$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	----	-13	----	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}, I_C = 250\mu A$
g_{fe}	Forward Transconductance ^④	16	24	----	S	$V_{CE} = 100V, I_C = 27A$
I_{CES}	Zero Gate Voltage Collector Current	----	----	250	μA	$V_{GE} = 0V, V_{CE} = 600V$
		----	----	6500		$V_{GE} = 0V, V_{CE} = 600V, T_J = 150^\circ\text{C}$
V_{FM}	Diode Forward Voltage Drop	----	1.3	1.7	V	$I_C = 25A$ See Fig. 13
		----	1.2	1.5		$I_C = 25A, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	----	----	± 100	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	----	180	270	nC	$I_C = 27A$
Q_{ge}	Gate - Emitter Charge (turn-on)	----	25	38		$V_{CC} = 400V$ See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	----	61	90		$V_{GE} = 15V$
$t_{d(on)}$	Turn-On Delay Time	----	46	----	ns	$T_J = 25^\circ\text{C}$
t_r	Rise Time	----	25	----		$I_C = 27A, V_{CC} = 480V$
$t_{d(off)}$	Turn-Off Delay Time	----	140	230		$V_{GE} = 15V, R_G = 5.0\Omega$
t_f	Fall Time	----	74	110		Energy losses include "tail" and diode reverse recovery.
E_{on}	Turn-On Switching Loss	----	0.99	----	mJ	See Fig. 9, 10, 11, 18
E_{off}	Turn-Off Switching Loss	----	0.59	----		
E_{ts}	Total Switching Loss	----	1.58	1.9		
$t_{d(on)}$	Turn-On Delay Time	----	44	----	ns	$T_J = 150^\circ\text{C}$, See Fig. 9, 10, 11, 18
t_r	Rise Time	----	27	----		$I_C = 27A, V_{CC} = 480V$
$t_{d(off)}$	Turn-Off Delay Time	----	240	----		$V_{GE} = 15V, R_G = 5.0\Omega$
t_f	Fall Time	----	130	----		Energy losses include "tail" and diode reverse recovery.
E_{ts}	Total Switching Loss	----	2.3	----	mJ	
L_E	Internal Emitter Inductance	----	13	----	nH	Measured 5mm from package
C_{ies}	Input Capacitance	----	4000	----	pF	$V_{GE} = 0V$
C_{oes}	Output Capacitance	----	250	----		$V_{CC} = 30V$ See Fig. 7
C_{res}	Reverse Transfer Capacitance	----	52	----		$f = 1.0MHz$
t_{rr}	Diode Reverse Recovery Time	----	50	75	ns	$T_J = 25^\circ\text{C}$ See Fig. 14
		----	105	160		$T_J = 125^\circ\text{C}$
I_{rr}	Diode Peak Reverse Recovery Current	----	4.5	10	A	$T_J = 25^\circ\text{C}$ See Fig. 15
		----	8.0	15		$T_J = 125^\circ\text{C}$
Q_{rr}	Diode Reverse Recovery Charge	----	112	375	nC	$T_J = 25^\circ\text{C}$ See Fig. 16
		----	420	1200		$T_J = 125^\circ\text{C}$
$di_{(rec)M}/dt$	Diode Peak Rate of Fall of Recovery During t_b	----	250	----	A/ μs	$T_J = 25^\circ\text{C}$
		----	160	----		$T_J = 125^\circ\text{C}$

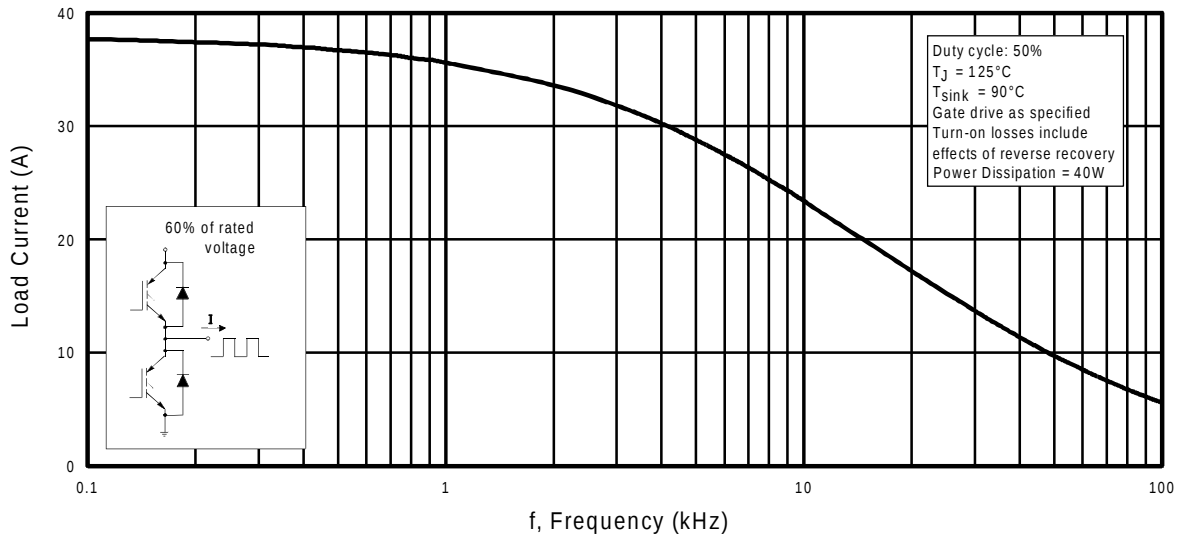


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

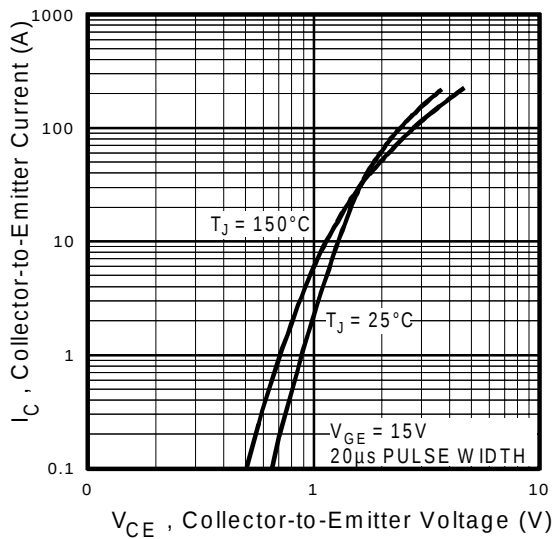


Fig. 2 - Typical Output Characteristics

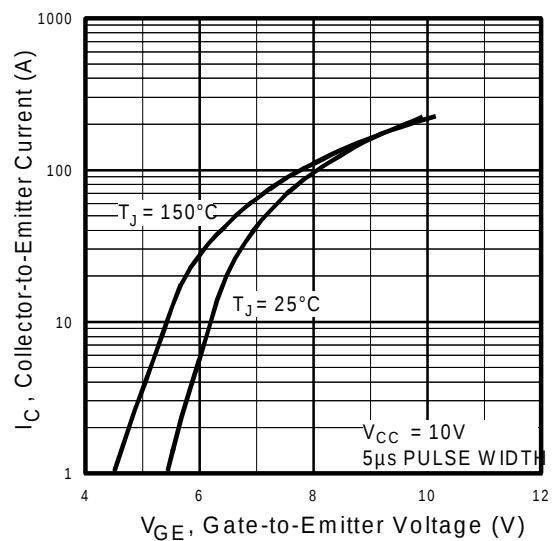


Fig. 3 - Typical Transfer Characteristics

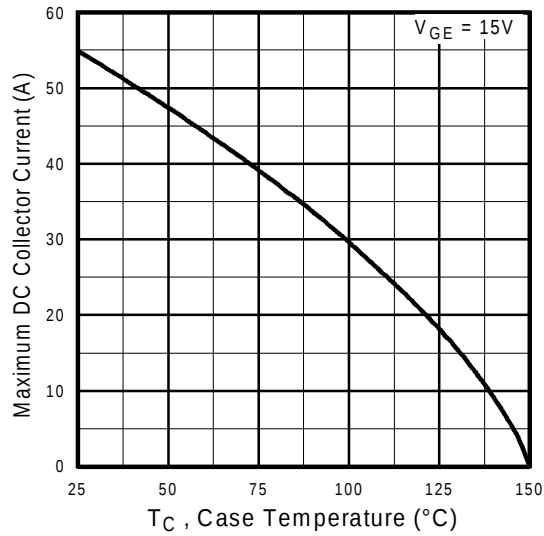


Fig. 4 - Maximum Collector Current vs. Case Temperature

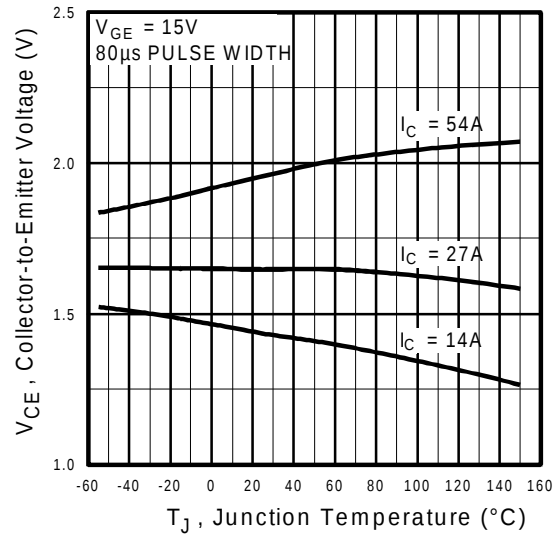


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

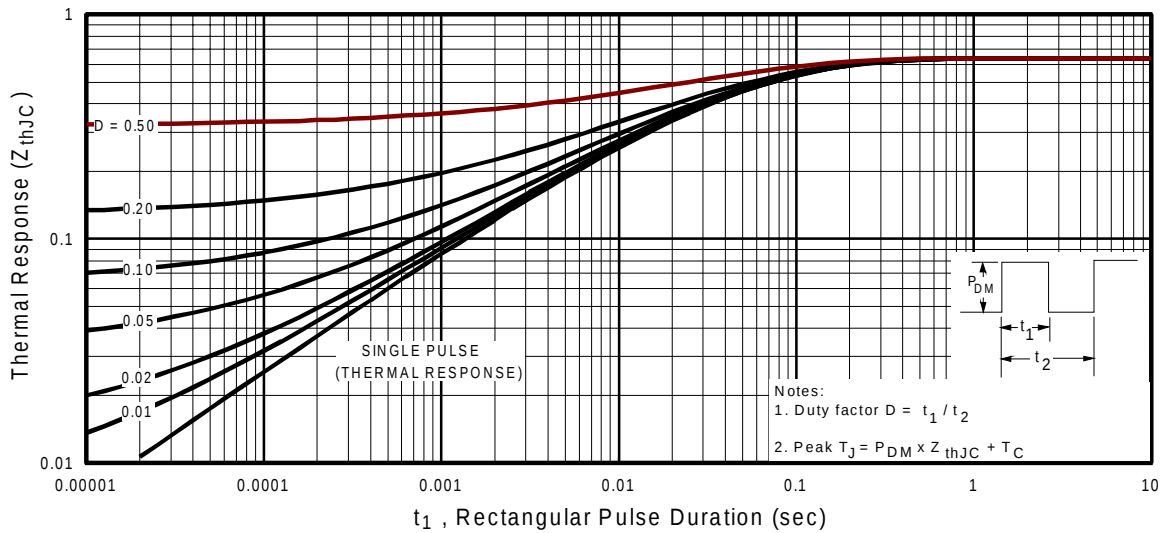


Fig. 6 - Maximum IGBT Effective Transient Thermal Impedance, Junction-to-Case

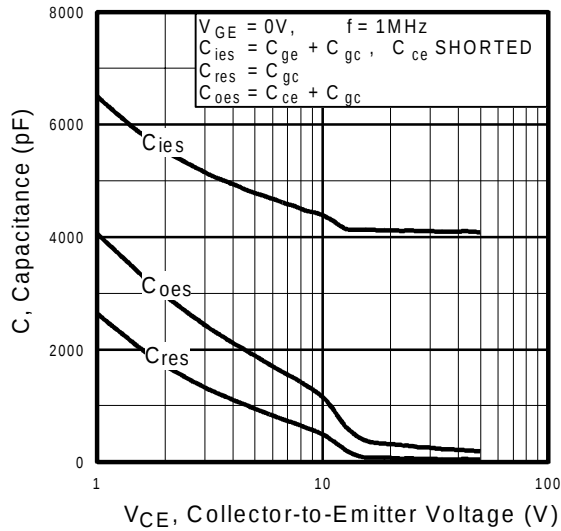


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

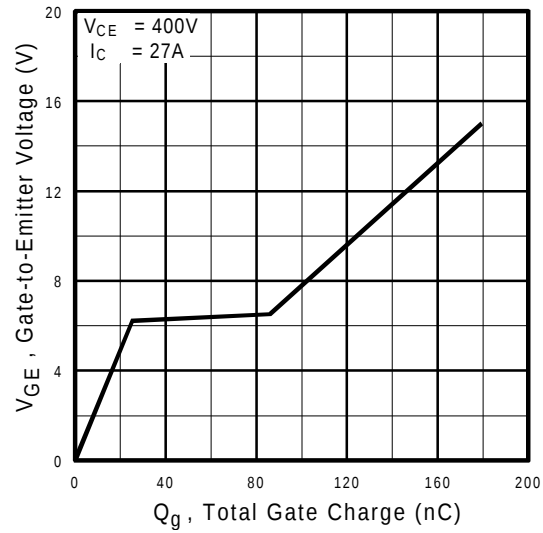


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

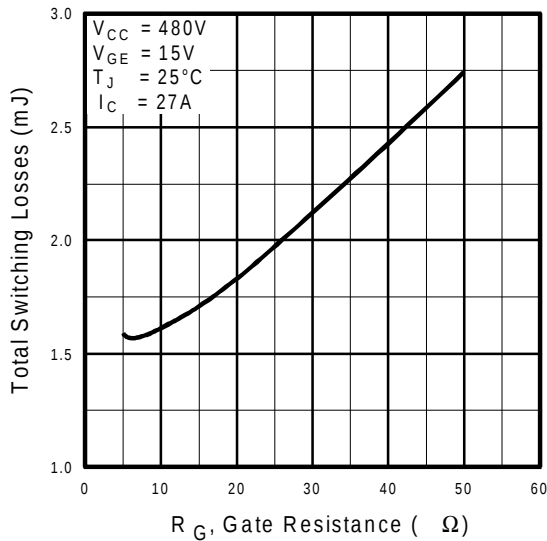


Fig. 9 - Typical Switching Losses vs. Gate Resistance

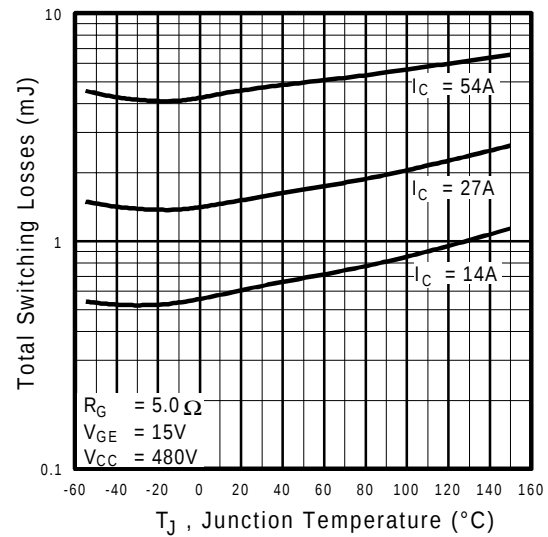
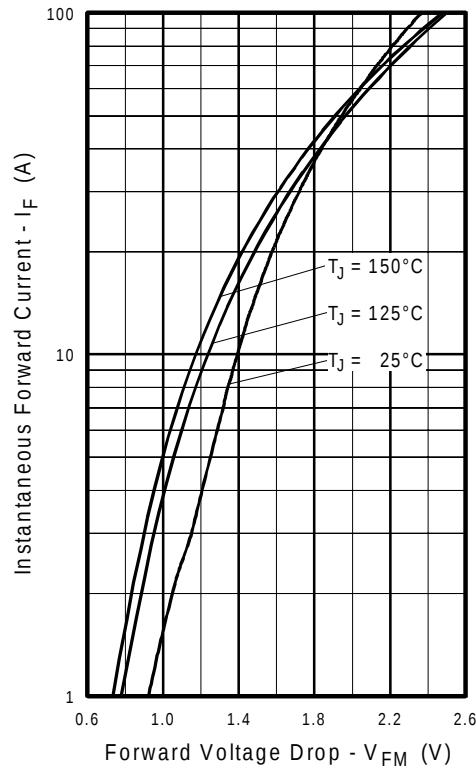
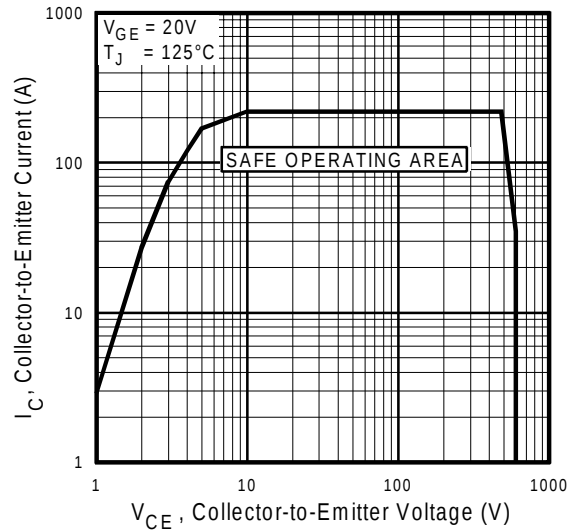
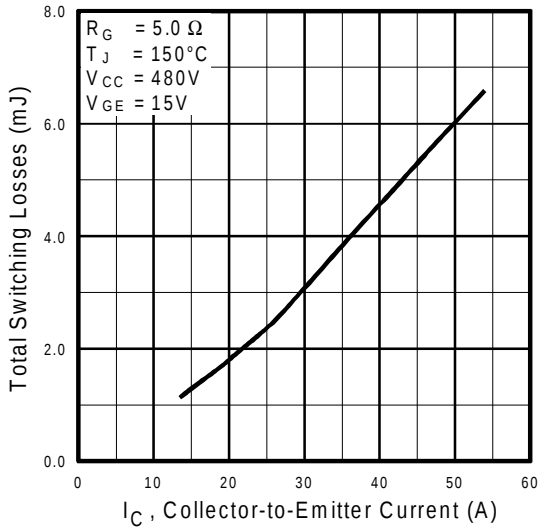


Fig. 10 - Typical Switching Losses vs. Junction Temperature

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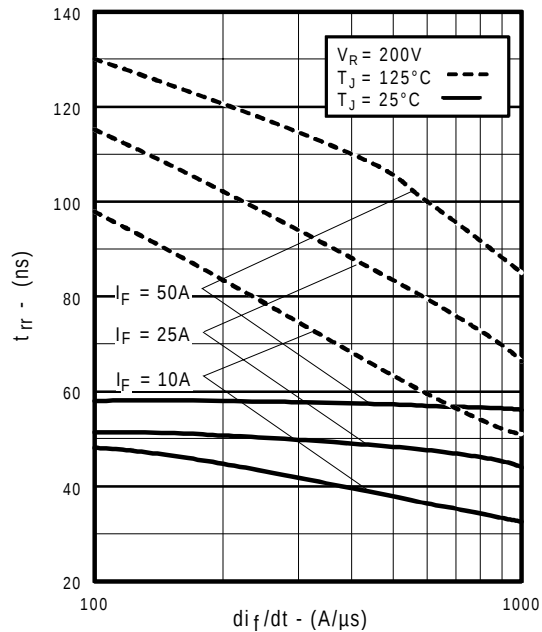


Fig. 14 - Typical Reverse Recovery vs. di_f/dt

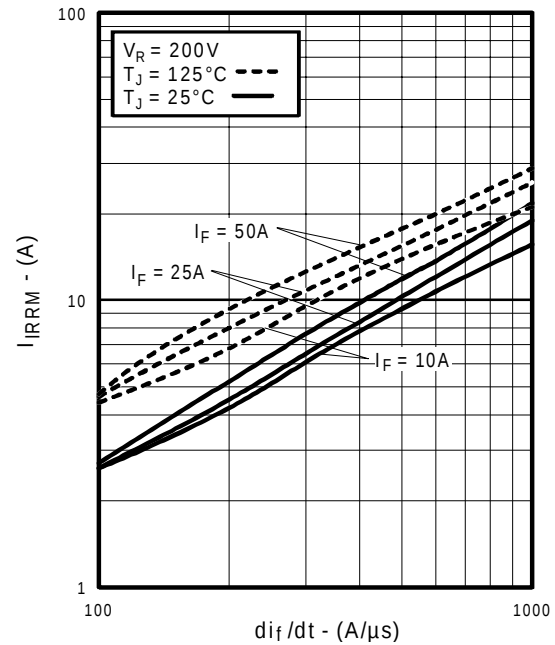


Fig. 15 - Typical Recovery Current vs. di_f/dt

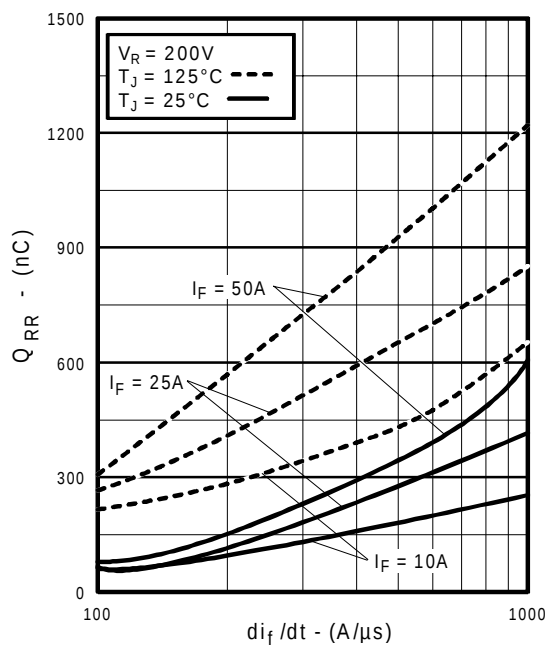


Fig. 16 - Typical Stored Charge vs. di_f/dt

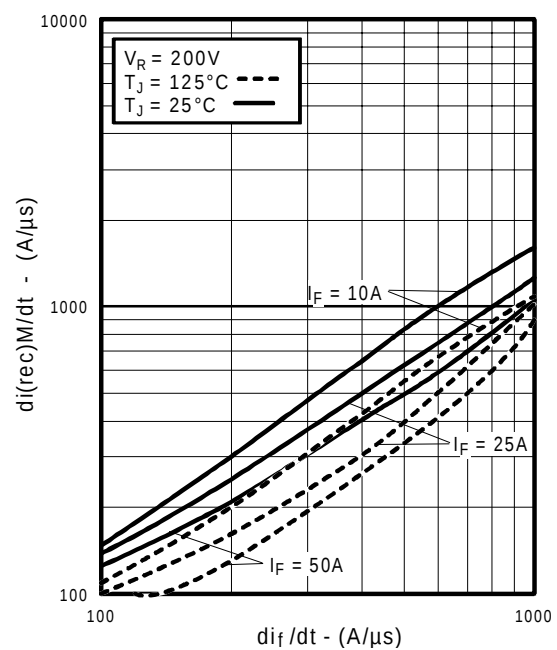


Fig. 17 - Typical $di_{(rec)M}/dt$ vs. di_f/dt

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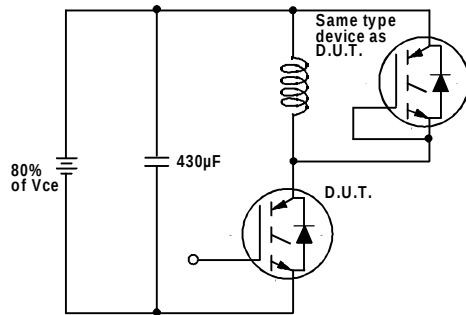


Fig. 18a - Test Circuit for Measurement of I_{LM} , E_{on} , $E_{off}(\text{diode})$, t_{rr} , Q_{rr} , I_{rr} , $t_{d(on)}$, t_r , $t_{d(off)}$, t_f

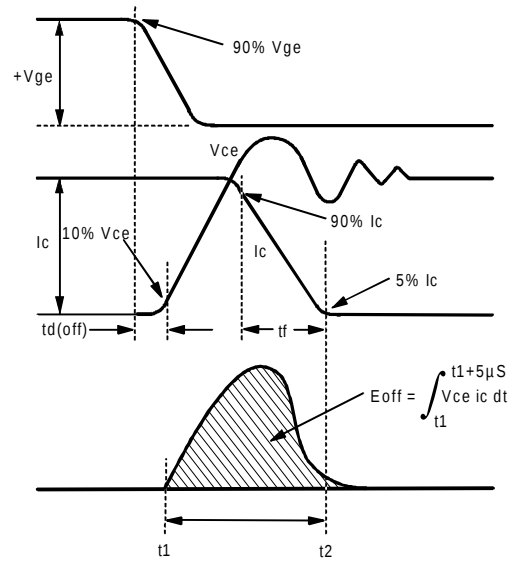


Fig. 18b - Test Waveforms for Circuit of Fig. 18a, Defining E_{off} , $t_{d(off)}$, t_f

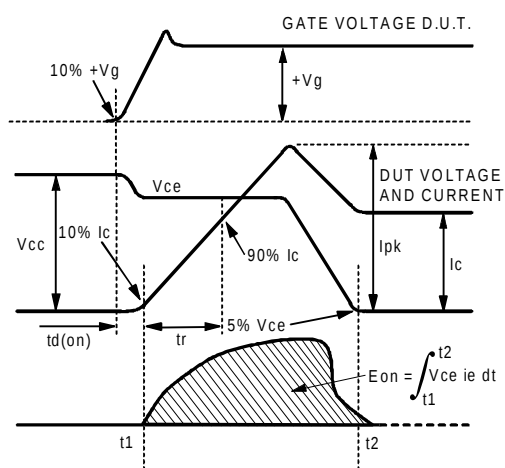


Fig. 18c - Test Waveforms for Circuit of Fig. 18a, Defining E_{on} , $t_{d(on)}$, t_r

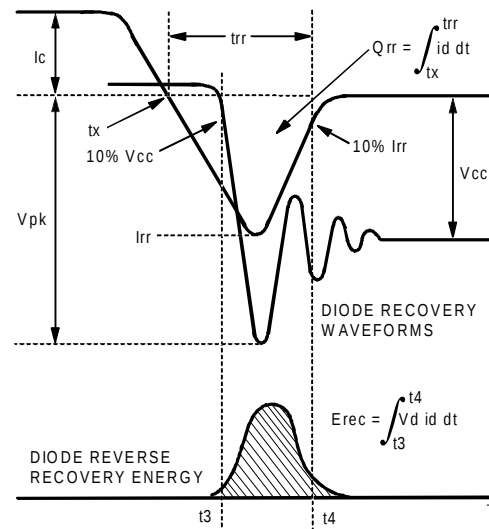


Fig. 18d - Test Waveforms for Circuit of Fig. 18a, Defining E_{rec} , t_{rr} , Q_{rr} , I_{rr}

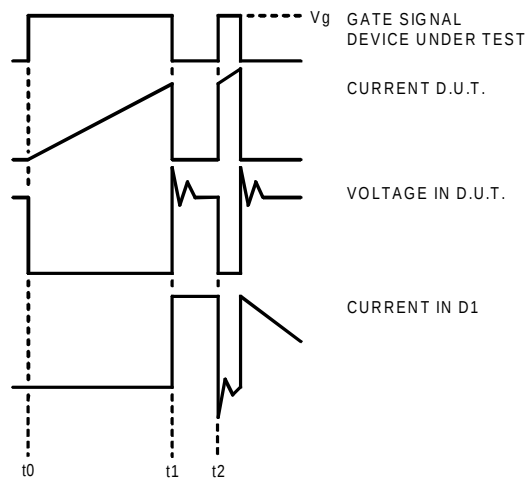


Figure 18e. Macro Waveforms for Figure 18a's Test Circuit

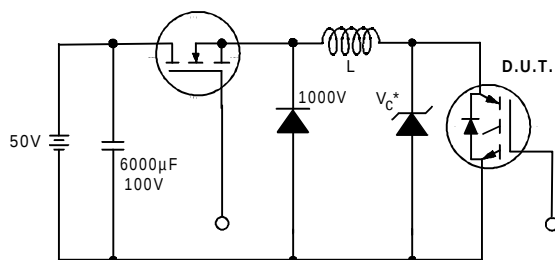


Figure 19. Clamped Inductive Load Test Circuit

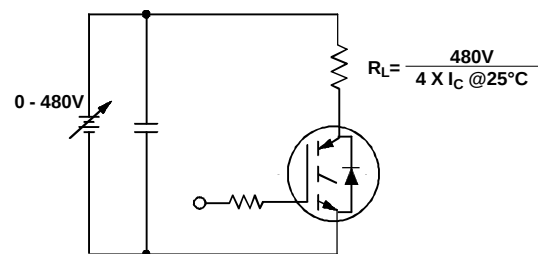


Figure 20. Pulsed Collector Current Test Circuit

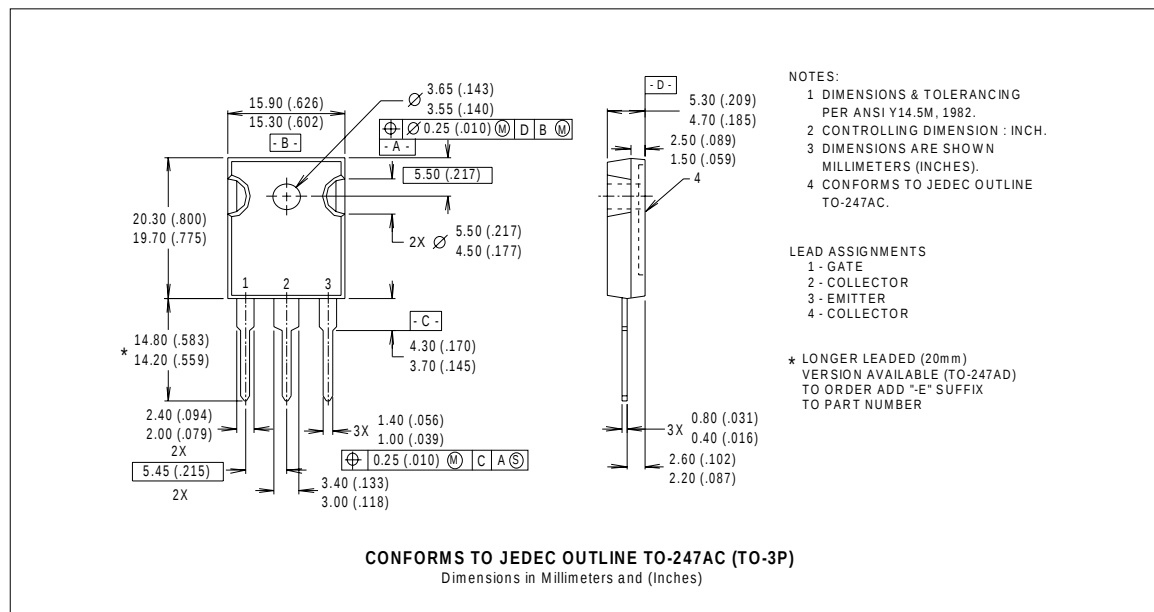
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Notes:

- ① Repetitive rating: $V_{GE} = 20V$; pulse width limited by maximum junction temperature (figure 20)
- ② $V_{CC} = 80\%(V_{CES})$, $V_{GE} = 20V$, $L = 10\mu H$, $R_G = 5.0\Omega$ (figure 19)
- ③ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ④ Pulse width $5.0\mu s$, single shot.

Case Outline — TO-247AC



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Note: For the most current drawings please refer to the IR website at:
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