

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R _{thJA}	-	40	
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.24	-	°C/W
Maximum Junction-to-Case (Drain)	R _{thJC}	-	0.83	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static				•	•	•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$	V, I _D = 250 μA	900	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference t	:o 25 °C, I _D = 1 mA	-	1.0	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V$	_{GS} , I _D = 250 μA	2.0	-	4.0	V
Gate-Source Leakage	I _{GSS}	V _G	_S = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 90	V _{DS} = 900 V, V _{GS} = 0 V		-	100	
2010 date vehage Brain Carrent	יטסס	$V_{DS} = 720 \text{ V}, \text{ V}$	$I_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$	-	-	500	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 2.8 A ^b	-	-	2.5	Ω
Forward Transconductance	9 _{fs}	$V_{DS} = 5$	0 V, I _D = 2.8 A ^b	2.5	-	-	S
Dynamic							
Input Capacitance	C _{iss}	V	_{GS} = 0 V,	-	1600	-	
Output Capacitance	C _{oss}	V	$_{0S} = 25 \text{ V},$	-	180	-	pF
Reverse Transfer Capacitance	C_{rss}	f = 1.0 MHz, see fig. 5		-	63	-	
Total Gate Charge	Q_g			-	-	120	
Gate-Source Charge	Q_{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $I_D = 4.7 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13 ^b		-	16	nC
Gate-Drain Charge	Q_{gd}			-	-	67	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=450~\text{V, I}_D=4.7~\text{A}~,$ $R_g=9.1~\Omega,~R_D=95~\Omega,~\text{see fig. }10^b$		-	15	-	- ns
Rise Time	t _r			-	36	-	
Turn-Off Delay Time	$t_{d(off)}$			-	110	-	
Fall Time	t _f			-	32	-	
Internal Drain Inductance	L_D	Between lead, 6 mm (0.25") fro	Between lead, 6 mm (0.25") from		5.0	-	- N.I.
Internal Source Inductance	L _S	package and center of die contact		-	13	-	- nH
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbo	MOSFET symbol showing the		-	4.7	- A
Pulsed Diode Forward Current ^a	I _{SM}	integral reverse p - n junction diode		-	-	19	
Body Diode Voltage	V_{SD}	$T_J = 25 ^{\circ}\text{C}, I_S$	$_{S} = 4.7 \text{ A}, V_{GS} = 0 \text{ V}^{b}$	-	-	1.8	V
Body Diode Reverse Recovery Time	t _{rr}	T 25 °C -	4.7.A. dl/dt = 100.A/usb		510	770	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$T_J = 25 ^{\circ}\text{C}, I_F = 4.7 \text{A}, dI/dt = 100 \text{A}/\mu\text{s}^b$		-	2.2	3.3	μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L _S and L		L _D)			

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width \leq 300 μ s; duty cycle \leq 2 %.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

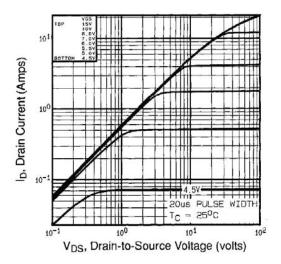


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

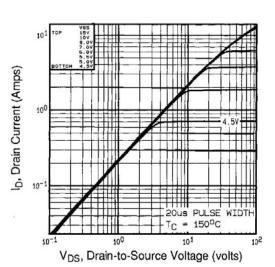


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

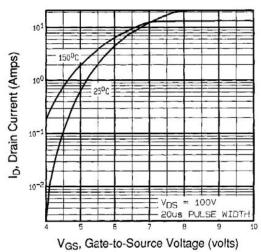


Fig. 3 - Typical Transfer Characteristics

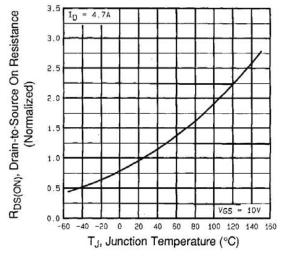


Fig. 4 - Normalized On-Resistance vs. Temperature



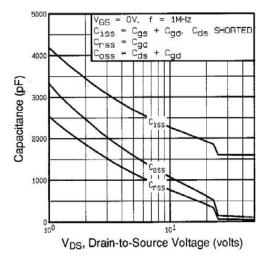


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

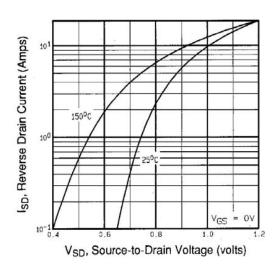


Fig. 7 - Typical Source-Drain Diode Forward Voltage

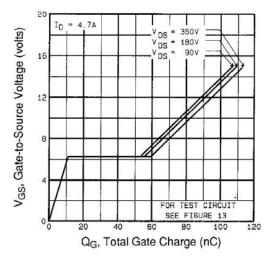


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

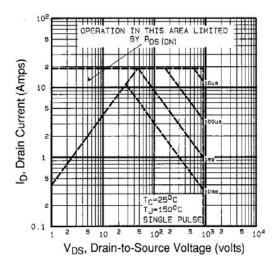


Fig. 8 - Maximum Safe Operating Area





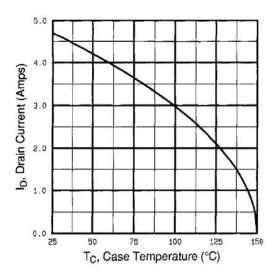


Fig. 9 - Maximum Drain Current vs. Case Temperature

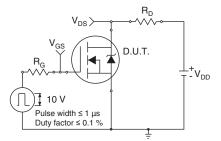


Fig. 10a - Switching Time Test Circuit

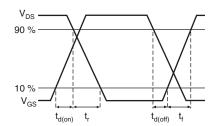


Fig. 10b - Switching Time Waveforms

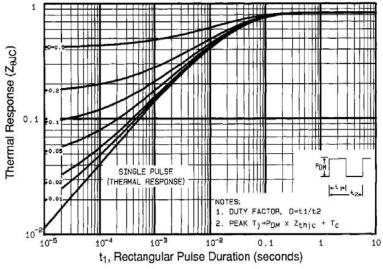
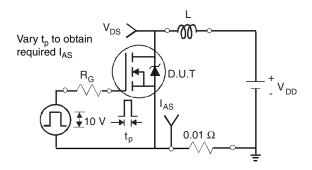


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case





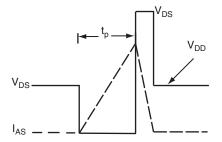


Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

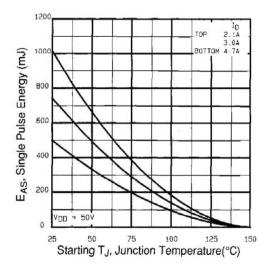


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

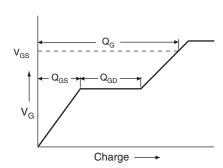


Fig. 13a - Basic Gate Charge Waveform

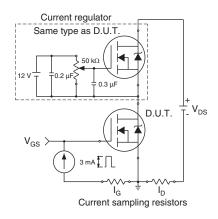
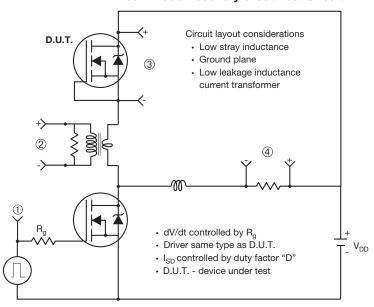


Fig. 13b - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



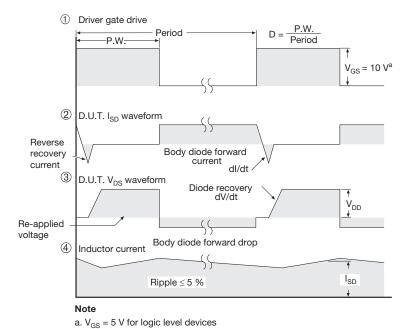


Fig. 14 - For N-Channel

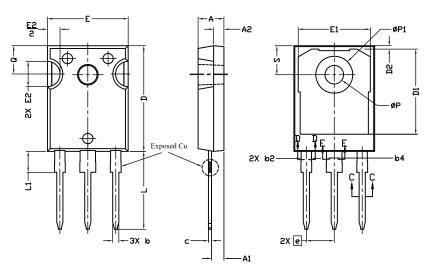
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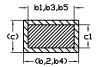


TO-247AC (High Voltage)

VERSION 1: FACILITY CODE = 9







Section C--C,D--D,E--E

	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
Α	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

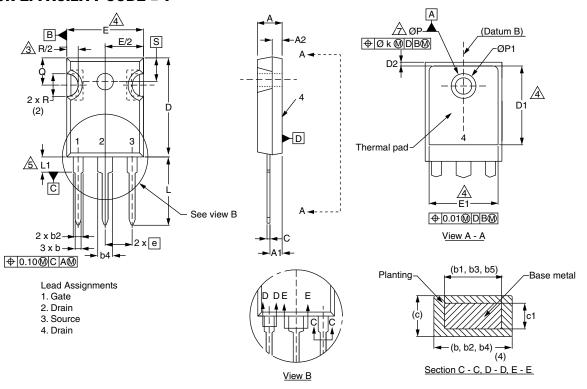
	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
D1	16.25	16.85	5
D2	0.56	0.76	
E	15.50	15.87	4
E1	13.46	14.16	5
E2	4.52	5.49	3
е	5.44 BSC		
L	14.90	15.40	
L1	3.96	4.16	6
ØΡ	3.56	3.65	7
Ø P1	7.19 ref.		
Q	5.31	5.69	
S	5.54	5.74	

Notes

- (1) Package reference: JEDEC® TO247, variation AC
- (2) All dimensions are in mm
- (3) Slot required, notch may be rounded
- (4) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- $^{(7)}$ Ø P to have a maximum draft angle of 1.5 $^\circ$ to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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VERSION 2: FACILITY CODE = Y



	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
Α	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN		
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	5.46 BSC	
Øk	0.254		
L	14.20	16.25	
L1	3.71	4.29	
ØΡ	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

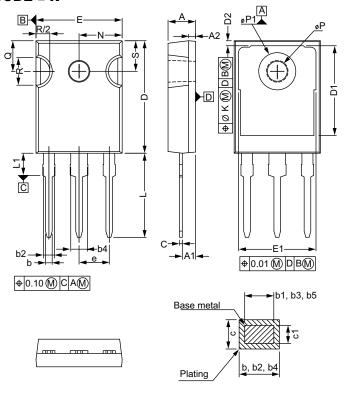
Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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VERSION 3: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	4.65	5.31	
A1	2.21	2.59	
A2	1.17	1.37	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.65	2.39	
b3	1.65	2.34	
b4	2.59	3.43	
b5	2.59	3.38	
С	0.38	0.89	
c1	0.38	0.84	
D	19.71	20.70	
D1	13.08	-	

	MILLIMETERS		
DIM.	MIN.	MAX.	
D2	0.51	1.35	
E	15.29	15.87	
E1	13.46	-	
е	5.46 BSC		
k	0.254		
L	14.20	16.10	
L1	3.71	4.29	
N	7.62 BSC		
Р	3.56	3.66	
P1	=	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

ECN: E20-0545-Rev. F, 19-Oct-2020

DWG: 5971

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

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