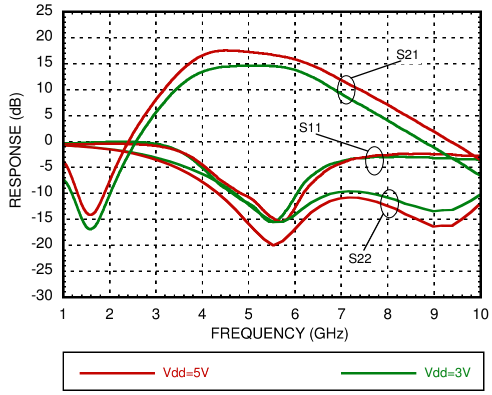
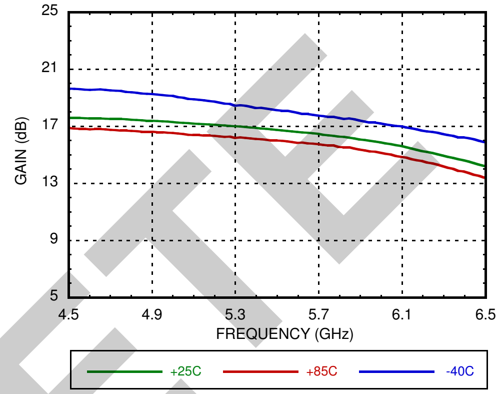
GAAS PHEMT MMIC LOW NOISE AMPLIFIER, 4.8 - 6.0 GHz

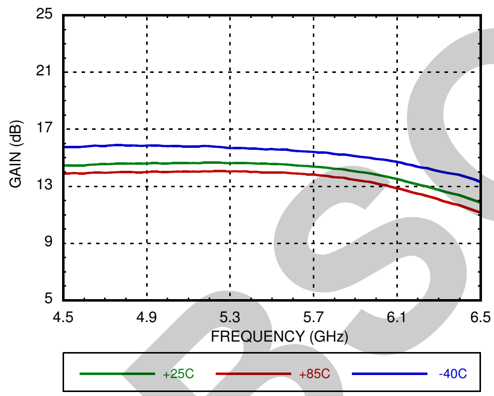
Broadband Gain & Return Loss [1][2]



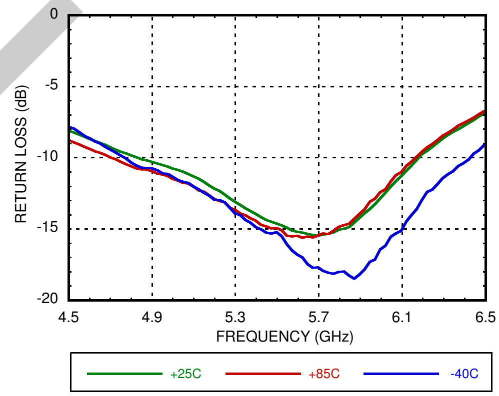
Gain vs. Temperature [1]



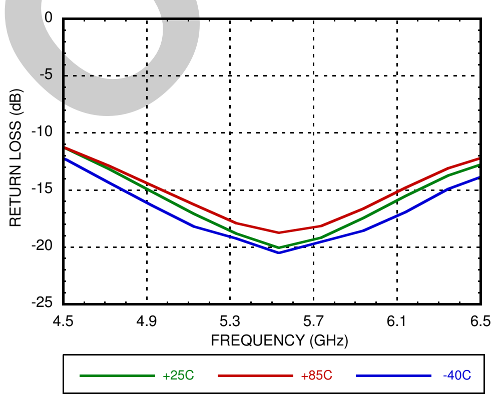
Gain vs. Temperature [2]



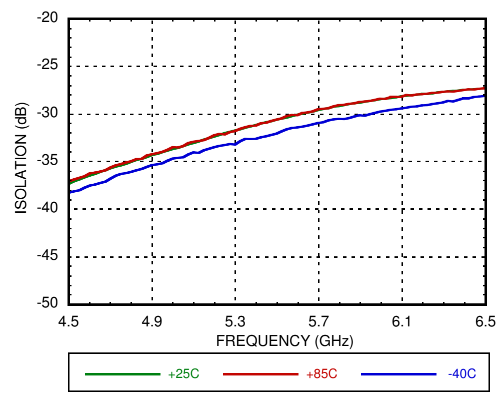
Input Return Loss vs. Temperature [1]



Output Return Loss vs. Temperature [1]

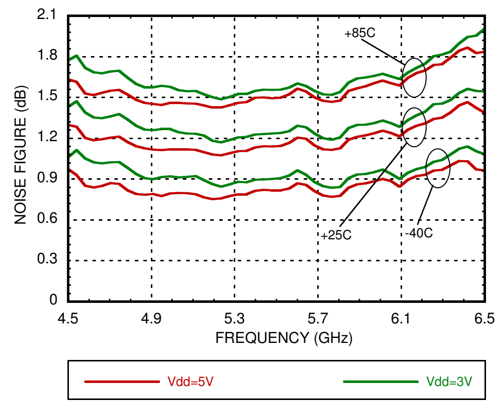


Reverse Isolation vs. Temperature [1]

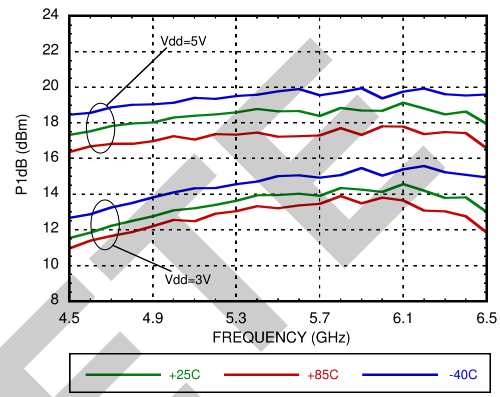


[1] Vdd = 5V, Rbias = 2kΩ   [2] Vdd = 3V, Rbias = 20kΩ

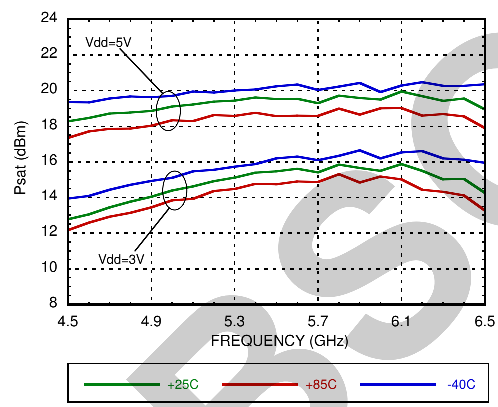
Noise Figure vs. Temperature [1] [2] [4]



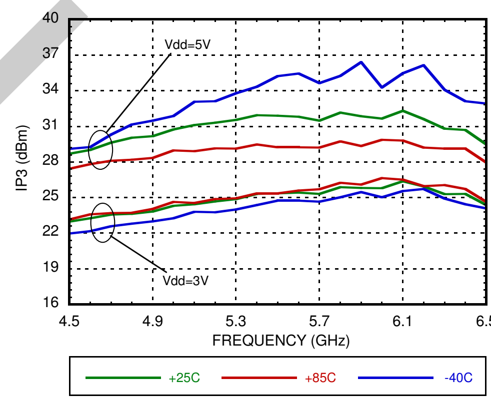
P1dB vs. Temperature [1] [2]



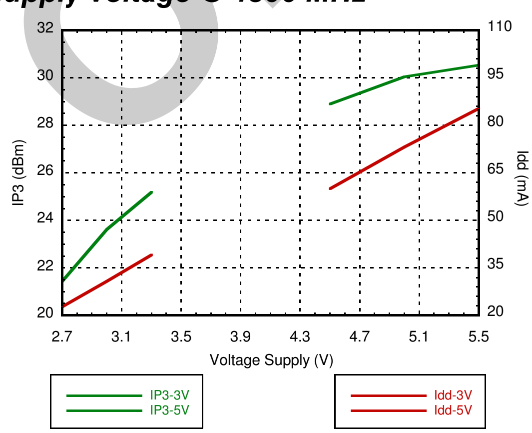
Psat vs. Temperature [1] [2]



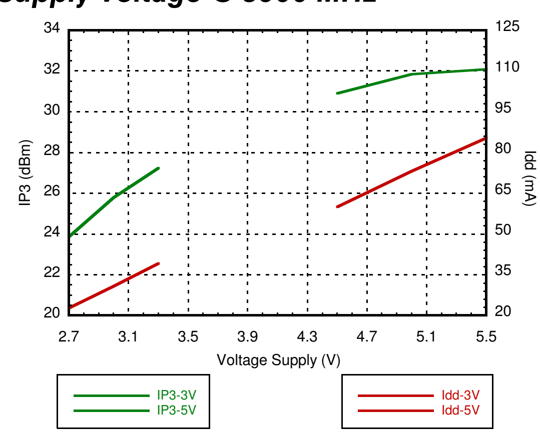
Output IP3 vs. Temperature [1] [2]



Output IP3 and Total Supply Current vs. Supply Voltage @ 4800 MHz [3]



Output IP3 and Total Supply Current vs. Supply Voltage @ 5900 MHz [3]



[1] Vdd = 5V, Rbias = 2k Ω [2] Vdd = 3V, Rbias = 20kΩ
[3] Rbias = 2kΩ for Vdd = 5V, Rbias = 20kΩ for Vdd = 3V
[4] Measurement reference plane shown on evaluation PCB drawing.

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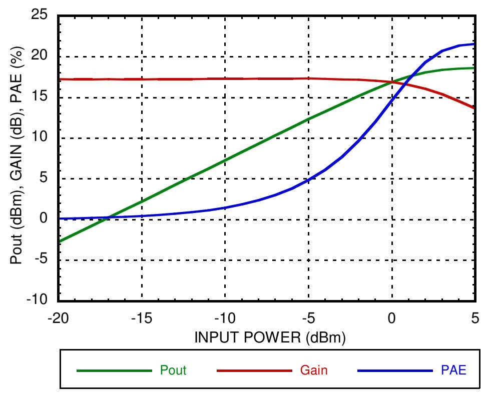
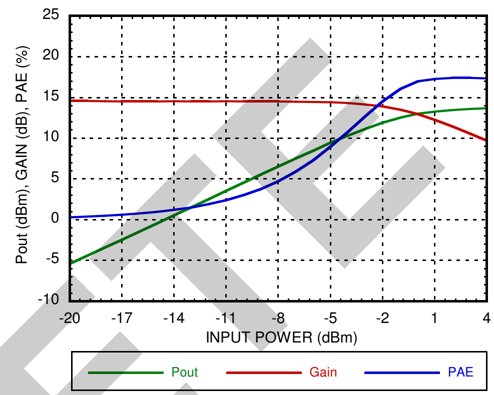
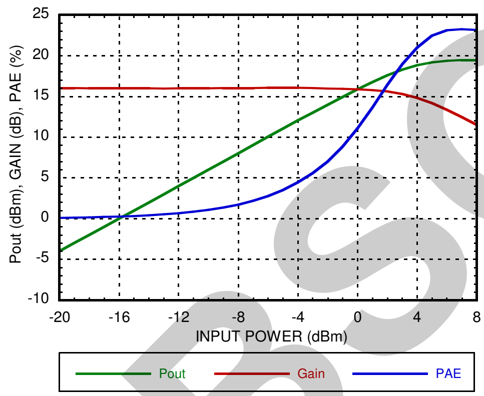
Power Compression @ 4800 MHz [1]



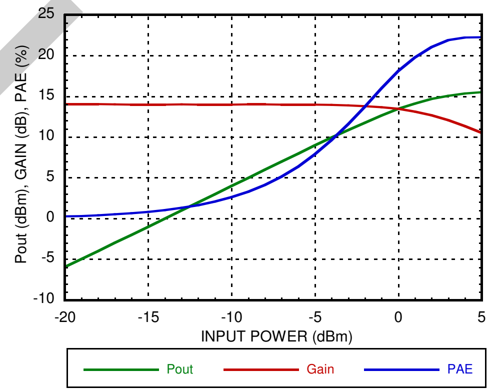
Power Compression @ 4800 MHz [2]



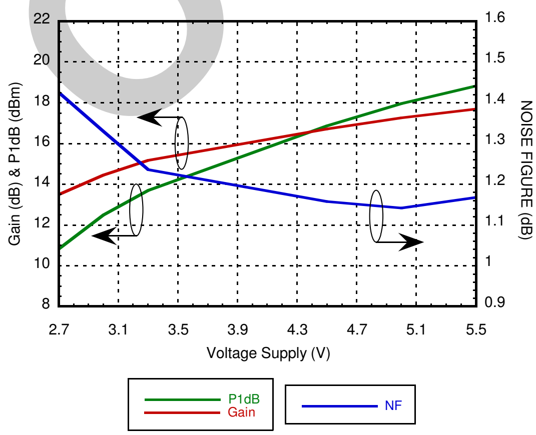
Power Compression @ 5900 MHz [1]



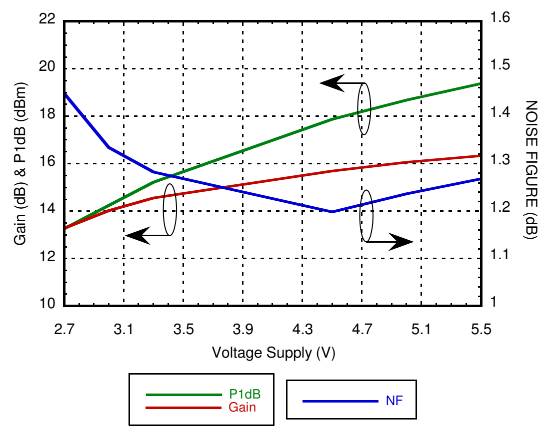
Power Compression @ 5900 MHz [2]



Gain, Power & Noise Figure vs. Supply Voltage @ 4800 MHz [3]



Gain, Power & Noise Figure vs. Supply Voltage @ 5900 MHz [3]



[1] Vdd = 5V, Rbias = 2k Ω [2] Vdd = 3V, Rbias = 20kΩ [3] Rbias = 2kΩ for Vdd = 5V, Rbias = 20kΩ for Vdd = 3V

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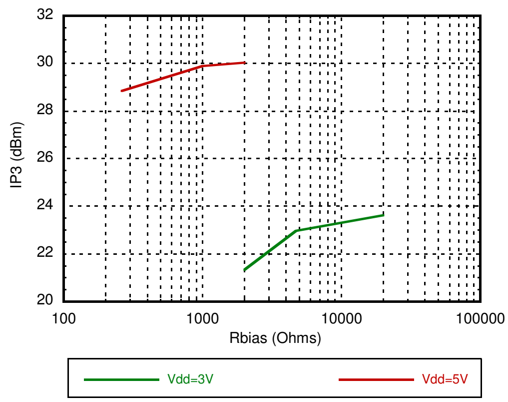
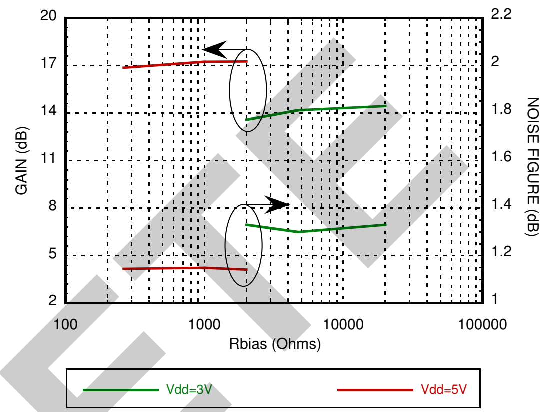
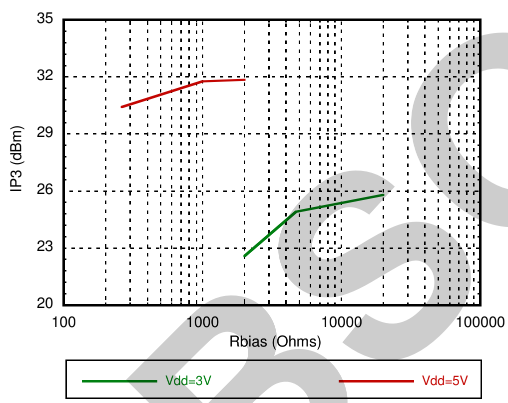
Output IP3 vs. Rbias @ 4800 MHz



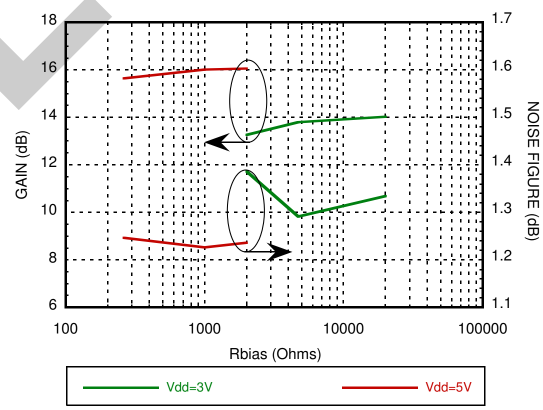
Gain, Noise Figure & Rbias @ 4800 MHz



Output IP3 vs. Rbias @ 5900 MHz



Gain, Noise Figure & Rbias @ 5900 MHz



Absolute Bias Resistor Range & Recommended Bias Resistor Values

Vdd (V)	Rbias (Ohms)			Idd (mA)
	Min	Max	Recommended	
3V	2k [1]	Open Circuit	2k	20
			4.7k	26
			20k	31
5V	150 [2]	Open Circuit	261	50
			1k	65
			2k	73

[1] With Vdd= 3V and Rbias < 2kΩ may result in the part becoming conditionally stable which is not recommended.

[2] With Vdd = 5V and Rbias<150Ω may result in the part becoming conditionally stable which is not recommended.

Absolute Maximum Ratings

Drain Bias Voltage (Vdd)	+5.5V
RF Input Power (RFIN) (Vdd = +5 Vdc)	+20 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 7.73 mW/°C above 85 °C)	0.5 W
Thermal Resistance (channel to ground paddle)	129.5 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Typical Supply Current vs. Supply Voltage

(Rbias = 2kΩ for Vdd = 5V, Rbias = 20kΩ for Vdd = 3V)

Vdd (V)	Idd (mA)
2.7	23
3.0	31
3.3	39
4.5	60
5.0	73
5.5	85

Note: Amplifier will operate over full voltage ranges shown above.

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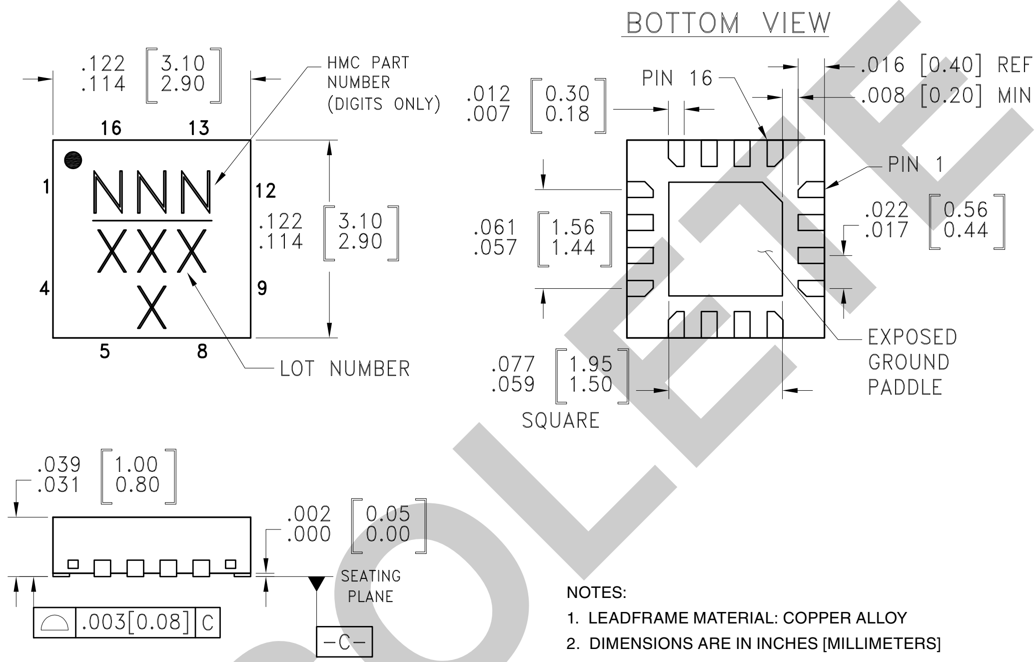
Outline Drawing



NOTES:
1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM. PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC717LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	717 XXXX

[1] Max peak reflow temperature of 235 °C
[2] Max peak reflow temperature of 260 °C
[3] 4-Digit lot number XXXX

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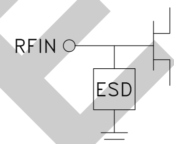
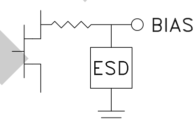
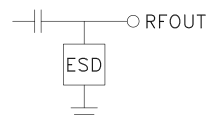
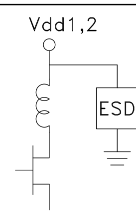
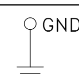
Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3 - 7, 9, 10, 12, 14, 16	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance.	
2	RFIN	This pin is DC coupled See the application circuit for off-chip component.	
8	BIAS	This pin is used to set the DC current of the amplifier by selection of the external bias resistor. See application circuit.	
11	RFOUT	This pin is AC coupled and matched to 50 Ohms	
13, 15	Vdd2, Vdd1	Power supply voltage. Bypass capacitors are required. See application circuit.	
	GND	Package bottom must be connected to RF/DC ground	

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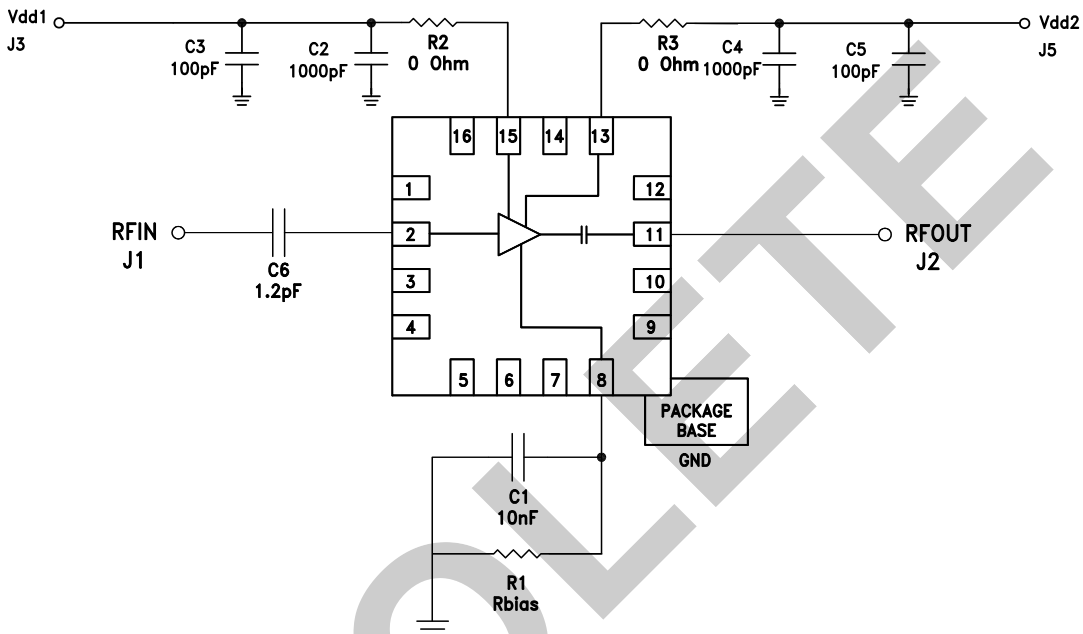
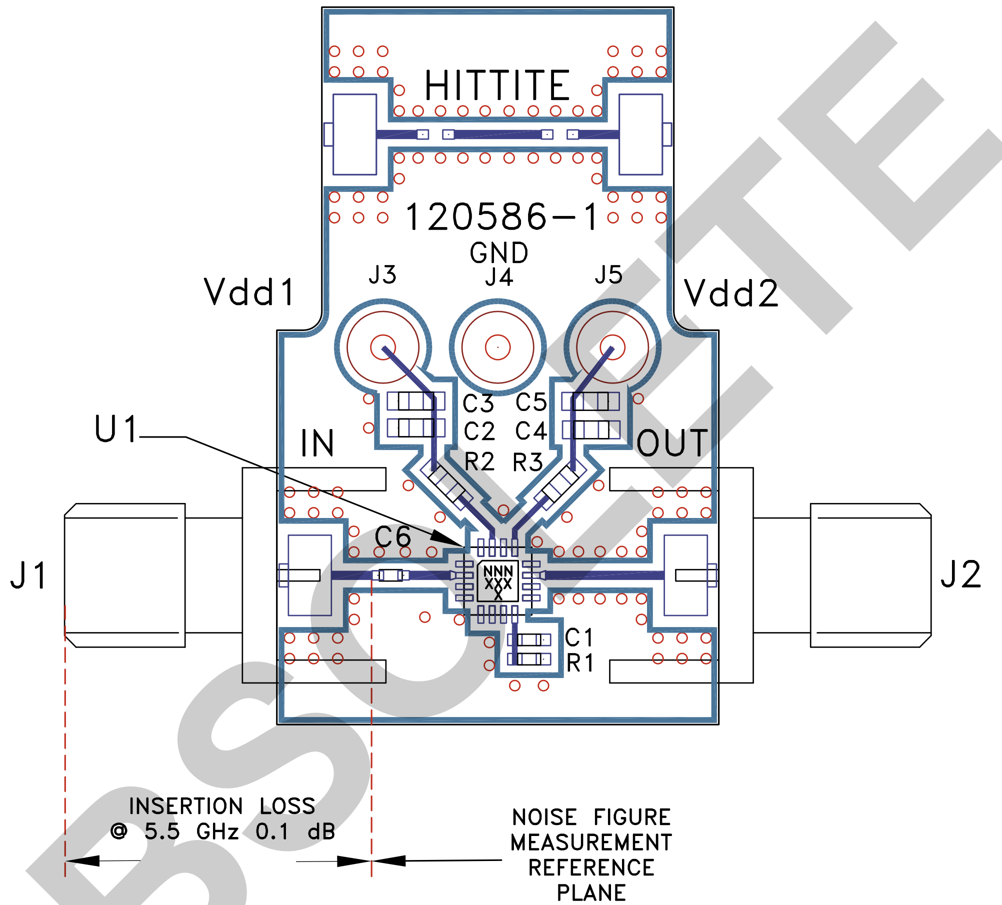
Application Circuit



Evaluation PCB



List of Materials for Evaluation PCB 122416 [1]

Item	Description
J1, J2	PCB Mount SMA Connector
J3 - J5	DC Pins
C1	10 nF Capacitor, 0402 Pkg.
C2, C4	1000 pF Capacitor, 0603 Pkg.
C3, C5	100 pF Capacitor, 0603 Pkg.
C6	1.2 pF Capacitor, 0402 Pkg.
R1	2k Ohm Resistor, 0402 Pkg. (Rbias)
R2, R3	0 Ohm Resistor, 0402 Pkg.
U1	HMC717LP3E Amplifier
PCB [2]	120586 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350.

The circuit board used in this application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.