

# HMC391\* PRODUCT PAGE QUICK LINKS

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## COMPARABLE PARTS

View a parametric search of comparable parts.

## EVALUATION KITS

- HMC391LP4 Evaluation Board

## DOCUMENTATION

### Application Notes

- Determining the FM Bandwidth of a Wideband Varactor Tuned VCO

### Data Sheet

- HMC391 Data Sheet

## REFERENCE MATERIALS

### Product Selection Guide

- RF, Microwave, and Millimeter Wave IC Selection Guide 2017

### Quality Documentation

- Package/Assembly Qualification Test Report: LP4, LP4B, LP4C, LP4K (QTR: 2013-00487 REV: 04)
- Package/Assembly Qualification Test Report: Plastic Encapsulated QFN (QTR: 05006 REV: 02)
- Semiconductor Qualification Test Report: GaAs HBT-A (QTR: 2013-00228)

## DESIGN RESOURCES

- HMC391 Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

## DISCUSSIONS

View all HMC391 EngineerZone Discussions.

## SAMPLE AND BUY

Visit the product page to see pricing options.

## TECHNICAL SUPPORT

Submit a technical question or find your regional support number.

## DOCUMENT FEEDBACK

Submit feedback for this data sheet.

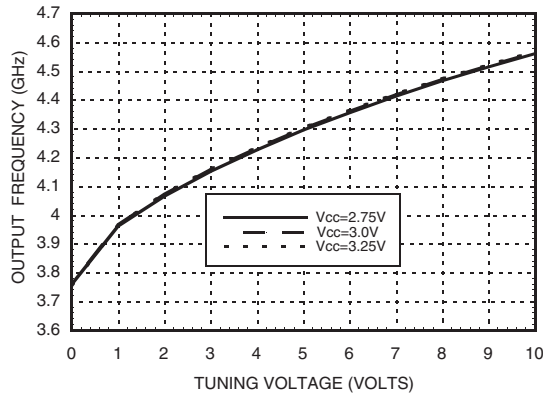
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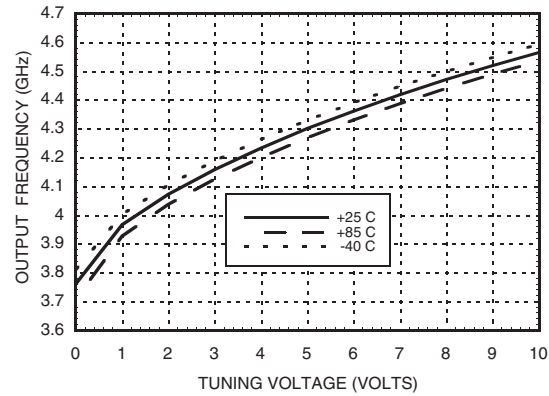


## MMIC VCO w/ BUFFER AMPLIFIER, 3.9 - 4.45 GHz

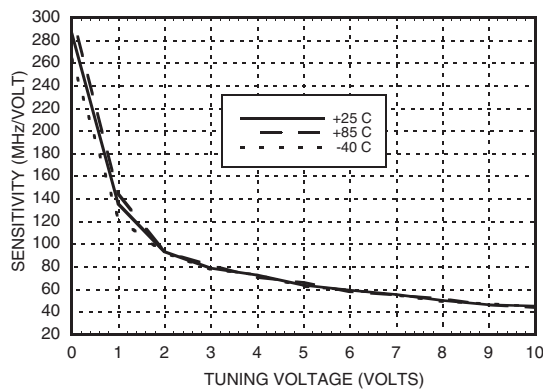
**Frequency vs. Tuning Voltage,  $T = 25^{\circ}\text{C}$**



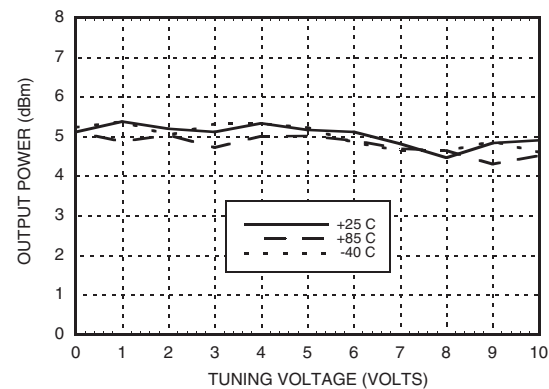
**Frequency vs. Tuning Voltage,  $V_{cc} = +3\text{V}$**



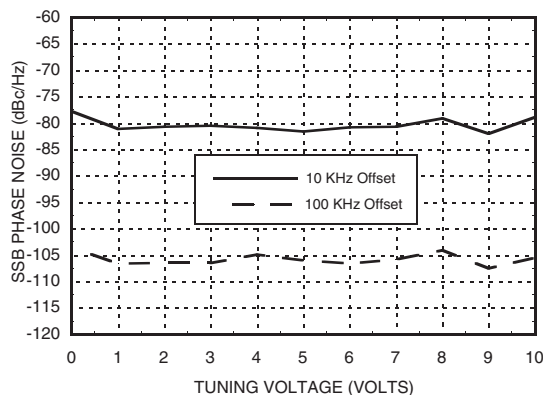
**Sensitivity vs. Tuning Voltage,  $V_{cc} = +3\text{V}$**



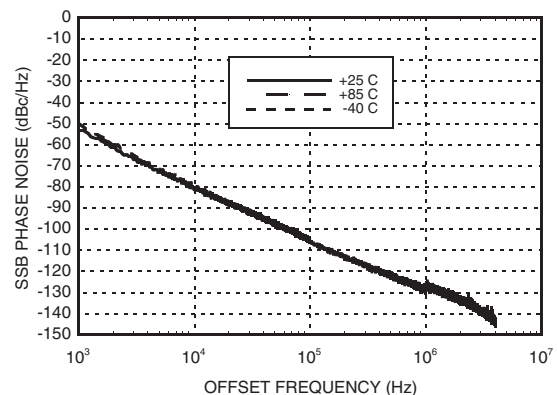
**Output Power vs.  
Tuning Voltage,  $V_{cc} = +3\text{V}$**



**Phase Noise vs. Tuning Voltage**



**Typical SSB Phase Noise @  $V_{tune} = +5\text{V}$**





## MMIC VCO w/ BUFFER AMPLIFIER, 3.9 - 4.45 GHz

### Absolute Maximum Ratings

Vcc	+3.5 Vdc
Vtune	0 to +11V
Channel Temperature	135 °C
Continuous Pdiss (T = 85°C) (derate 3 mW/°C above 85°C)	150 mW
Thermal Resistance (junction to ground paddle)	333 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

### Typical Supply Current vs. Vcc

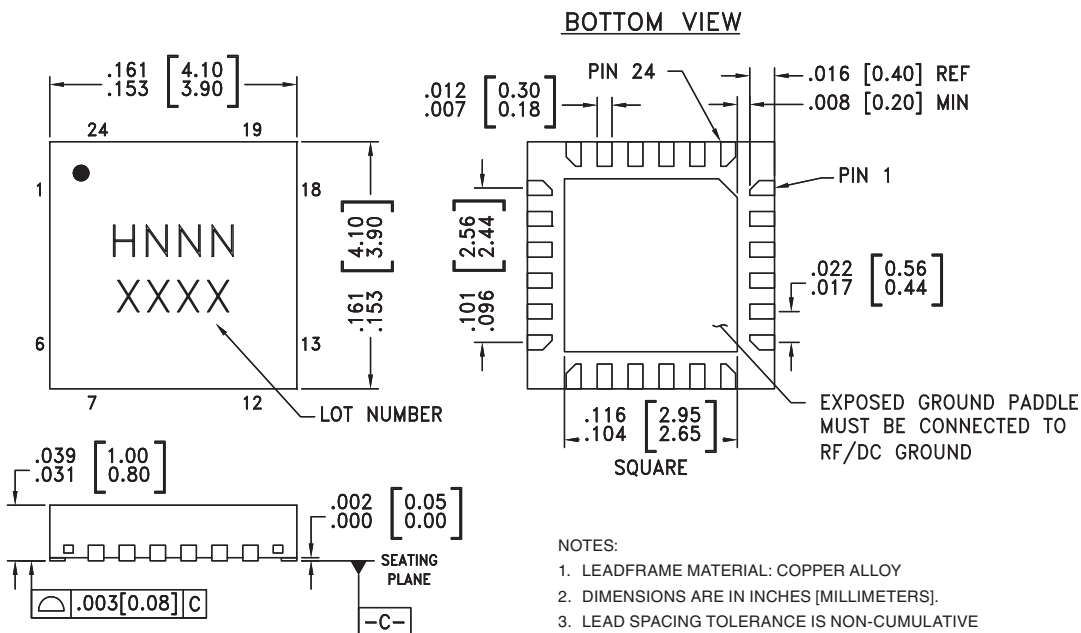
Vcc (V)	Icc (mA)
2.75	22
3.0	30
3.25	39

Note: VCO will operate over full voltage range shown above.



**ELECTROSTATIC SENSITIVE DEVICE  
OBSERVE HANDLING PRECAUTIONS**

### Outline Drawing



#### NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY
2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.  
PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED PCB LAND PATTERN

### Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking <sup>[3]</sup>
HMC391LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 <sup>[1]</sup>	H391 XXXX
HMC391LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	H391 XXXX

[1] Max peak reflow temperature of 235 °C


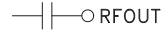
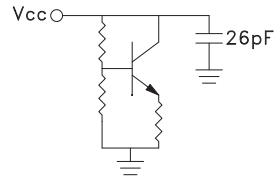
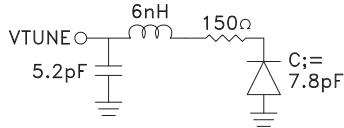
[2] Max peak reflow temperature of 260 °C

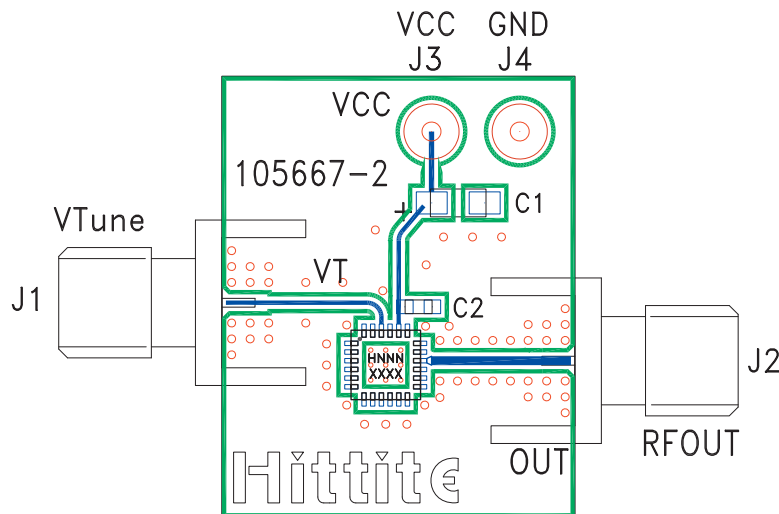
[3] 4-Digit lot number XXXX

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**MMIC VCO w/ BUFFER  
AMPLIFIER, 3.9 - 4.45 GHz**
**Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1- 14, 17 - 19, 21, 23, 24	N/C	No Connection	
15	GND	This pin must be connected to RF & DC ground. Package bottom has an exposed metal paddle that must be RF & DC grounded.	
16	RFOUT	RF output (AC coupled)	
20	Vcc	Supply Voltage Vcc= 3V	
22	VTUNE	Control Voltage Input. Modulation port bandwidth dependent on drive source impedance.	

**Evaluation PCB**

**List of Materials for Evaluation PCB 105706 <sup>[1]</sup>**

Item	Description
J1 - J2	PCB Mount SMA RF Connector
J3 - J4	DC Pin
C1	4.7 $\mu$ F Tantalum Capacitor
C2	10,000 pF Capacitor, 0603 Pkg.
U1	HMC391LP4 / HMC391LP4E VCO
PCB <sup>[2]</sup>	105667 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

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