

ECH8619

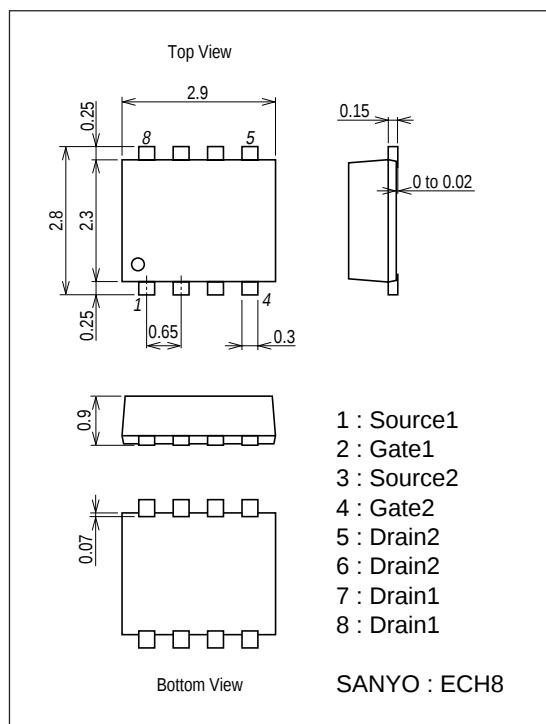
Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	$V_{DS}=20V, f=1MHz$		560		pF
Output Capacitance	Coss	$V_{DS}=20V, f=1MHz$		60		pF
Reverse Transfer Capacitance	Crss	$V_{DS}=20V, f=1MHz$		41		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		11		ns
Rise Time	t_r	See specified Test Circuit.		11		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		61		ns
Fall Time	t_f	See specified Test Circuit.		32		ns
Total Gate Charge	Qg	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		12.8		nC
Gate-to-Source Charge	Qgs	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		2.1		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=30V, V_{GS}=10V, I_D=3A$		2.7		nC
Diode Forward Voltage	V_{SD}	$I_S=3A, V_{GS}=0V$		0.81	1.2	V
[P-channel]						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-1mA, V_{GS}=0V$	-60			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-60V, V_{GS}=0V$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=-10V, I_D=-1mA$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=-10V, I_D=-1A$	2.1	3.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=-1A, V_{GS}=-10V$		160	210	m Ω
	$R_{DS(on)2}$	$I_D=-0.5A, V_{GS}=-4V$		210	295	m Ω
Input Capacitance	Ciss	$V_{DS}=-20V, f=1MHz$		660		pF
Output Capacitance	Coss	$V_{DS}=-20V, f=1MHz$		54		pF
Reverse Transfer Capacitance	Crss	$V_{DS}=-20V, f=1MHz$		42		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		10.5		ns
Rise Time	t_r	See specified Test Circuit.		7.0		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		93		ns
Fall Time	t_f	See specified Test Circuit.		30		ns
Total Gate Charge	Qg	$V_{DS}=-30V, V_{GS}=-10V, I_D=-2A$		15		nC
Gate-to-Source Charge	Qgs	$V_{DS}=-30V, V_{GS}=-10V, I_D=-2A$		2.1		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=-30V, V_{GS}=-10V, I_D=-2A$		2.7		nC
Diode Forward Voltage	V_{SD}	$I_S=-2A, V_{GS}=0V$		-0.82	-1.2	V

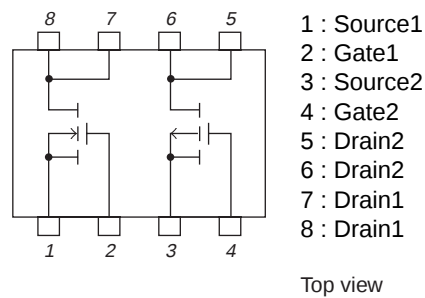
Package Dimensions

unit : mm (typ)

7011A-001

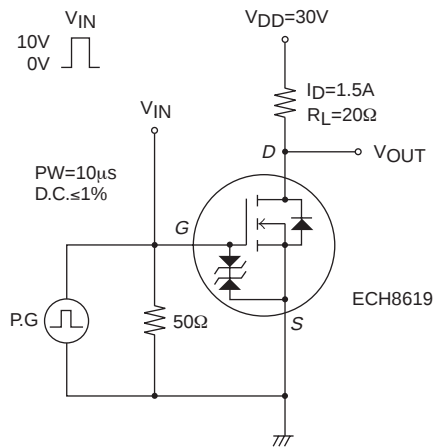


Electrical Connection

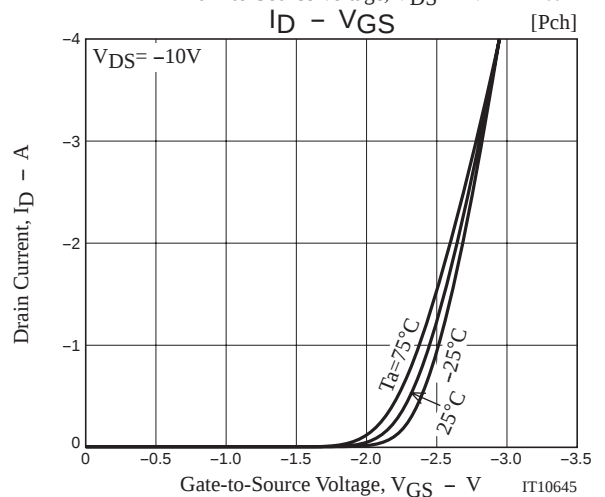
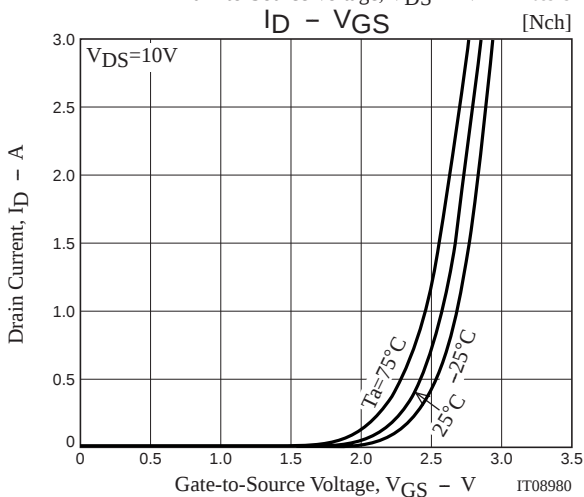
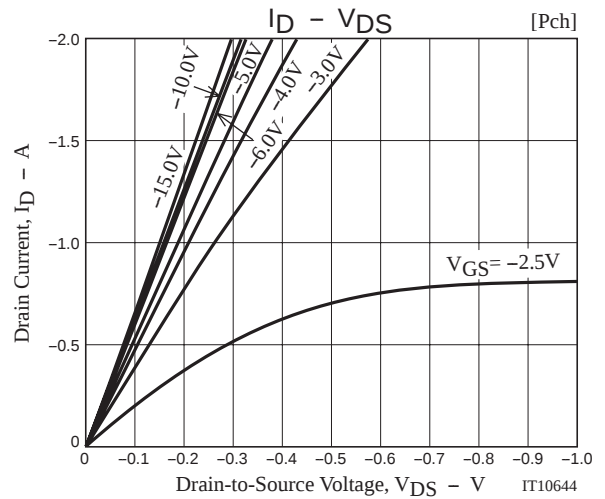
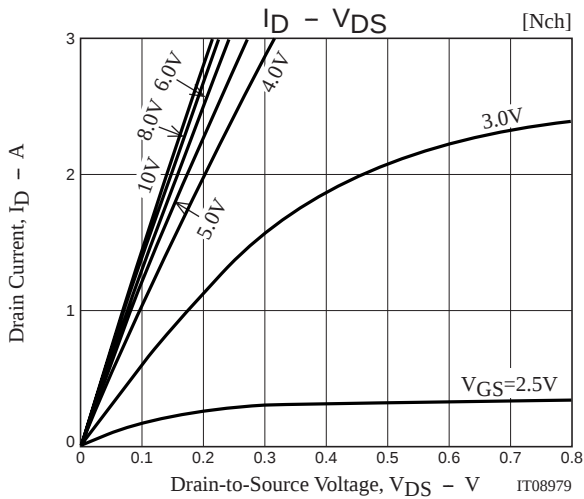
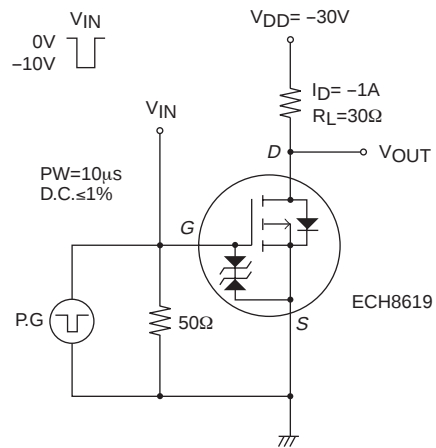


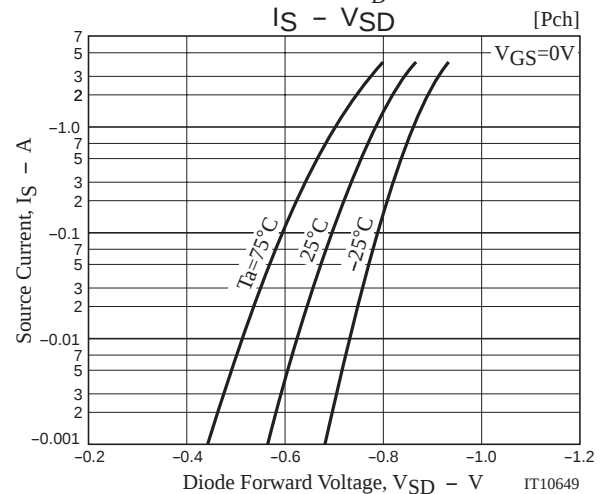
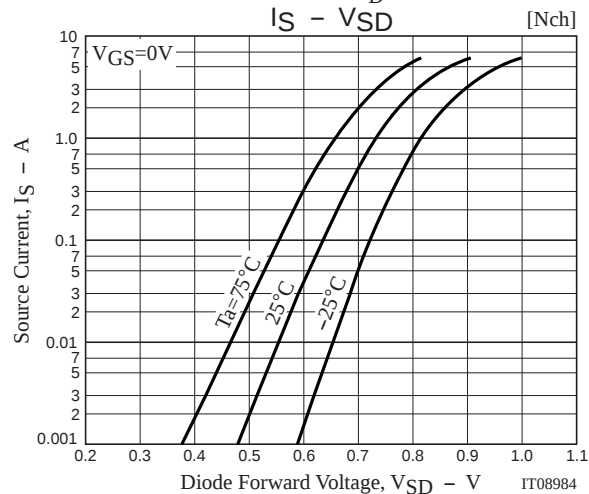
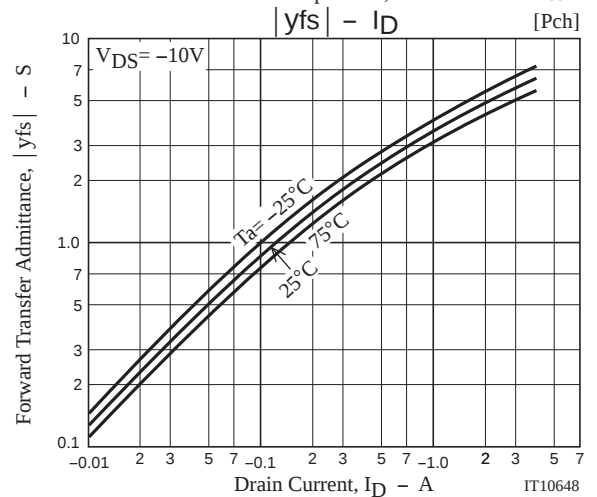
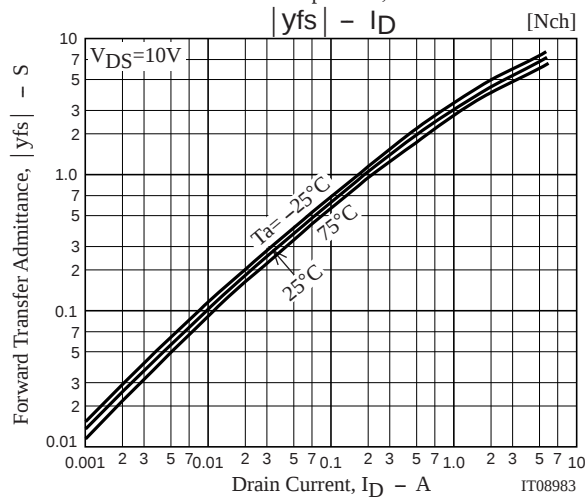
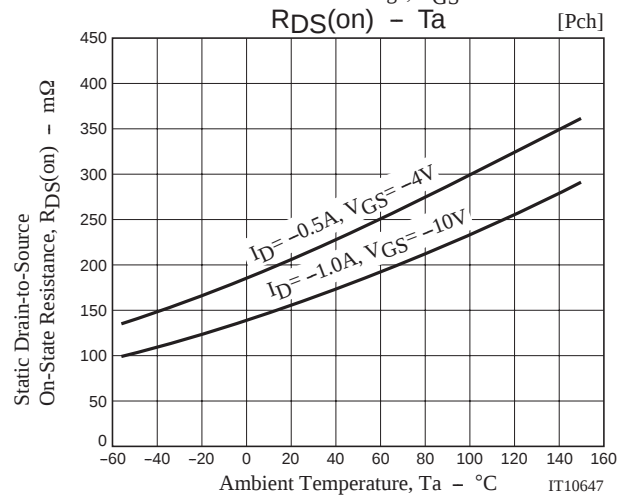
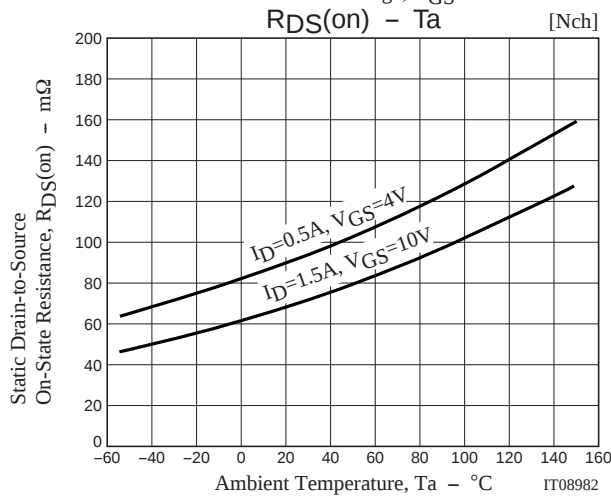
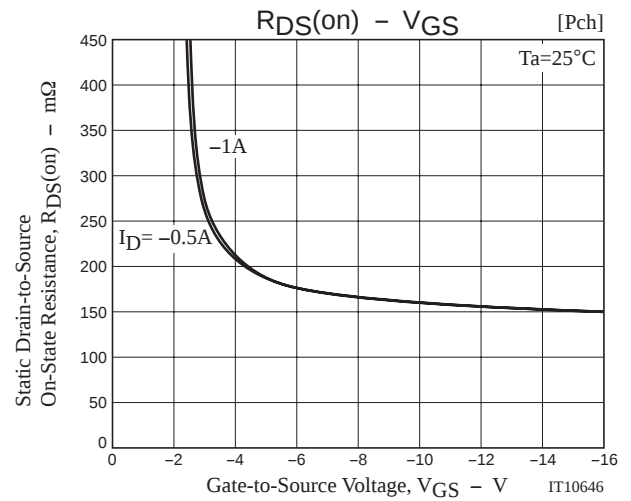
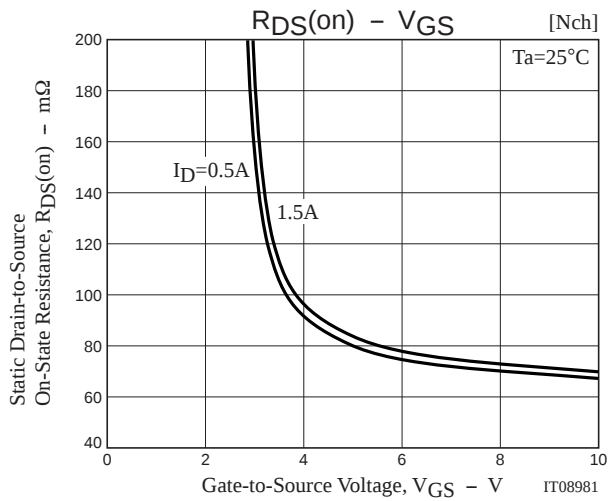
Switching Time Test Circuit

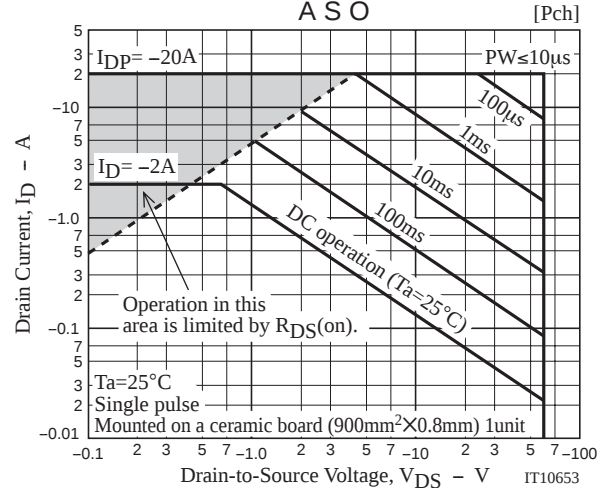
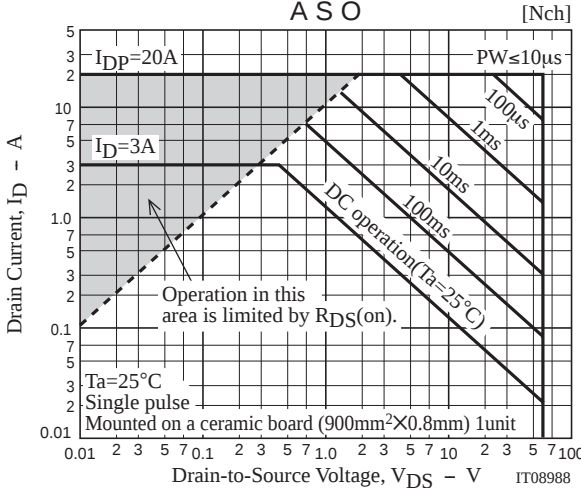
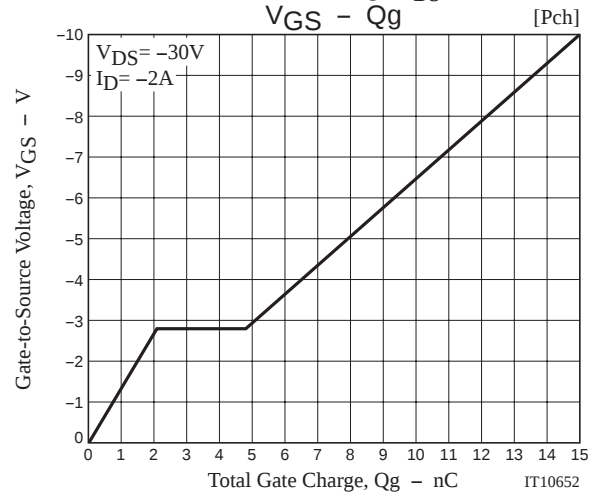
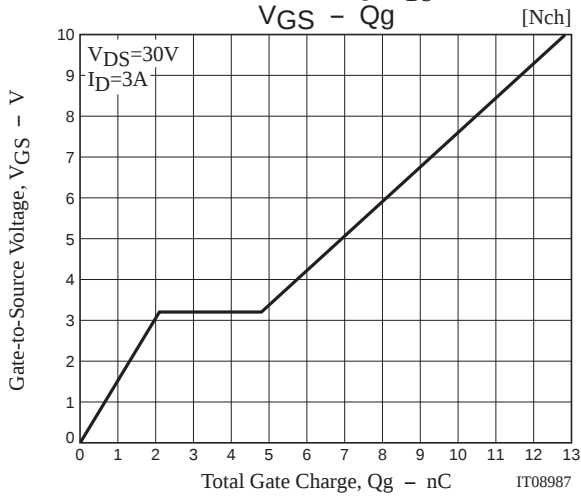
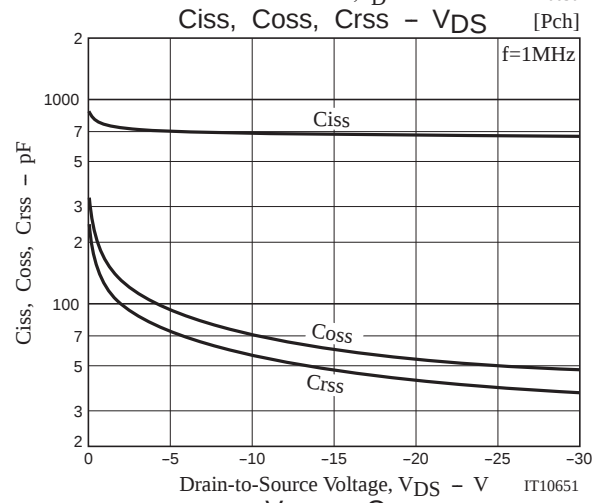
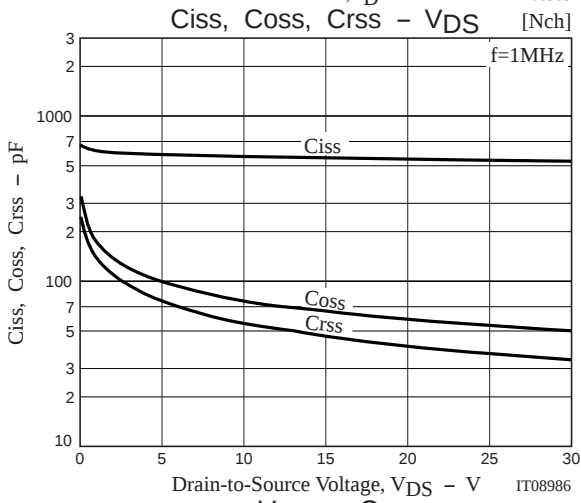
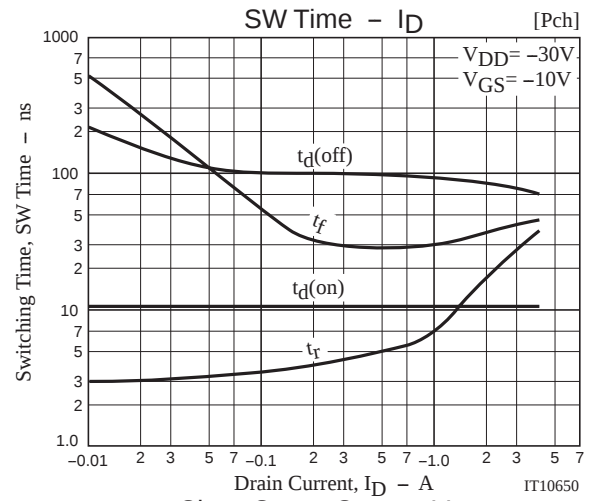
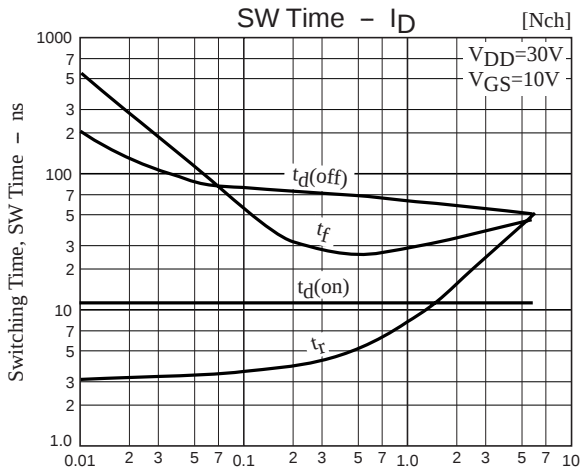
[N-channel]

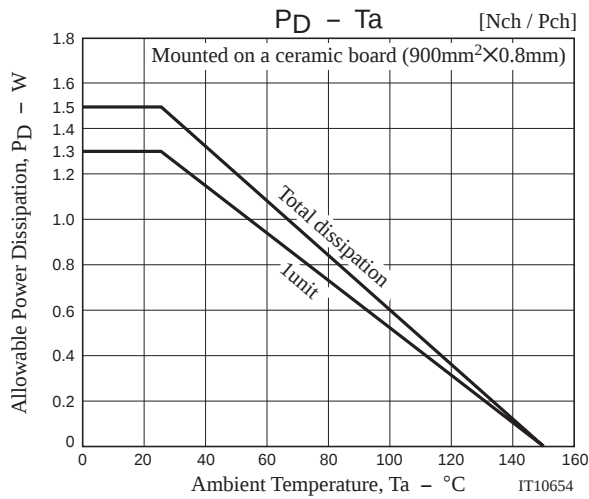


[P-channel]









ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. SCILLC strives to supply high-quality high-reliability products and recommends adopting safety measures when designing equipment to avoid accidents or malfunctions. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals," must be validated for each customer application by customer's technical experts. SCILLC shall not be held liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada.

Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local
Sales Representative