

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current	$T_C = +25$ °C $T_C = +100$ °C	I _D	108 76	А
Maximum Continuous Body Diode Forward Current	T _C = +25°C	I _S	90	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I _{DM}	92	Α
Avalanche Current, L=0.3mH (Note 7)		I _{AS}	10	Α
Avalanche Energy, L=0.3mH (Note 7)		E _{AS}	15	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	Steady State	P _D	2.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	61	°C/W
Total Power Dissipation	T _C = +25°C	P _D	166	W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	0.9	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

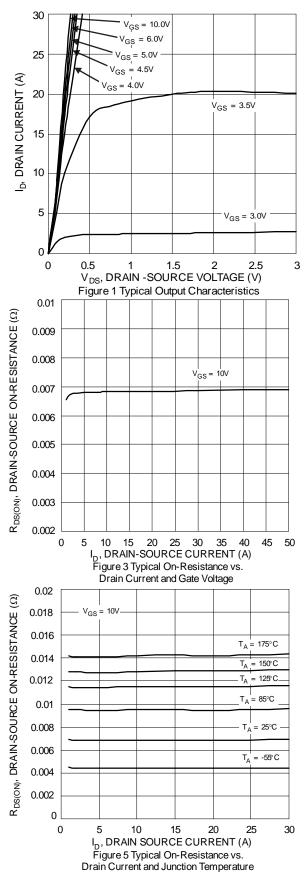
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	1.9	3.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	-	6.9	9.5	mΩ	$V_{GS} = 10V, I_D = 13A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.3	V	$V_{GS} = 0V, I_{S} = 13A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	-	2592	_	pF	$V_{DS} = 50V$, $V_{GS} = 0V$ f = 1MHz	
Output Capacitance	Coss	_	792	_			
Reverse Transfer Capacitance	C _{rss}	_	45	_			
Gate Resistance	R_{G}	_	2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	53.7	_		$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Source Charge	Q _{gs}	_	10.6	_	nC		
Gate-Drain Charge	Q_{gd}	_	8.2	_			
Turn-On Delay Time	t _{D(ON)}	_	11.6	_		$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 13A, R_{G} = 6\Omega$	
Turn-On Rise Time	t _R	_	14.1	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	42.9	_			
Turn-Off Fall Time	t _F	_	22	_			
Reverse Recovery Time	t _{RR}	_	49.8	_	ns	1 404 11/11 4004/	
Reverse Recovery Charge	Q _{RR}	_	85.1	_	$_{\rm nC}$ I _F = 13A, di/dt = 100A/ μ s		

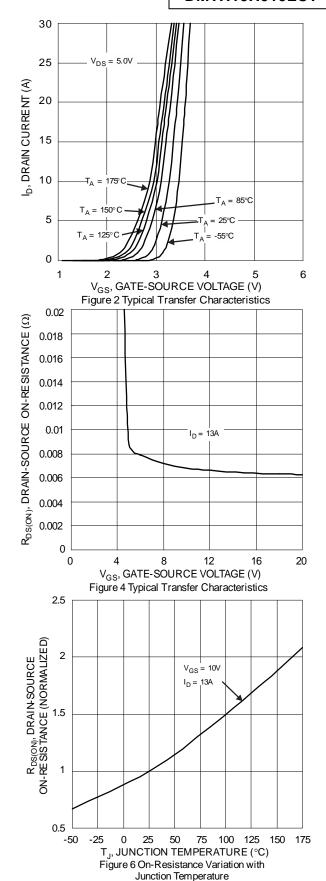
Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to product testing.











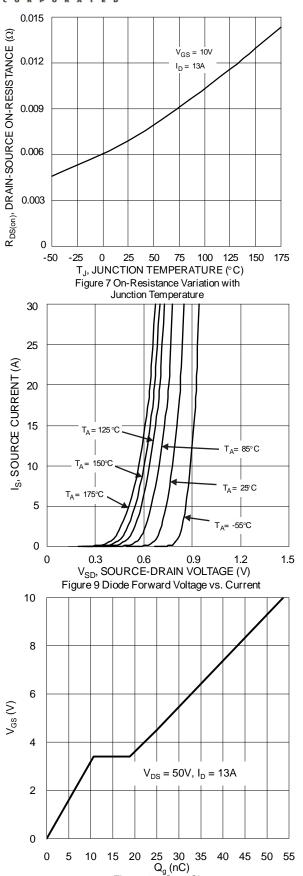
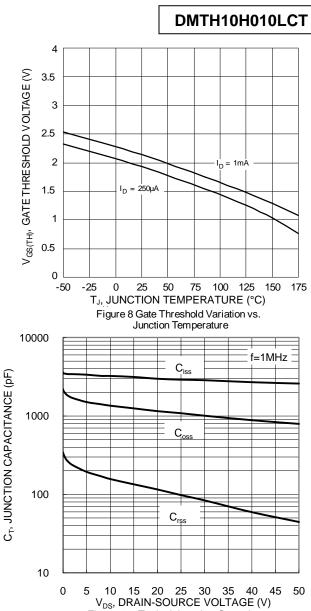
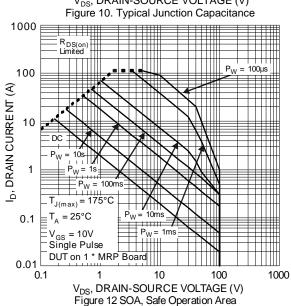
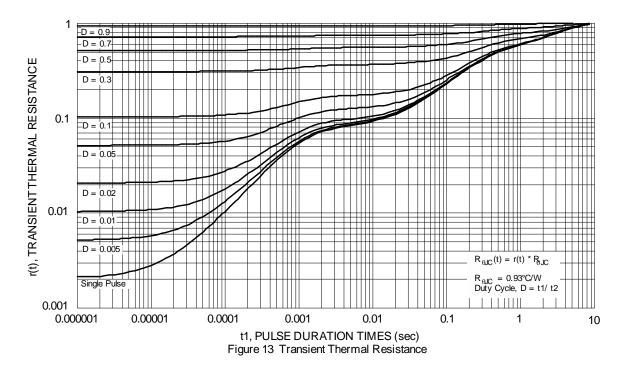


Figure 11. Gate Charge





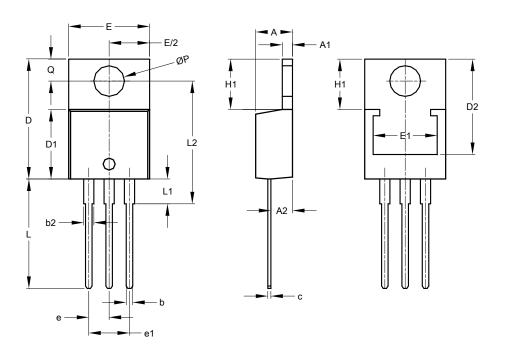




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO220AB



TO220AB					
Dim	Min	Max	Тур		
Α	3.56	4.82	-		
A1	0.51	1.39	-		
A2	2.04	2.92	-		
b	0.39	1.01	0.81		
b2	1.15	1.77	1.24		
С	0.356	0.61	-		
D	14.22	16.51	-		
D1	8.39	9.01	-		
D2	11.45	12.87	-		
е	-	-	2.54		
e1	-	-	5.08		
Ε	9.66	10.66	-		
E1	6.86	8.89	-		
H1	5.85	6.85	-		
L	12.70	14.73	-		
L1	-	6.35	-		
L2	15.80	16.20	16.00		
Р	3.54	4.08	-		
ø	2.54	3.42	-		
All Dimensions in mm					



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