

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	80	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	T _A = +25°C	I _D	9.5	A
	T _A = +70°C		7.6	
	T _C = +25°C	I _D	35	A
	T _C = +70°C		28	
Maximum Continuous Body Diode Forward Current (Note 5)		I _S	2	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)		I _{DM}	80	A

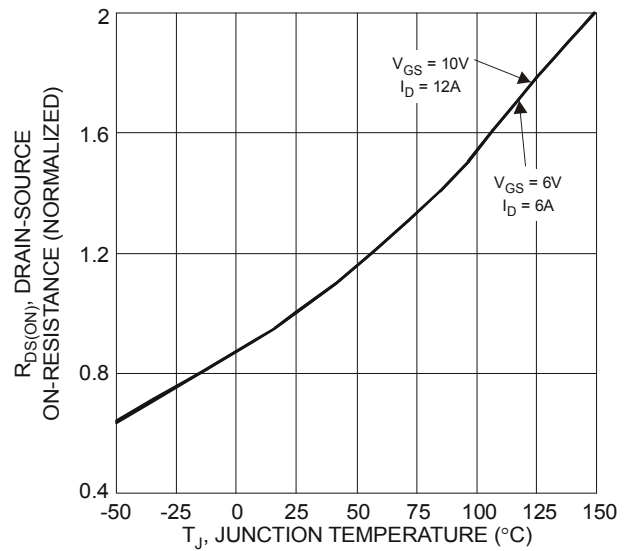
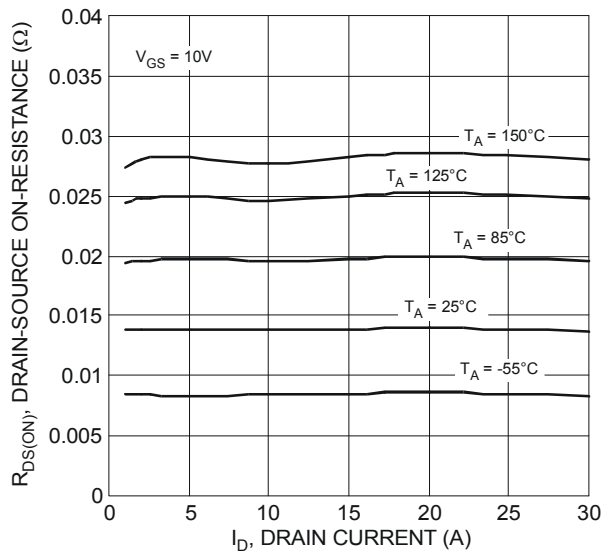
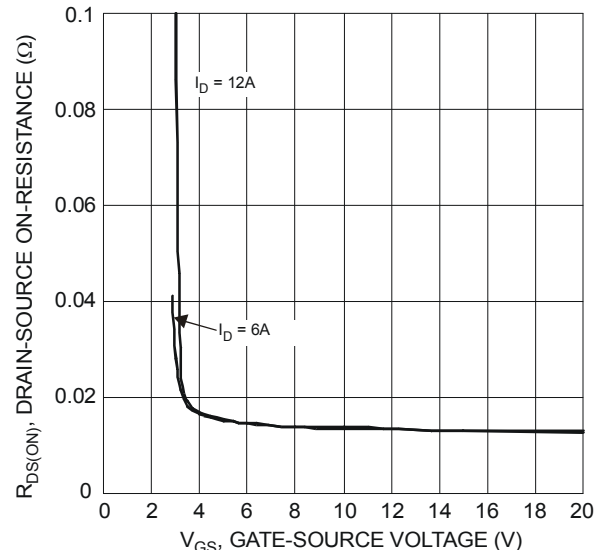
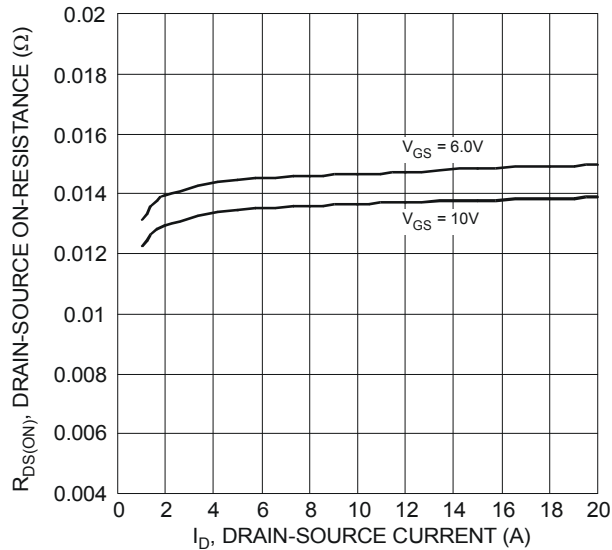
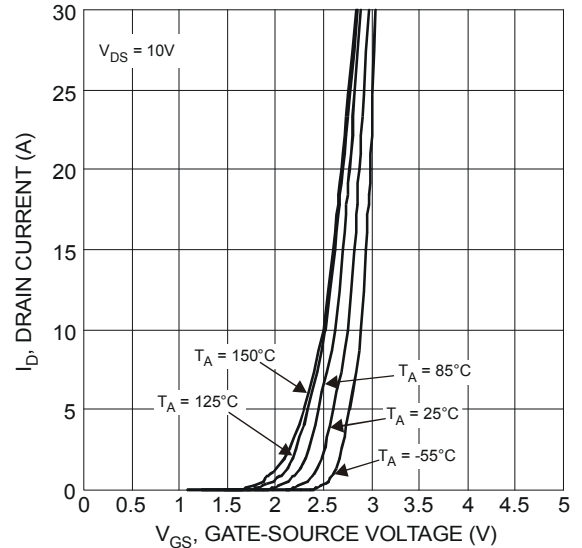
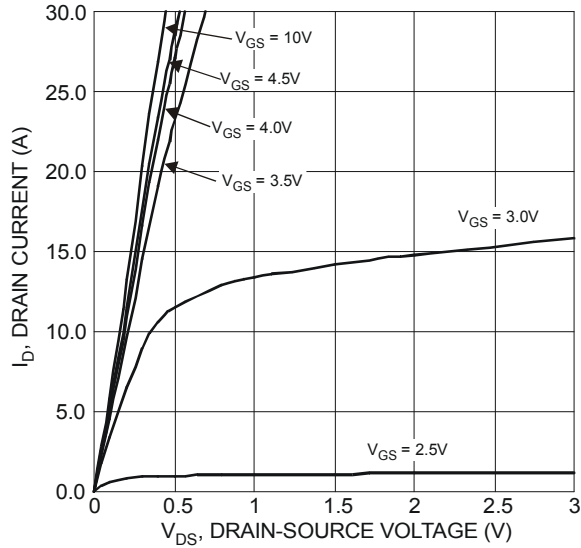
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

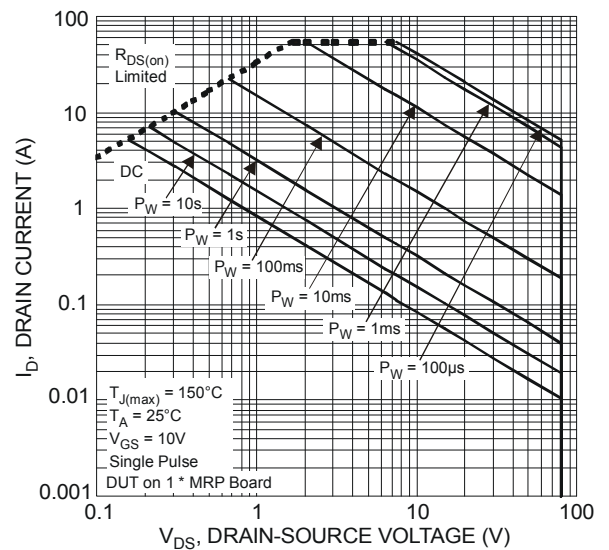
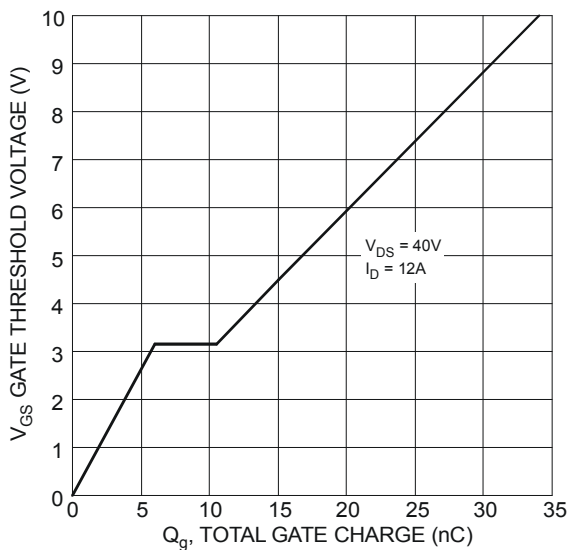
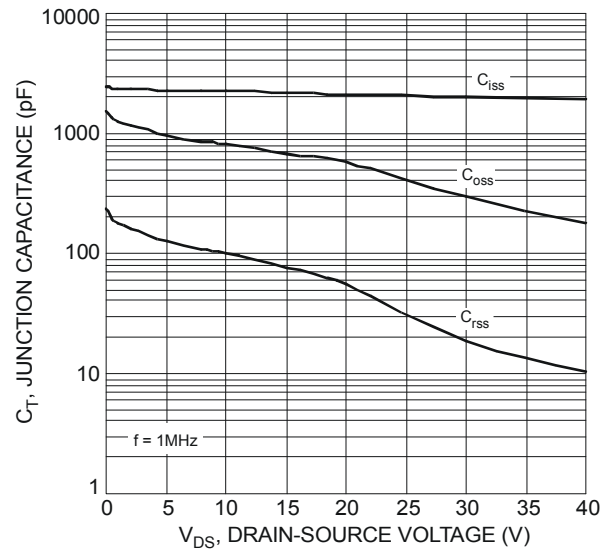
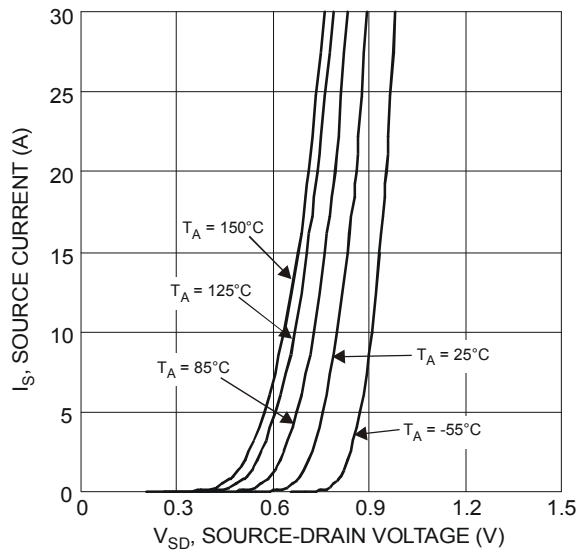
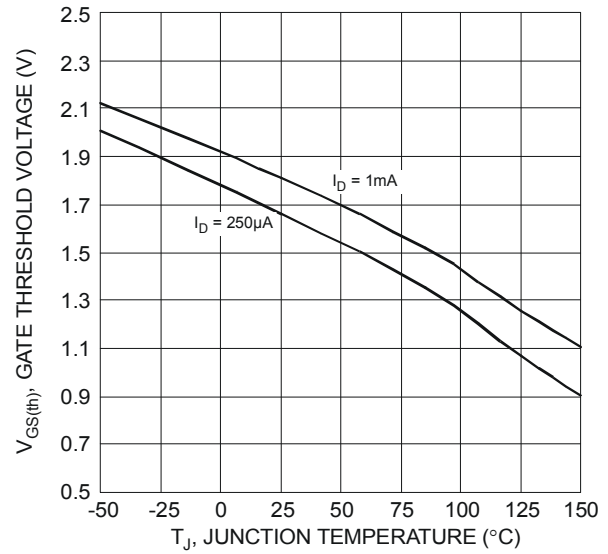
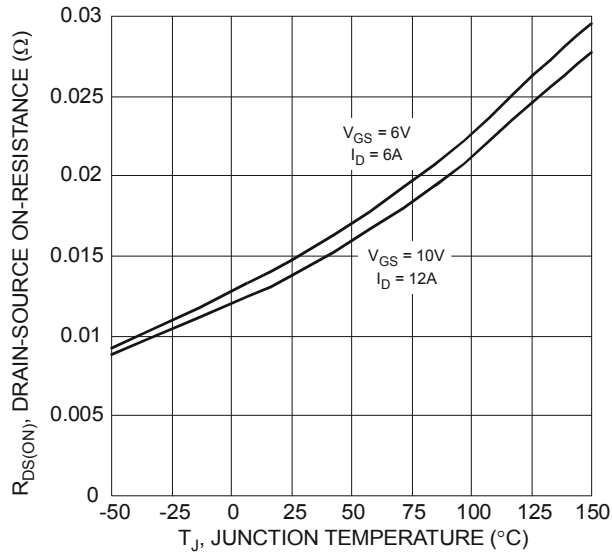
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	2.2	W
	T _C = +25°C		30	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	57	°C/W
	t<10s		35	
Thermal Resistance, Junction to Case (Note 5)		R _{θJC}	4.2	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to 150	

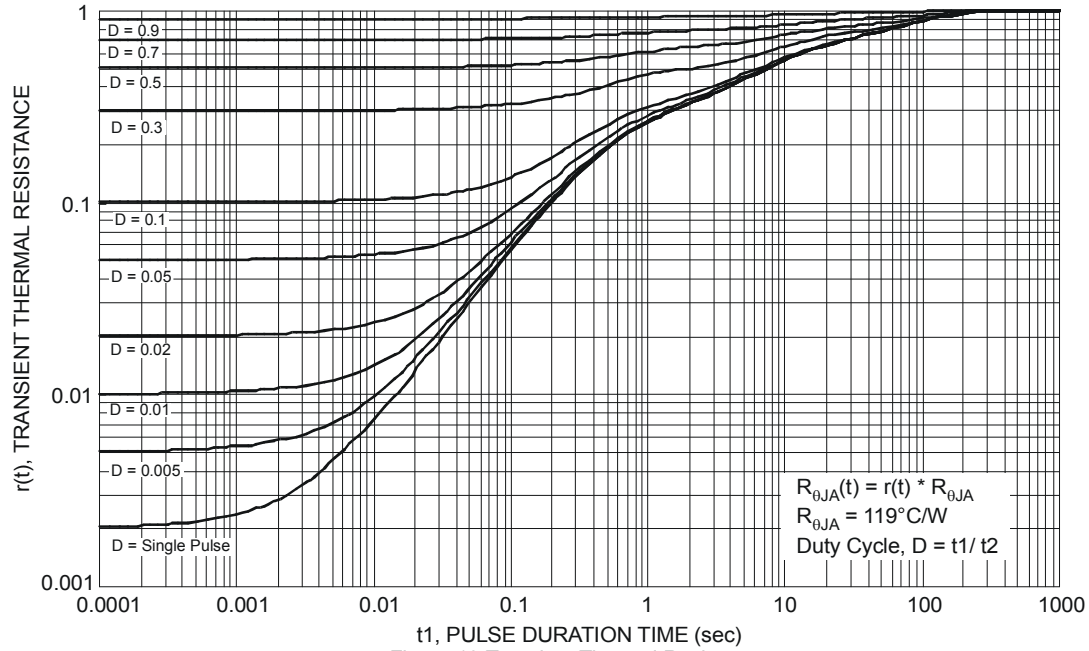
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	80	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 64V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	1	1.5	3	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	13	16	mΩ	V _{GS} = 10V, I _D = 12A
		—	14	22		V _{GS} = 6V, I _D = 6A
Diode Forward Voltage	V _{SD}	—	0.9	1.2	V	V _{GS} = 0V, I _S = 12A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	—	1949	—	pF	V _{DS} = 40V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	177	—		
Reverse Transfer Capacitance	C _{rss}	—	10	—		
Gate resistance	R _g	—	0.7	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	15	—	nC	V _{DS} = 40V, I _D = 12A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	34	—		
Gate-Source Charge	Q _{gs}	—	6	—		
Gate-Drain Charge	Q _{gd}	—	4.5	—		
Turn-On Delay Time	t _{D(on)}	—	4.9	—	nS	V _{DD} = 40V, V _{GS} = 10V, I _D = 12A, R _G = 1.6Ω,
Turn-On Rise Time	t _r	—	3.8	—		
Turn-Off Delay Time	t _{D(off)}	—	16.5	—		
Turn-Off Fall Time	t _f	—	3.5	—		
Body Diode Reverse Recovery Time	t _{rr}	—	30.2	—	nS	I _F = 12A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{rr}	—	34.6	—	nC	

- Notes:
5. R_{θJA} is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to product testing.

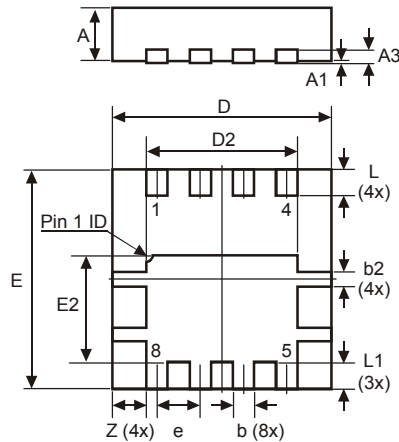






Package Outline Dimensions

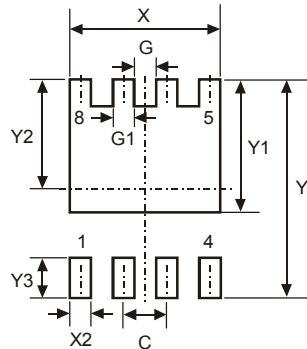
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



POWERDI®3333-8			
Dim	Min	Max	Typ
D	3.25	3.35	3.30
E	3.25	3.35	3.30
D2	2.22	2.32	2.27
E2	1.56	1.66	1.61
A	0.75	0.85	0.80
A1	0	0.05	0.02
A3	—	—	0.203
b	0.27	0.37	0.32
b2	—	—	0.20
L	0.35	0.45	0.40
L1	—	—	0.39
e	—	—	0.65
Z	—	—	0.515
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for latest version.



Dimensions	Value (in mm)
C	0.650
G	0.230
G1	0.420
Y	3.700
Y1	2.250
Y2	1.850
Y3	0.700
X	2.370
X2	0.420

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