

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 7) V _{GS} = 5.0V	Steady State	T _A = +25°C T _A = +70°C	I _D	340 270	mA
	t < 5s	T _A = +25°C T _A = +70°C	I _D	400 300	mA
Maximum Continuous Body Diode Forward Current (Note 7)			I _S	0.4	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 7)			I _{DM}	1.2	A

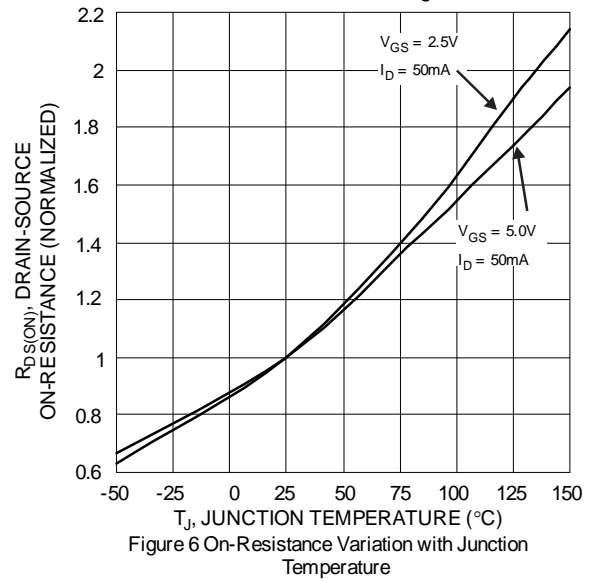
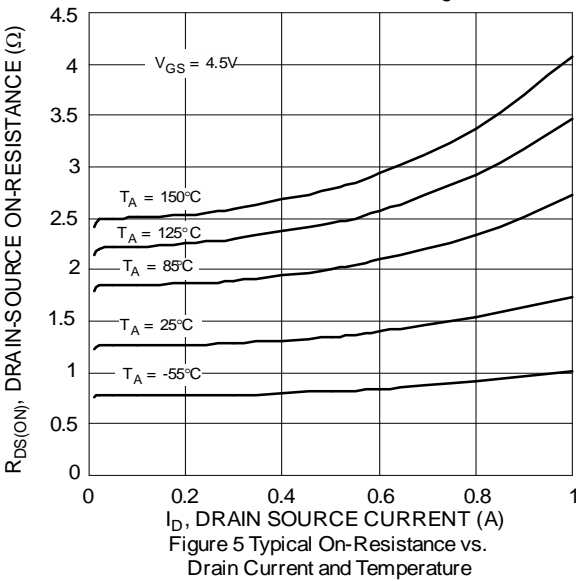
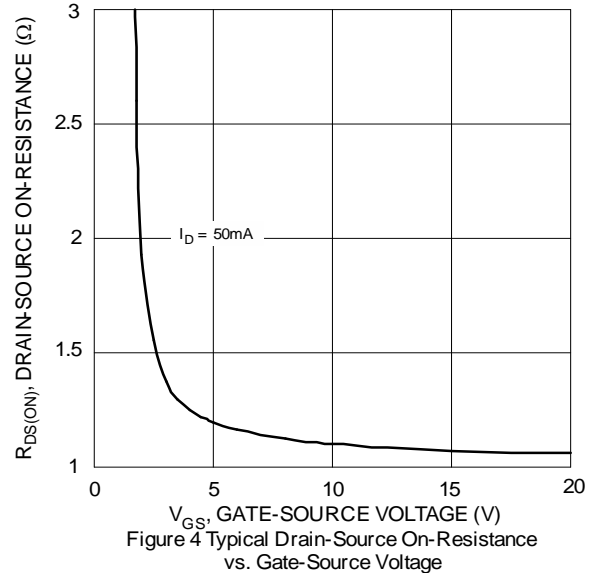
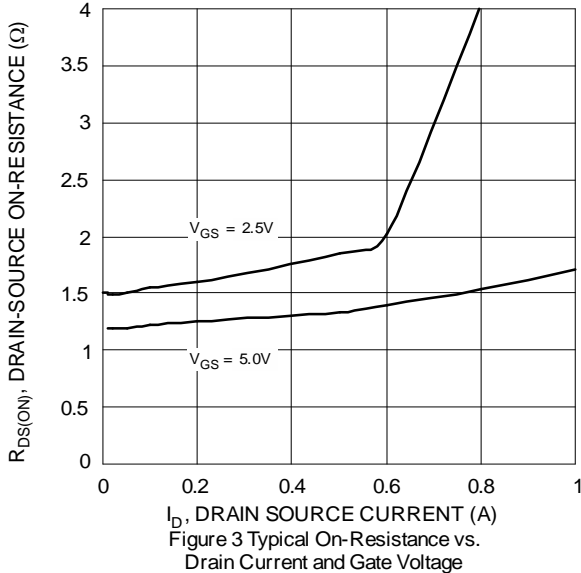
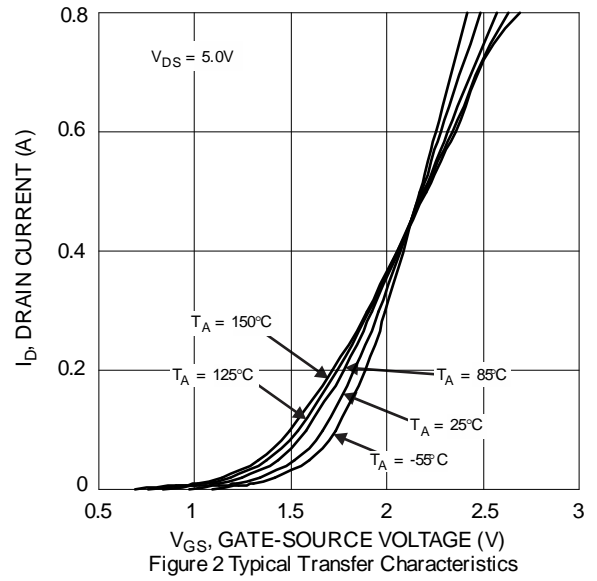
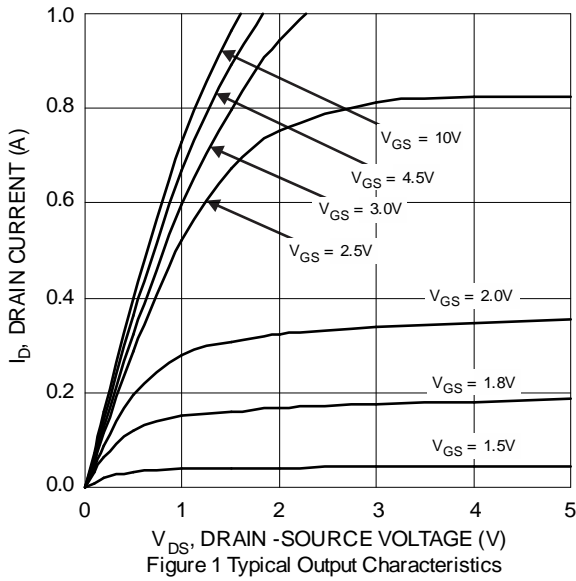
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P _D	320	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	393	°C/W
	t < 5s		306	
Total Power Dissipation (Note 7)		P _D	440	mW
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	R _{θJA}	289	°C/W
	t < 5s		235	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0	µA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	V _{DS} = 10V, I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.2	2.0	Ω	V _{GS} = 5.0V, I _D = 0.05A
			1.6	2.5		V _{GS} = 2.5V, I _D = 0.05A
			2.5	3.5		V _{GS} = 1.8V, I _D = 0.05A
Forward Transconductance	Y _{fs}	200	—	—	mS	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage	V _{SD}	—	0.75	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	28.5	—	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	3.9	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.5	—	pF	
Gate Resistance	R _g	—	65	—	Ω	f = 1MHz, V _{GS} = 0V, V _{DS} = 0V
Total Gate Charge	Q _g	—	0.4	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	0.1	—	nC	
Gate-Drain Charge	Q _{gd}	—	0.1	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	2.1	—	ns	V _{DD} = 30V, V _{GS} = 10V, R _G = 25Ω, I _D = 200mA
Turn-On Rise Time	t _R	—	1.8	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	14.4	—	ns	
Turn-Off Fall Time	t _F	—	8.4	—	ns	

- Notes:
6. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 7. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.



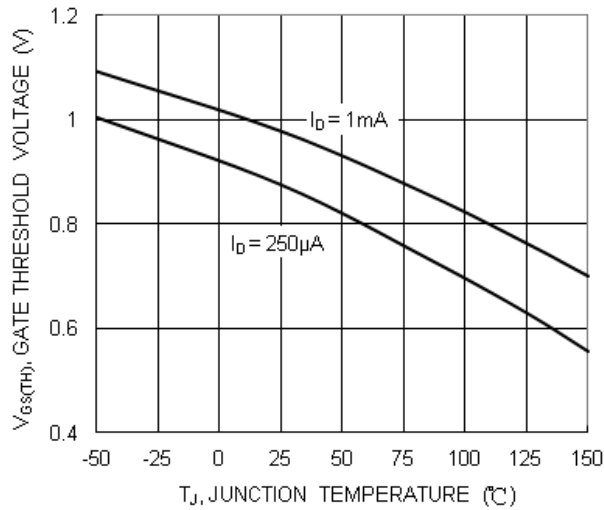


Figure 7. Gate Threshold Variation vs. Junction Temperature

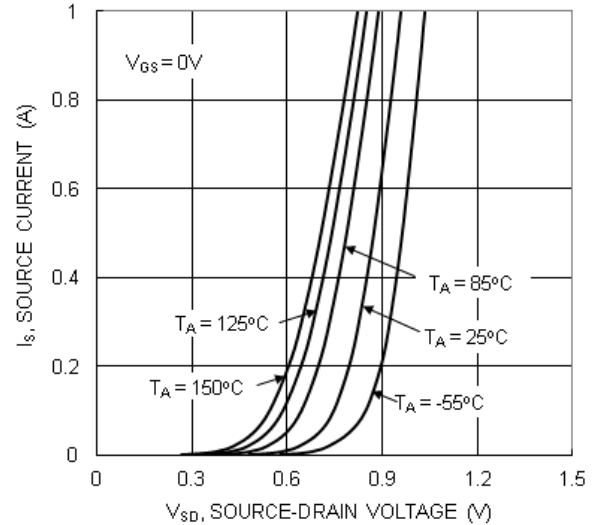


Figure 8. Diode Forward Voltage vs. Current

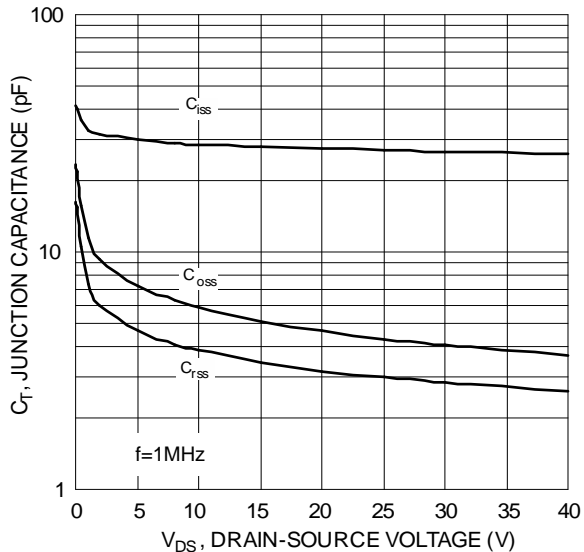


Figure 9 Typical Junction Capacitance

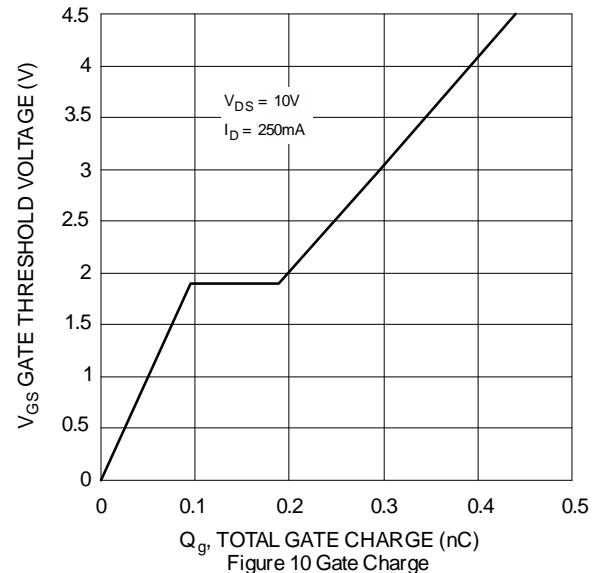


Figure 10 Gate Charge

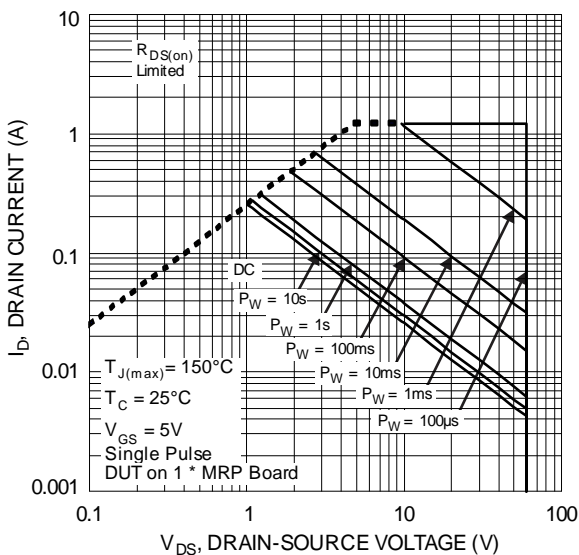
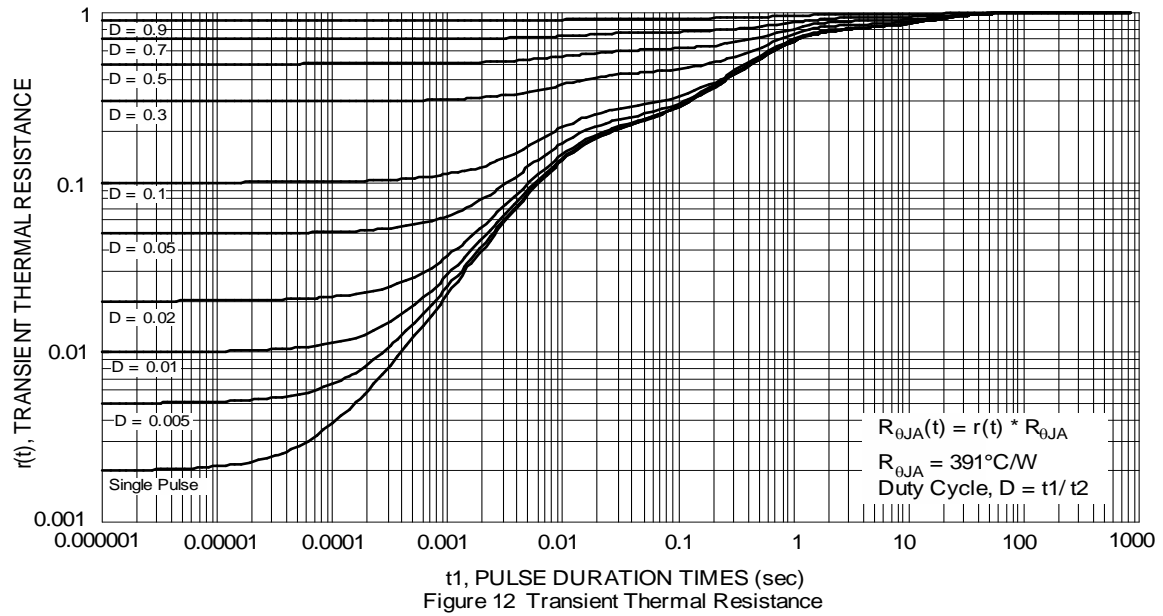
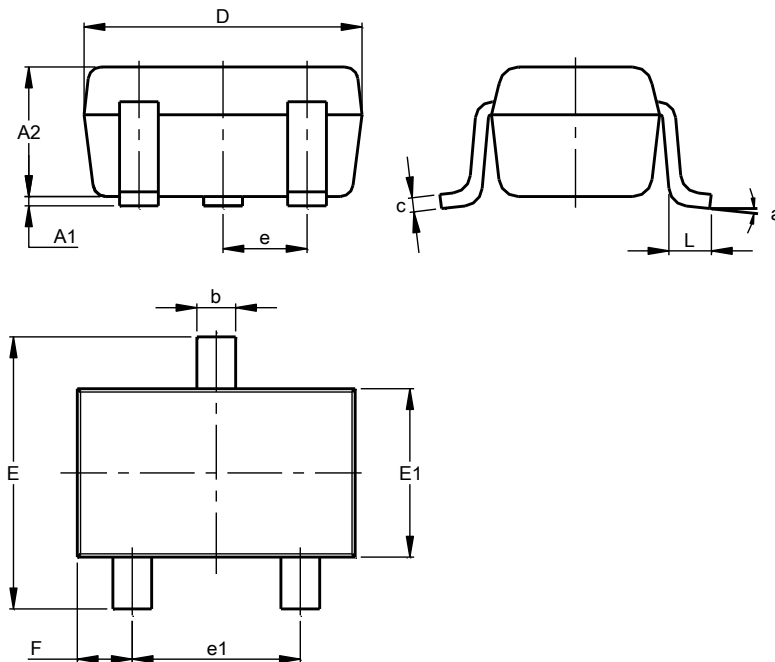


Figure 11 SOA, Safe Operation Area



Package Outline Dimensions

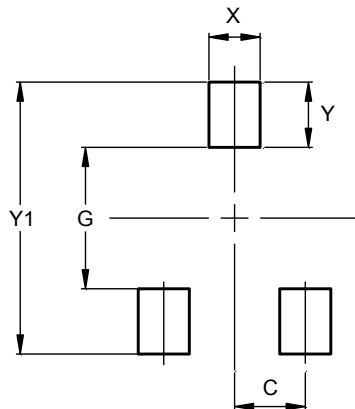
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



SOT323			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.25	0.40	0.30
c	0.10	0.18	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
e1	1.20	1.40	1.30
F	0.375	0.475	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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