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DMN4027SSS

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit	
Drain-Source voltage			V _{DSS}	40	V	
Gate-Source voltage (Note 2)			V _{GS}	±20	V	
		(Note 4)		8.0		
Continuous Drain current	V _{GS} = 10V	$T_A = 70^{\circ}C$ (Note 4)	ID	6.5	А	
		(Note 3)		6.0		
Pulsed Drain current	Ilsed Drain current V _{GS} = 10V (Note 5)		I _{DM}	37	A	
Continuous Source current (Body diode) ((Note 4)	I _S	4.2	A	
Pulsed Source current (Body diode) (Note		(Note 5)	I _{SM}	37	A	

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Power dissipation	(Note 3)	5	1.56 12.5	W	
Linear derating factor	(Note 4)	P _D	2.8 22.5	mW/°C	
Thermal Resistance, Junction to Ambient	(Note 3)	P	80		
mermai Resistance, Junction to Ambient	(Note 4)	R _{0JA}	44.5	°C/W	
Thermal Resistance, Junction to Lead	(Note 6)	R _{0JL}	35		
Operating and storage temperature range	TJ, TSTG	-55 to 150	٥C		

Notes:

AEC-Q101 V_{GS} maximum is ±16V.
For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

4. Same as note (3), except the device is measured at t \leq 10 sec.

5. Same as note (3), except the device is pulsed with D= 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.

6. Thermal resistance from junction to solder-point (at the end of the drain lead).

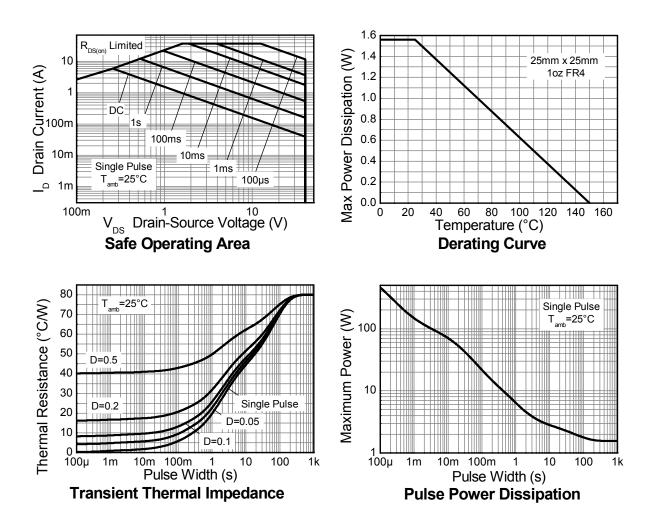


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DMN4027SSS

Thermal Characteristics



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Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV _{DSS}	40	-		V	I _D = 250μA, V _{GS} = 0V		
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μA	V _{DS} = 40V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	V _{GS} = ±20V, V _{DS} = 0V		
ON CHARACTERISTICS								
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	I _D = 250μA, V _{DS} = V _{GS}		
Statia Drain Source On Registence (Note 7)		_	0.017	0.027	Ω	V _{GS} = 10V, I _D = 7A V _{GS} = 4.5V, I _D = 6A		
Static Drain-Source On-Resistance (Note 7)	R _{DS} (ON)		0.031	0.047				
Forward Transconductance (Notes 7 & 8)	g fs	_	22.8	_	S	V _{DS} = 15V, I _D = 7A		
Diode Forward Voltage (Note 7)	V _{SD}	_	0.85	1.1	V	I _S = 7A, V _{GS} = 0V		
Reverse recovery time (Note 8)	t _{rr}		12.2	_	ns			
Reverse recovery charge (Note 8)	Qrr	_	5.4		nC	I _S = 2.5, di/dt= 100A/μs		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C _{iss}	_	604		pF	V _{DS} = 20V, V _{GS} = 0V - f= 1MHz		
Output Capacitance	C _{oss}	_	106		pF			
Reverse Transfer Capacitance	C _{rss}	_	59.6		pF			
Total Gate Charge (Note 9)	Qg	_	6.3		nC	V _{GS} = 4.5V		
Total Gate Charge (Note 9)	Qg	_	12.9		nC	V _{DS} = 20V		
Gate-Source Charge (Note 9)	Q _{gs}	_	2.4		nC	V _{GS} = 10V	I _D = 7A	
Gate-Drain Charge (Note 9)	Q _{gd}	_	3		nC			
Turn-On Delay Time (Note 9)	t _{D(on)}		3.1		ns			
Turn-On Rise Time (Note 9)	tr	_	3.1		ns	V _{DD} = 20V, V _{GS} = 10V I _D = 1A, R _G ≅ 6.0Ω		
Turn-Off Delay Time (Note 9)	t _{D(off)}		15.4		ns			
Turn-Off Fall Time (Note 9)	t _f	_	7.5		ns	1		

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.

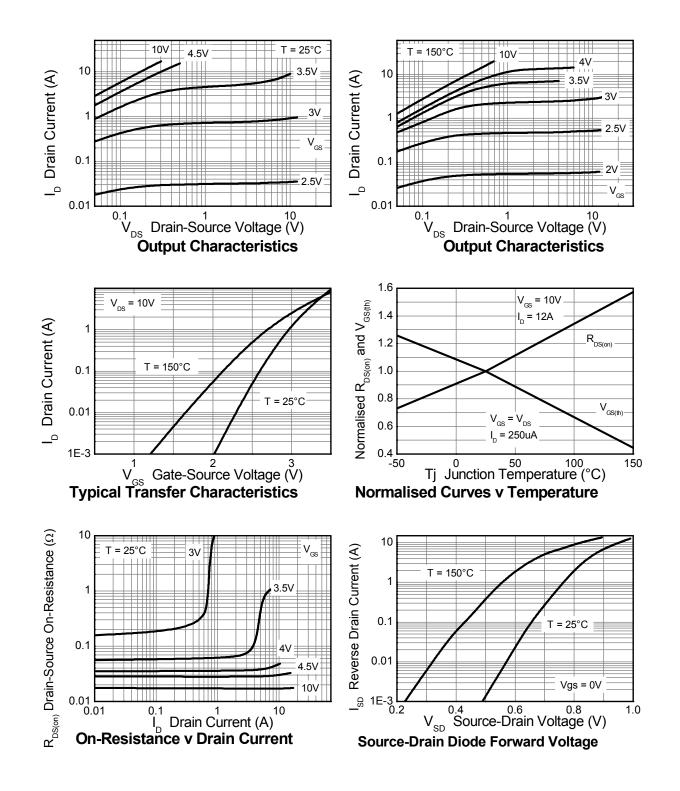


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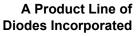
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Typical Characteristics



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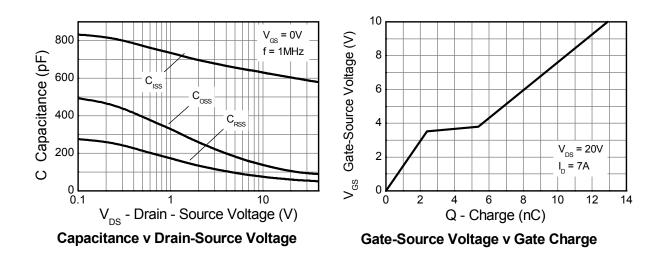




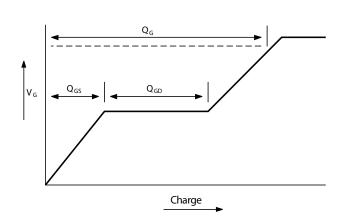


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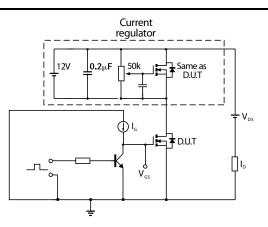
Typical Characteristics - continued



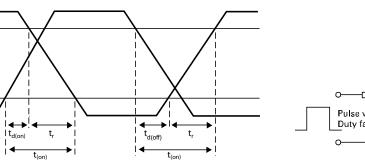
Test Circuits



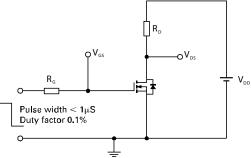
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms



Switching time test circuit

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V_{DS} 90%

10% V_{GS}

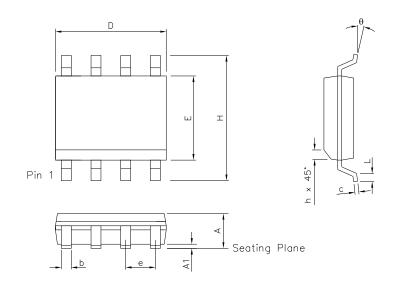


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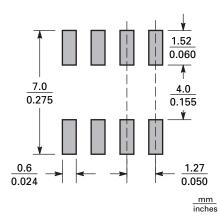
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Package Outline Dimensions



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
А	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	с	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Suggested Pad Layout





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