

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	40	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _C = +25°C T _C = +70°C	I _D	39 31	A
	Steady State	T _A = +25°C T _A = +70°C	I _D	11.9 9.5	A
Maximum Body Diode Forward Current (Note 6)			I _S	2	A
Pulsed Drain Current (10μs pulse, Duty cycle = 1%)			I _{DM}	80	A
Avalanche Current (Notes 7) L = 0.1mH			I _{AS}	27	A
Avalanche Energy (Notes 7) L = 0.1mH			E _{AS}	37	mJ

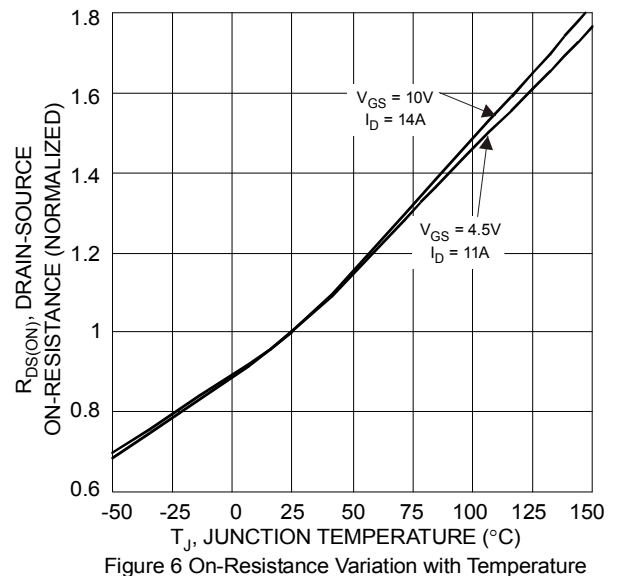
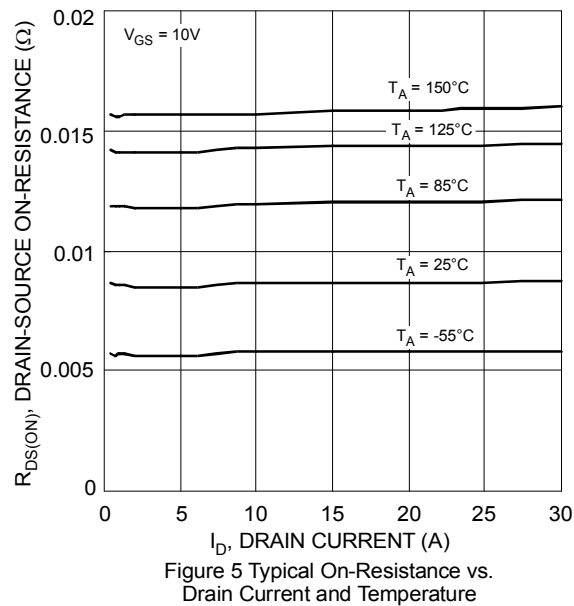
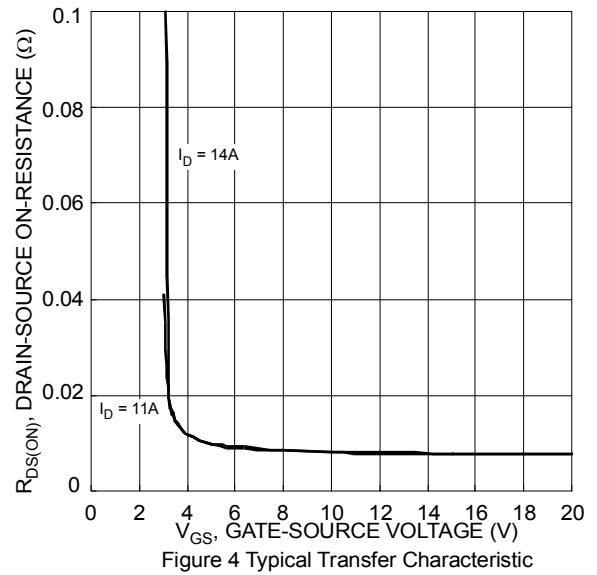
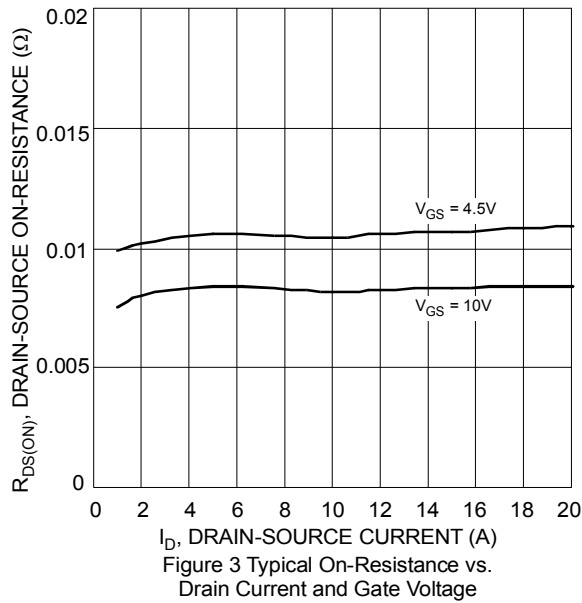
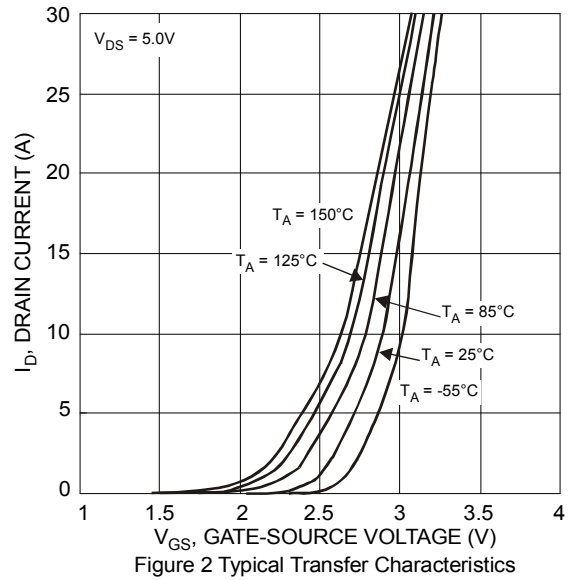
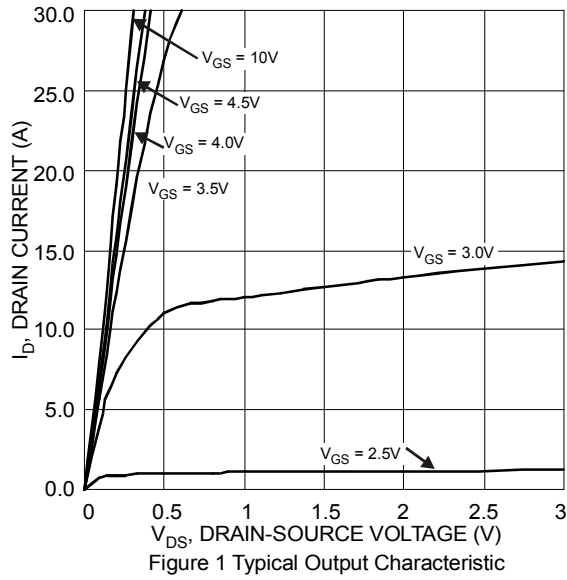
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

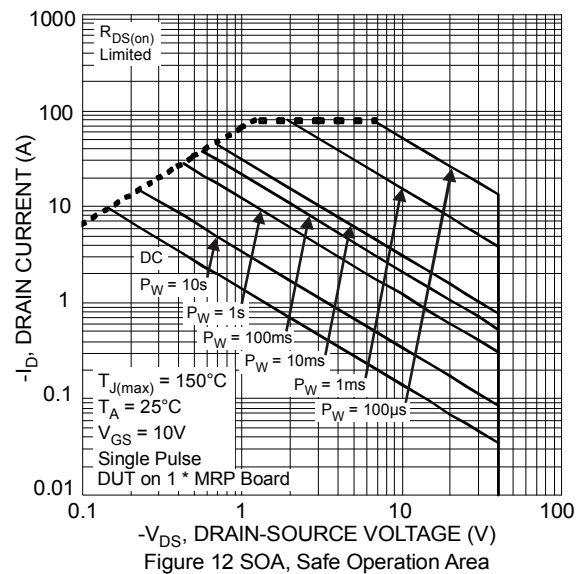
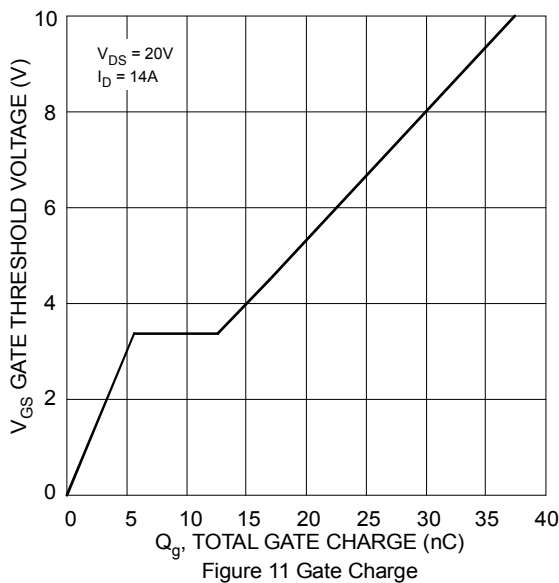
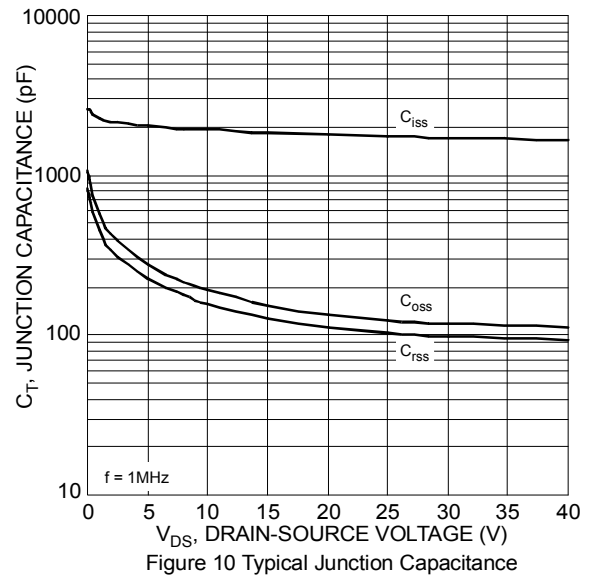
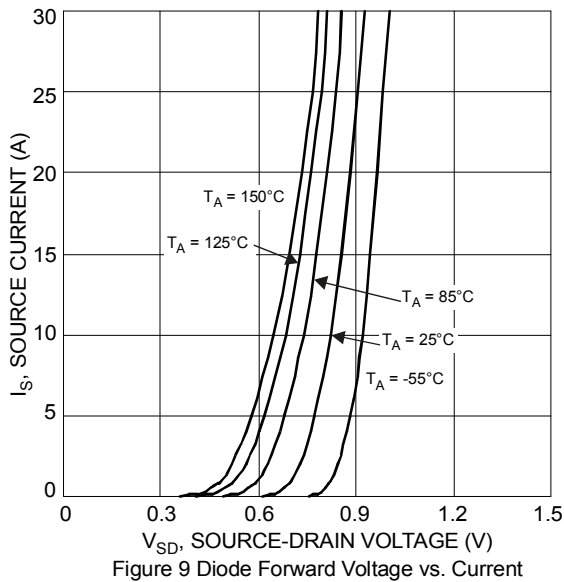
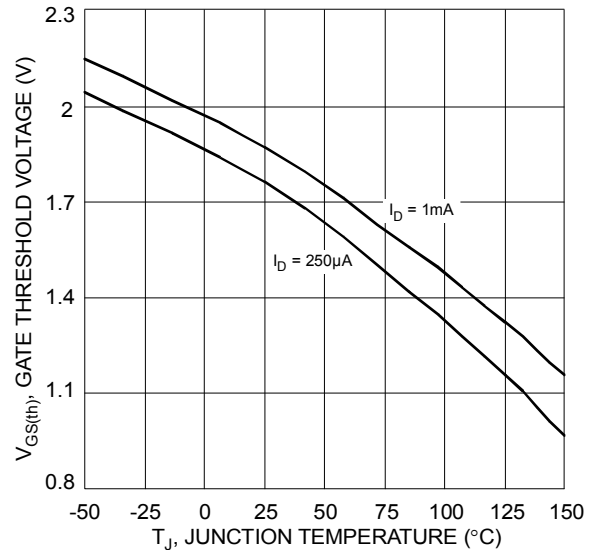
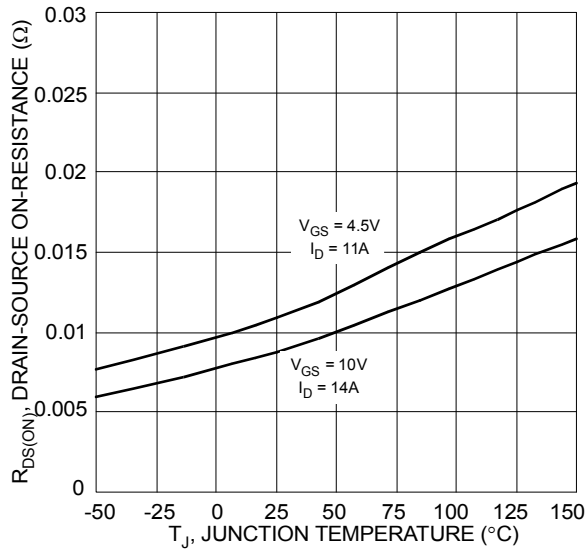
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P _D	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R _{θJA}	78	°C/W
	t < 10s		31	
Total Power Dissipation (Note 6)		P _D	2.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R _{θJA}	51	°C/W
	t < 10s		21	
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	4.7	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	μA	V _{DS} = 32V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	—	3.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	8.7	11.5	mΩ	V _{GS} = 10V, I _D = 14A
		—	11.1	14.5		V _{GS} = 4.5V, I _D = 11A
Diode Forward Voltage	V _{SD}	—	0.72	—	V	V _{GS} = 0V, I _S = 14A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	1810	—	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	135	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	112	—	pF	
Gate Resistance	R _g	—	1.7	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	17	—	nC	V _{DS} = 20V, , I _D = 14A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	37	—	nC	
Gate-Source Charge	Q _{gs}	—	5.6	—	nC	
Gate-Drain Charge	Q _{gd}	—	7.1	—	nC	
Turn-On Delay Time	t _{D(on)}	—	5.1	—	ns	V _{GS} = 10V, V _{DS} = 20V, R _G = 6Ω, I _D = 14A
Turn-On Rise Time	t _r	—	13	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	36	—	ns	
Turn-Off Fall Time	t _f	—	13	—	ns	
Body Diode Reverse Recovery Time	t _{rr}	—	12.2	—	ns	I _S = 3A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{rr}	—	5.4	—	nC	I _S = 3A, dI/dt = 100A/μs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = 25°C
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.





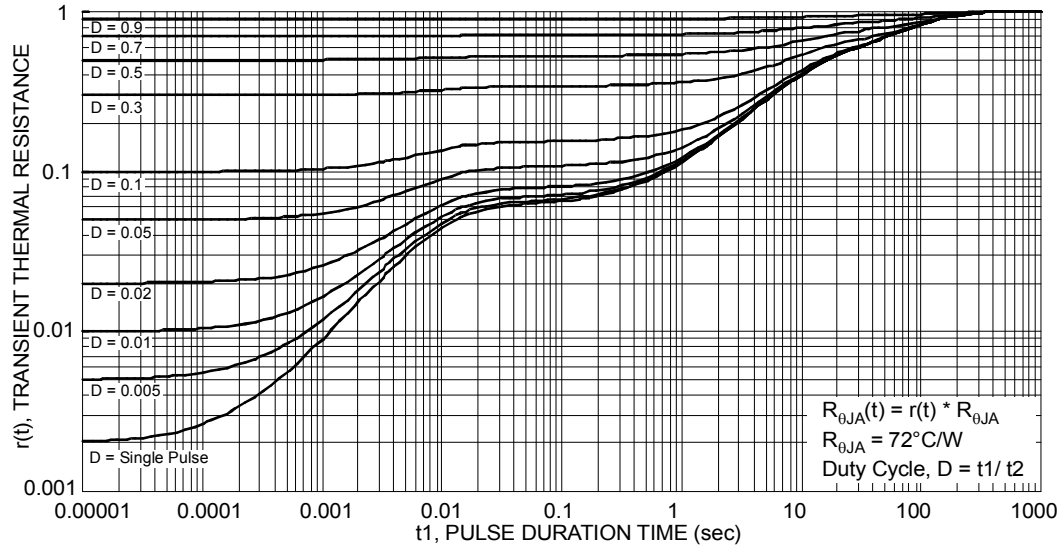
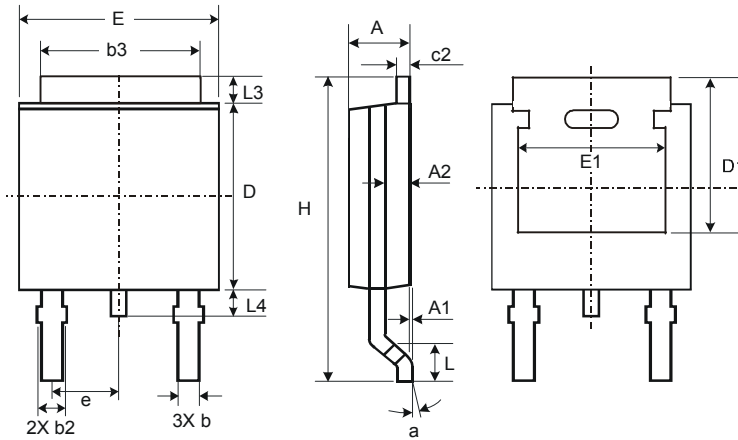


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

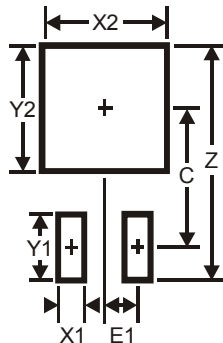
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



TO252			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b1	0.76	1.14	0.95
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c2	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	—	—
e	—	—	2.286
E	6.45	6.70	6.58
E1	4.32	—	—
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	—
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
C	6.9
E1	2.3

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