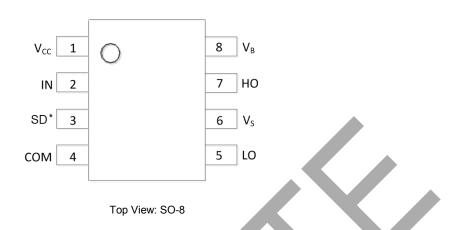


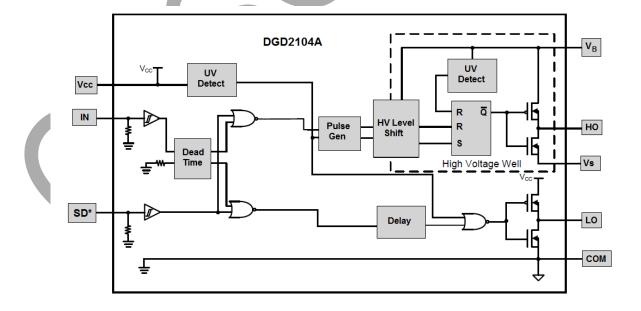
Pin Diagrams



Pin Descriptions

Pin Number	Pin Name	Function
1	Vcc	Logic and Low Side Supply
2	IN	Logic Input for High-Side and Low-Side Gate Driver Outputs (HO and LO), in Phase with HO
3	SD*	Logic Input for Shutdown, Enabled Low
4	COM	Low-Side and Logic Return
5	LO	Low-Side Gate Drive Output
6	Vs	High-Side Floating Supply Return
7	HO	High-Side Gate Drive Output
8	VB	High-Side Floating Supply

Functional Block Diagram





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
High-Side Floating Supply Voltage	VB	-0.3 to +624	V
High-Side Floating Supply Offset Voltage	Vs	V _B -24 to V _B +0.3	V
High-Side Floating Output Voltage	V _{HO}	V _S -0.3 to V _B +0.3	V
Offset Supply Voltage Transient	dV _S / dt	50	V/ns
Low-Side Fixed Supply Voltage	V _{CC}	-0.3 to +24	V
Low-Side Output Voltage	V _{LO}	-0.3 to V _{CC} +0.3	V
Logic Input Voltage (IN and SD*)	V _{IN}	-0.3 to V _{CC} +0.3	V

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor (Note 5)	PD	0.625	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	200	°C/W
Operating Temperature	TJ	+150	
Lead Temperature (Soldering, 10s)	TL	+300	°C
Storage Temperature Range	T _{STG}	-55 to +150	

Note: 5. When mounted on a standard JEDEC 2-layer FR-4 board.

Recommended Operating Conditions

Parameter	Symbol	Min	Мах	Unit
High Side Floating Supply Absolute Voltage	VB	V _S + 10	V _S + 20	V
High Side Floating Supply Offset Voltage	Vs	(Note 6)	600	V
High Side Floating Output Voltage	V _{HO}	Vs	VB	V
Low Side Fixed Supply Voltage	Vcc	10	20	V
Low Side Output Voltage	V _{LO}	0	V _{CC}	V
Logic Input Voltage (IN and SD*)	VIN	0	5	V
Ambient Temperature	T _A	-40	+125	°C

Note: 6. Logic operation for V_S of -5V to +600V.





DC Electrical Characteristics (V_{BIAS} (V_{CC}, V_{BS}) = 15V, @T_A = +25°C, unless otherwise specified.) (Note 7)

Parameter	Symbol	Min	Тур	Max	Unit	Condition
Logic "1" (IN) & Logic "0" (SD*) Input Voltage	VIH	2.5	-	-	V	V _{CC} = 10V to 20V
Logic "0" (IN) & Logic "1" (SD*) Input Voltage	VIL	-	-	0.8	V	V _{CC} = 10V to 20V
High Level Output Voltage, V _{BIAS} - V _O	V _{OH}	-	0.05	0.2	V	I _O = 2mA
Low Level Output Voltage, V _O	V _{OL}	-	0.02	0.1	V	I _O = 2mA
Offset Supply Leakage Current	I _{LK}	-	-	50	μA	$V_{B} = V_{S} = 600V$
Quiescent V _{BS} Supply Current	IBSQ	-	30	55	μA	V _{IN} = 0V or 5V
Quiescent V _{CC} Supply Current	Iccq	-	370	500	μA	V _{IN} = 0V or 5V
Logic "1" Input Bias Current	I _{IN+}	-	3	10	μA	V _{IN} = 5V, SD* = 0V
Logic "0" Input Bias Current	I _{IN-}	-	-	5	μA	V _{IN} = 0V, SD* = 5V
V _{CC} Supply Under-Voltage Positive Going Threshold	V _{CCUV+}	8.0	8.9	9.8	V	-
V _{CC} Supply Under-Voltage Negative Going Threshold	V _{CCUV-}	7.4	8.2	9.0	V	-
Output High Short Circuit Pulsed Current	I _{O+}	130	210	-	mA	V _O = 0V, PW ≤ 10µs
Output Low Short Circuit Pulsed Current	I _{O-}	270	360	_	mA	V _O = 15V, PW ≤ 10µs

Note: 7. The V_{IN} and I_{IN} parameters are applicable to the two logic input pins: IN and SD*. The V_O and I_O parameters are applicable to the respective output pins: HO and LO.

AC Electrical Characteristics (V_{BIAS} (V_{CC}, V_{BS}) = 15V, C_L = 1000pF, @T_A = +25°C, unless otherwise specified.)

Parameter	Symbol	Min	Тур	Max	Unit	Condition
Turn-On Propagation Delay	ton	-	680	820	ns	$V_{\rm S} = 0V$
Turn-Off Propagation Delay	toff	-	150	220	ns	V _S = 600V
Shutdown Propagation Delay	t _{SD}	-	160	220	ns	-
Delay Matching, HO & LO Turn-On / Turn-Off	tом	-	-	60	ns	—
Turn-On Rise Time	t _R	-	100	170	ns	$V_{\rm S} = 0V$
Turn-Off Fall Time	t⊧		50	60	ns	$V_{\rm S} = 0V$
Deadtime: t _{DT LO-HO} & t _{DT HO-LO}	t _{DT}	400	520	650	ns	-





Timing Waveforms

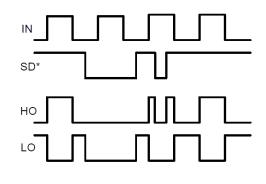


Figure 1. Input / Output Timing Diagram

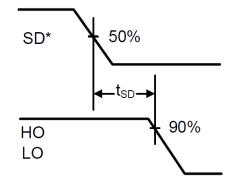
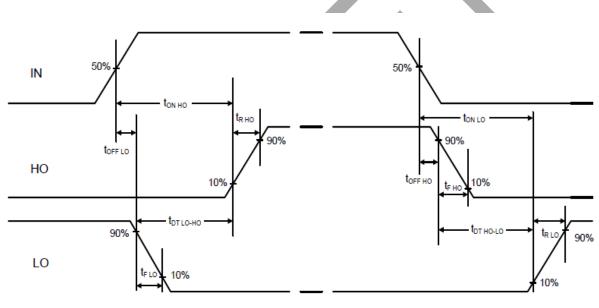


Figure 2. Shutdown Waveform Definition



 $\begin{array}{l} \text{Deadtime } t_{\text{DT LO-HO}} = t_{\text{ON HO}} \text{-} t_{\text{OFF LO}} \\ t_{\text{DT HO-LO}} = t_{\text{ON LO}} \text{-} t_{\text{OFF HO}} \end{array}$

Deadtime matching t_{MDT} = t_{DT LO-HO} - t_{DT HO-LO} $\begin{array}{l} \text{Delay matching} \\ t_{\text{DM OFF}} = t_{\text{OFF LO}} - t_{\text{OFF HO}} \\ t_{\text{DM ON}} = t_{\text{ON LO}} - t_{\text{ON HO}} \end{array}$





Typical Performance Characteristics (@T_A = +25°C, unless otherwise specified.)

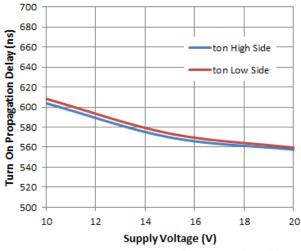


Figure 4. Turn-on Propagation Delay vs. Supply Voltage

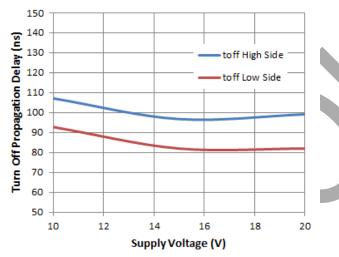


Figure 6. Turn-off Propagation Delay vs. Supply Voltage

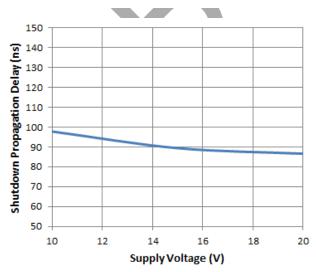


Figure 8. Shutdown Propagation Delay vs. Supply Voltage

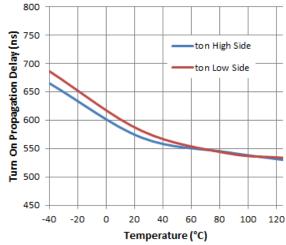


Figure 5. Turn-on Propagation Delay vs. Temperature

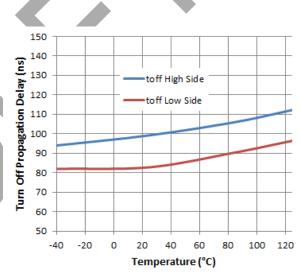


Figure 7. Turn-off Propagation Delay vs. Temperature

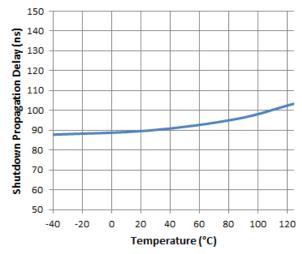
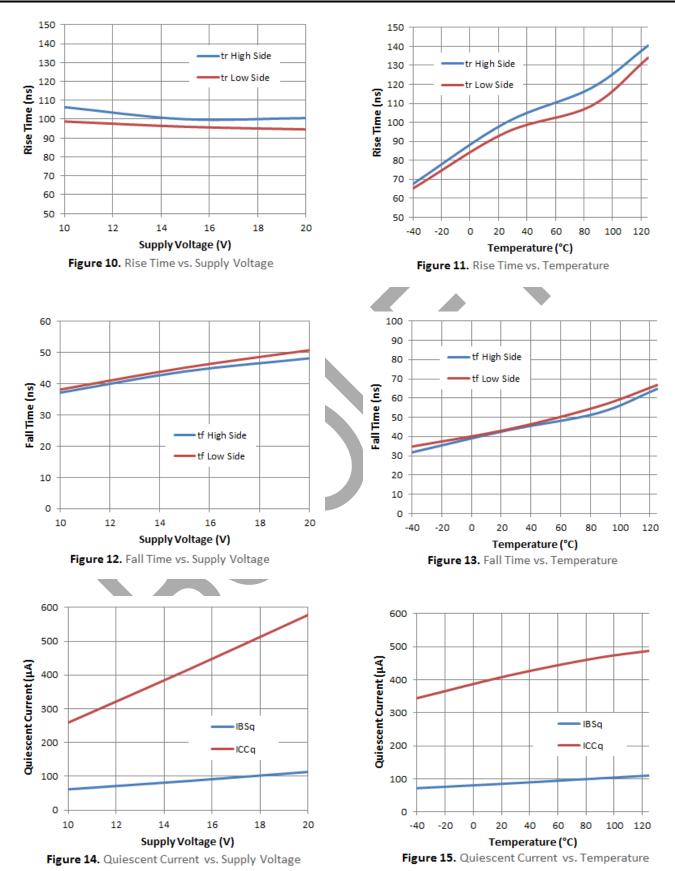
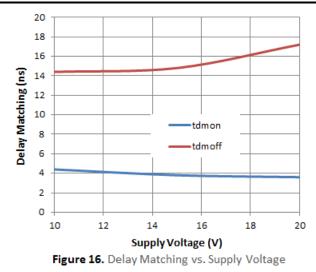


Figure 9. Shutdown Propagation Delay vs. Temperature









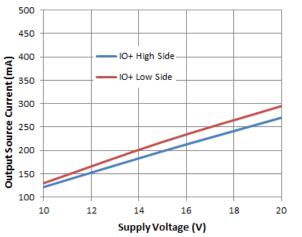
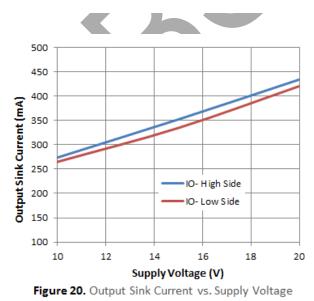
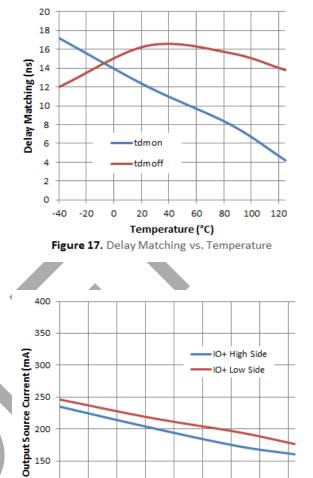


Figure 18. Output Source Current vs. Supply Voltage







60

80

100

120

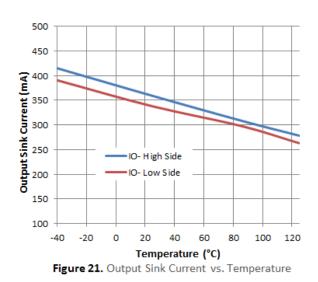
100

-40

-20

0

20





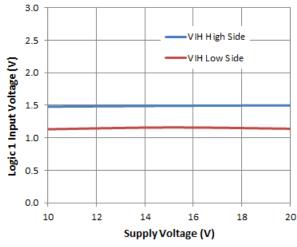
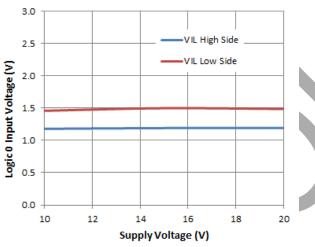
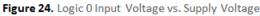
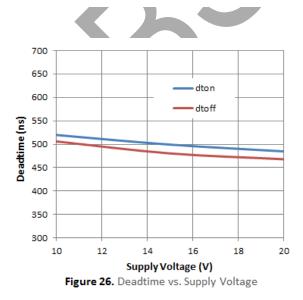
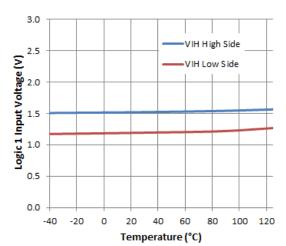


Figure 22. Logic 1 Input Voltage vs. Supply Voltage

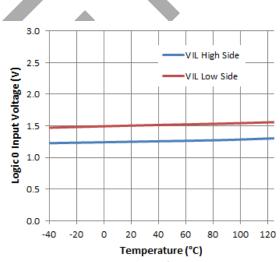


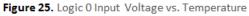


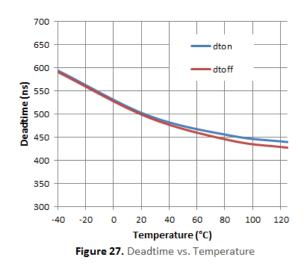














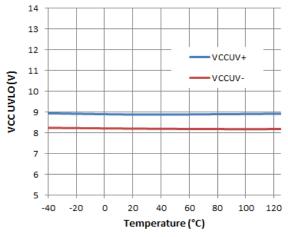


Figure 28. VCC UVLO vs. Temperature

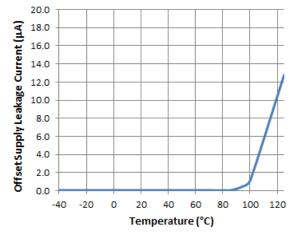


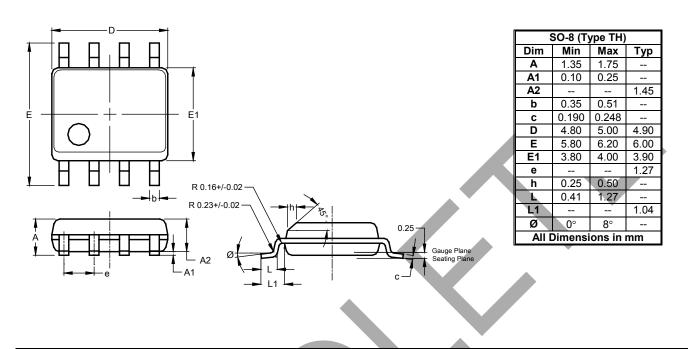
Figure 29. Offset Supply Leakage Current vs. Temperature



Package Outline Dimensions

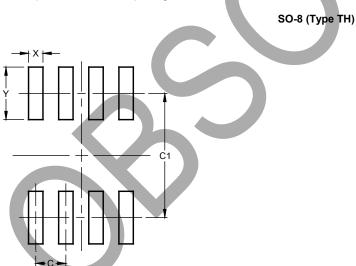
Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8 (Type TH)



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)				
С	1.27				
C1	5.20				
Х	0.60				
Y	2.20				



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