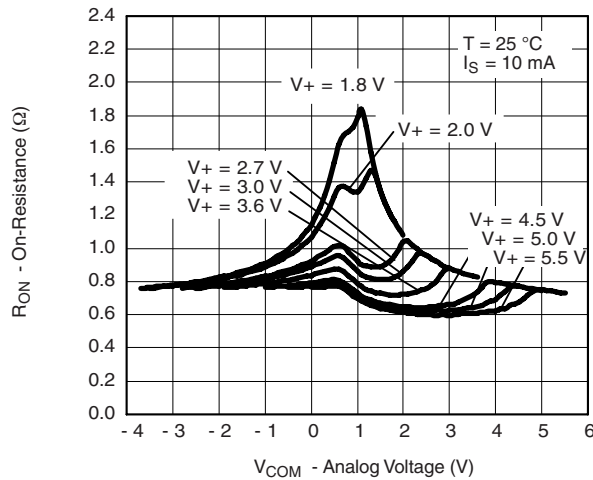
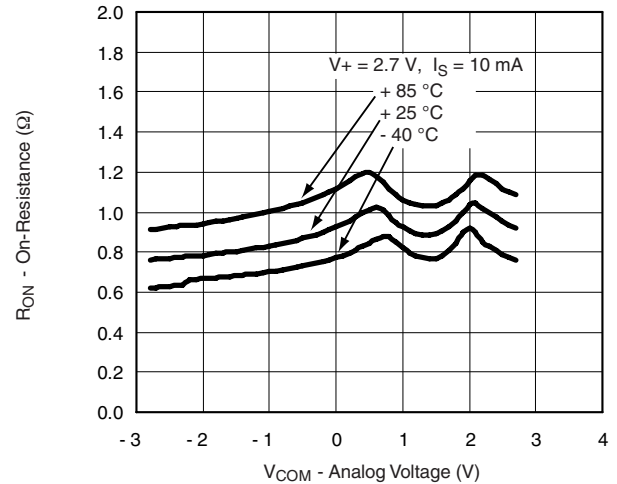
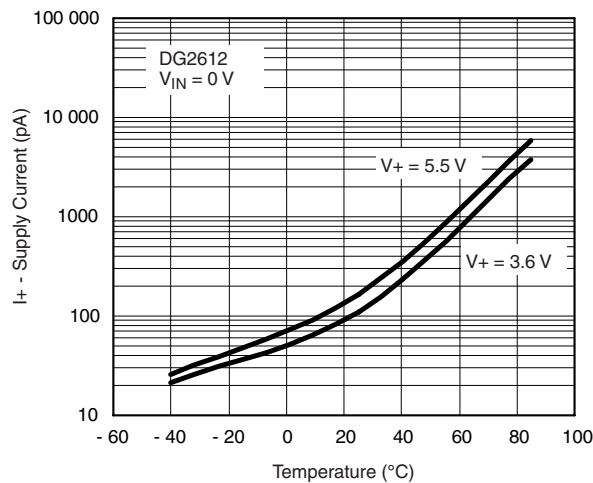
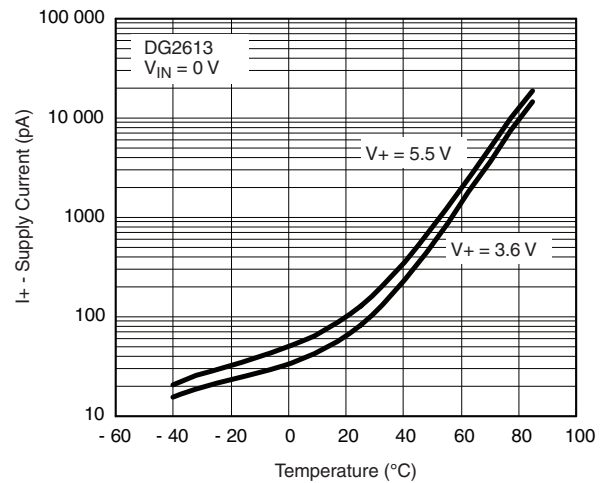
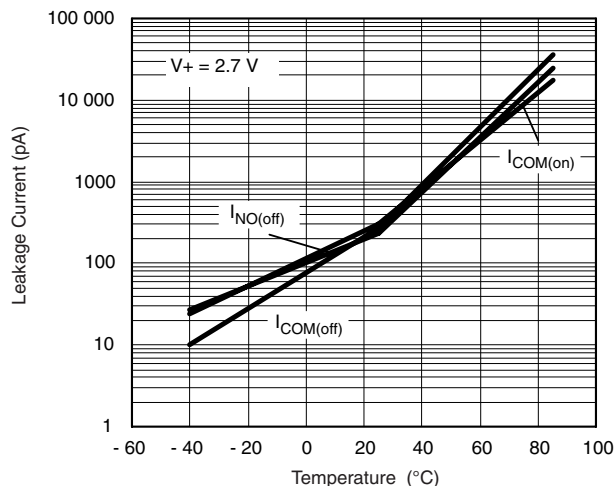
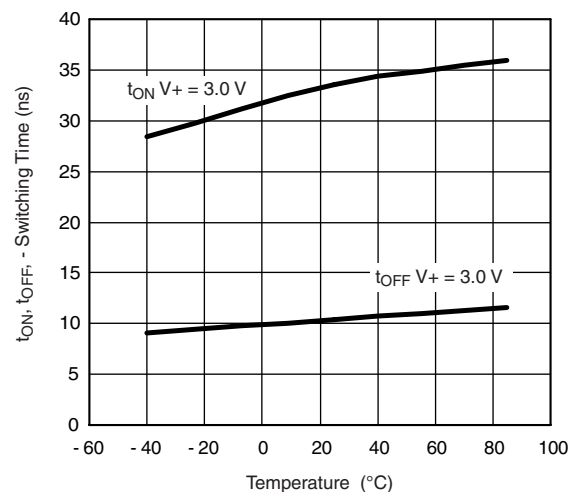


SPECIFICATIONS (V+ = 3 V)							
Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 3 V, ± 10 %,V <sub>IN</sub> = 0.5 V or 1.4 V <sup>e</sup>	Temp. <sup>a</sup>	Limits - 40 °C to 85 °C			Unit
				Min. <sup>b</sup>	Typ. <sup>c</sup>	Max. <sup>b</sup>	
Analog Switch							
Analog Signal Range <sup>d</sup>	V <sub>NO</sub> , V <sub>NC</sub> , V <sub>COM</sub>		Full	V+ - 5.5		V+	V
On-Resistance	R <sub>ON</sub>	V+ = 2.7 V, V <sub>COM</sub> = - 1 V/0 V/1 V/2 V I <sub>NO</sub> , I <sub>NC</sub> = 10 mA	Room Full		1.0	1.4 1.6	Ω
R <sub>ON</sub> Match <sup>d</sup>	ΔR <sub>ON</sub>		Room			0.1	
R <sub>ON</sub> Flatness <sup>d</sup>	R <sub>ON</sub> Flatness		Room			0.3	
Shunt Switch Resistance	R <sub>SH</sub>	I <sub>NO</sub> or I <sub>NC</sub> = 10 mA, V+ = 2.7 V, DG2612 only	Full		150	300	Ω
Switch Off Leakage Current	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 3.3 V, V <sub>NO</sub> , V <sub>NC</sub> = 1 V/3 V, V <sub>COM</sub> = 3 V/1 V	Room Full	- 2 - 100		2 100	nA
	I <sub>COM(off)</sub>		Room Full	- 2 - 100		2 100	
Channel-On Leakage Current	I <sub>COM(on)</sub>	V+ = 3.3 V, V <sub>NO</sub> , V <sub>NC</sub> = V <sub>COM</sub> = 1 V/3 V	Room Full	- 2 - 100		2 100	
Digital Control							
Input High Voltage	V <sub>INH</sub>	V+ = 1.8 V to 2.0 V	Full	1.0			V
		V+ = 2.7 V to 3.6 V		1.4			
		V+ = 4.2 V to 5.5 V		2.0			
Input Low Voltage	V <sub>INL</sub>	V+ = 1.8 V to 2.0 V				0.4	
		V+ = 2.7 V to 3.6 V				0.5	
		V+ = 4.2 V to 5.5 V				0.8	
Input Capacitance	C <sub>in</sub>		Full		5		pF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	1		1	μA
Dynamic Characteristics							
Turn-On Time	t <sub>ON</sub>	V <sub>NO</sub> or V <sub>NC</sub> = 1.5 V, R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 35 pF	Room Full		34 60 63		ns
Turn-Off Time	t <sub>OFF</sub>		Room Full		10 35 37		
Break-Before-Make Time	t <sub>BBM</sub>		Room	4	16		
Charge Injection <sup>d</sup> (DG2613)	Q <sub>INJ</sub>	C <sub>L</sub> = 1 nF, V <sub>GEN</sub> = 0 V, R <sub>GEN</sub> = 0 Ω	Room		2.4		pC
Off-Isolation <sup>d</sup>	OIRR	R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 100 kHz DG2612	Room		- 61		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>		Room		- 67		
Off-Isolation <sup>d</sup>	OIRR	R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 100 kHz DG2613	Room		- 67		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>		Room		- 73		
N <sub>O</sub> , N <sub>C</sub> Off Capacitance <sup>d</sup>	C <sub>NO(off)</sub> C <sub>NC(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		36		pF
Channel-On Capacitance <sup>d</sup>	C <sub>ON</sub>		Room		95		
Power Supply							
Power Supply Range	V+			1.8		5.5	V
Power Supply Current	I+	V <sub>IN</sub> = 0 or V+			0.01	1.0	μA

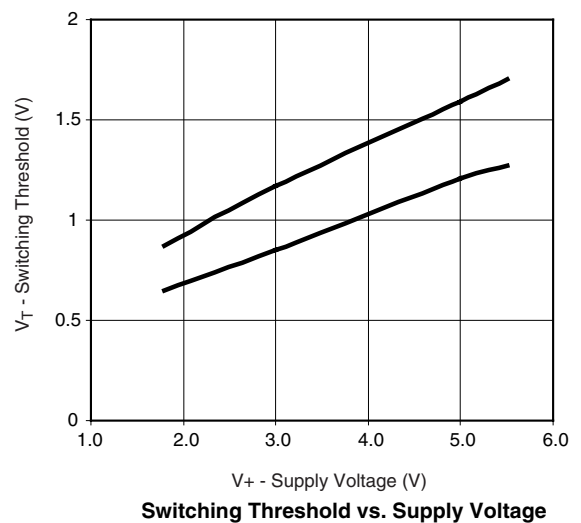
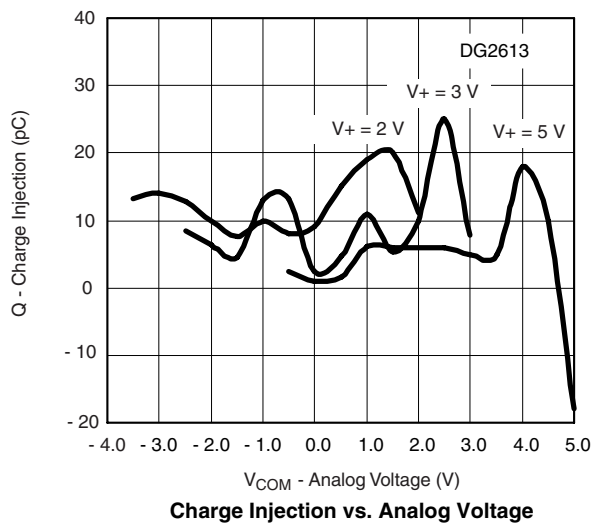
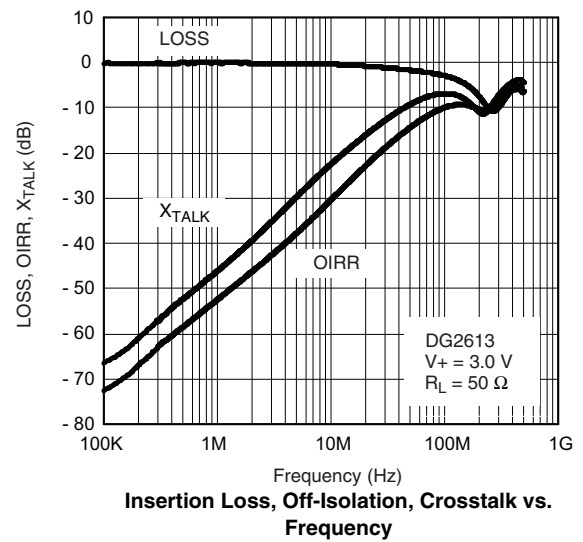
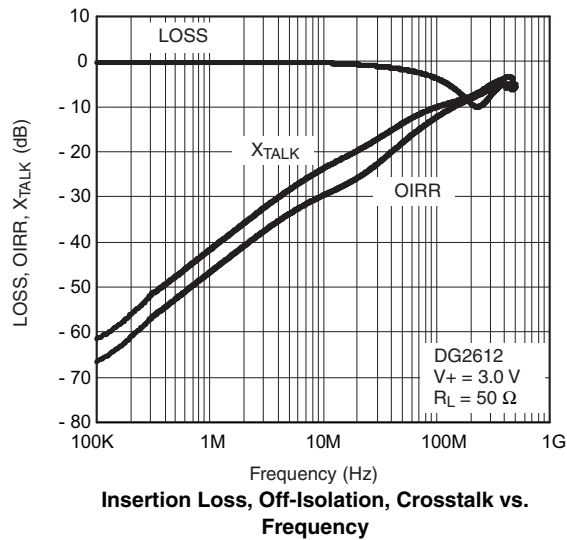
Notes:

- Room = 25 °C, Full = as determined by the operating suffix.
- Typical values are for design aid only, not guaranteed nor subject to production testing.
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Guarantee by design, nor subjected to production test.
- V<sub>IN</sub> = input voltage to perform proper function.

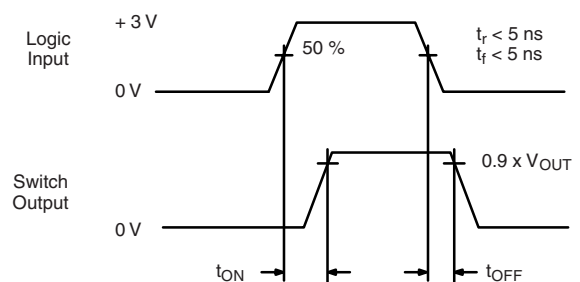
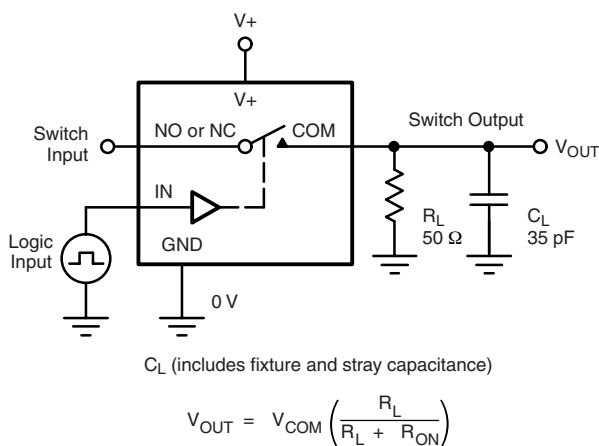
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $T_A = 25^\circ\text{C}$ , unless otherwise noted

 **$R_{ON}$  vs.  $V_{COM}$  and Supply Voltage**

 **$R_{ON}$  vs. Analog Voltage and Temperature**

**Supply Current vs. Temperature**

**Supply Current vs. Temperature**

**Leakage Current vs. Temperature**

**Switching Time vs. Temperature and Supply Voltage**

## TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$ , unless otherwise noted

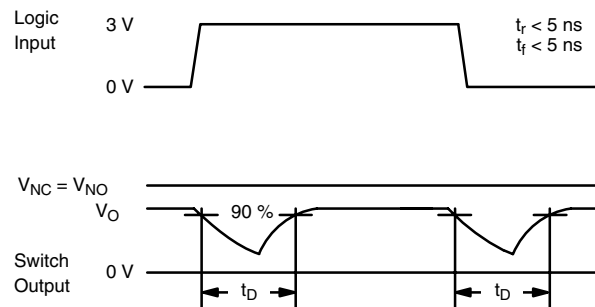
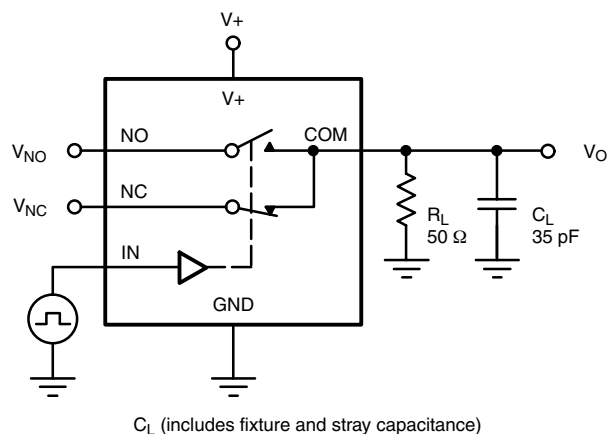
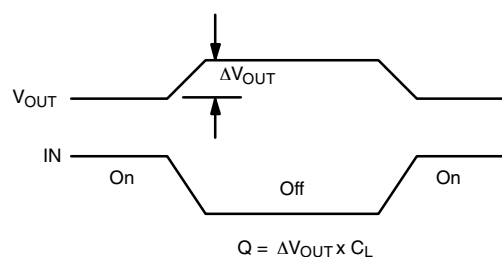
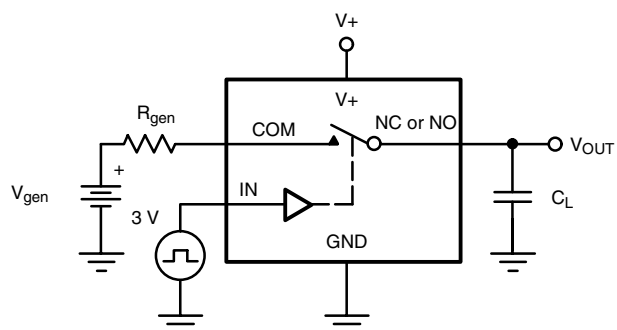


## TEST CIRCUITS

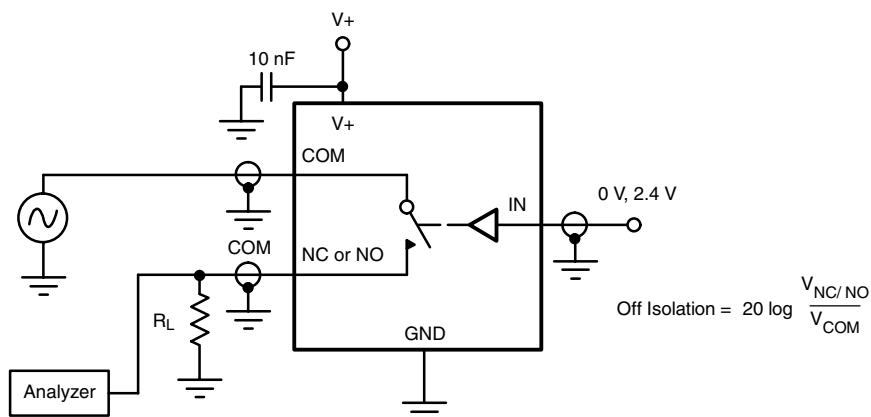


Logic "1" = Switch On  
Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time

**TEST CIRCUITS**

**Figure 2. Break-Before-Make Interval**


IN depends on switch configuration: input polarity determined by sense of switch.

**Figure 3. Charge Injection**

**Figure 4. Off-Isolation**

## TEST CIRCUITS

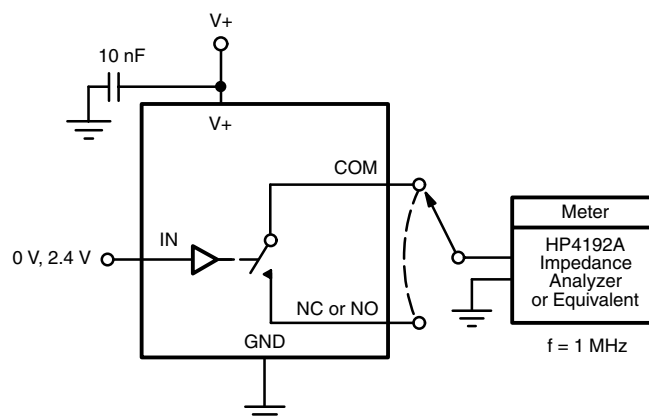
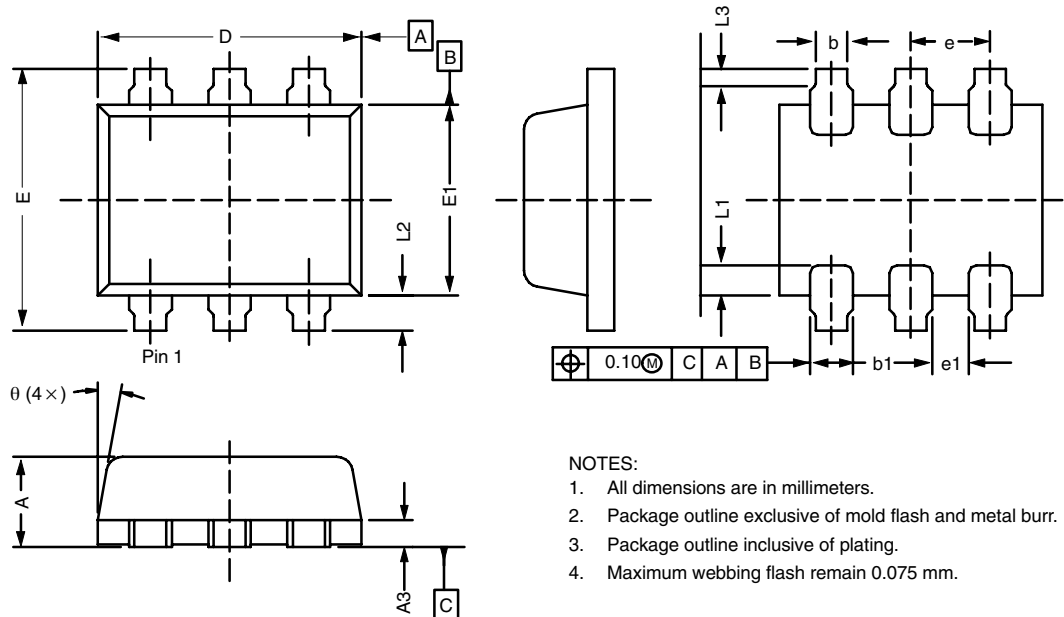


Figure 5. Channel Off/On Capacitance

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### SC-89: 6-LEAD (SOT-666)



#### NOTES:

1. All dimensions are in millimeters.
2. Package outline exclusive of mold flash and metal burr.
3. Package outline inclusive of plating.
4. Maximum webbing flash remain 0.075 mm.

Dim	MILLIMETERS*			INCHES		
	Min	Nom	Max	Min	Nom	Max
A	0.56	—	0.60	0.022	—	0.024
A3	0.13	0.17	0.18	0.005	0.006	0.007
b	0.17	—	0.25	0.006	—	0.010
b1	—	0.27	0.34	—	0.011	0.013
D	1.50	1.66	1.70	0.059	0.065	0.067
E	1.50	1.65	1.70	0.059	0.065	0.067
E1	1.10	1.20	1.30	0.043	0.047	0.051
e	0.50 BSC			0.020 BSC		
e1	0.20	—	—	0.008	—	—
L1	0.11	0.19	0.26	0.004	0.007	0.010
L2	0.10	0.23	0.30	0.004	0.009	0.012
L3	0.05	0.10	—	0.002	0.004	—
θ	8°	10°	12°	8°	10°	12°
ECN: S-52444—Rev. D, 28-Nov-05						
DWG: 5891						

\*Use millimeters as the primary measurement



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