

Device	Zener Voltage (Note 1)			$Z_Z @ I_Z (\Omega)$	Leakage Current		$T_C (mV / ^\circ C)$		C (pF) $V_Z = 0, f = 1MHz$
	Min.	Max.	$I_Z (mA)$		$I_R (\mu A)$	$V_R (V)$	Min.	Max.	
BZX79C27	25.1	28.9	2	80	0.05	18.9	-	23.5	30
BZX79C30	28	32	2	80	0.05	21	-	26	27
BZX79C33	31	35	2	80	0.05	23.1	-	29	25
BZX79C36	34	38	2	90	0.05	25.2	-	31	23
BZX79C39	37	41	2	130	0.05	27.3	-	34	21
BZX79C43	40	46	2	150	0.05	30.1	-	37	21
BZX79C47	44	50	2	170	0.05	32.9	-	40	19
BZX79C51	48	54	2	180	0.5	35.7	-	44	19
BZX79C56	52	60	2	200	0.05	39.2	-	47	18

V_F Forward Voltage = 1.5V Max @ $I_F = 100mA$

Notes:

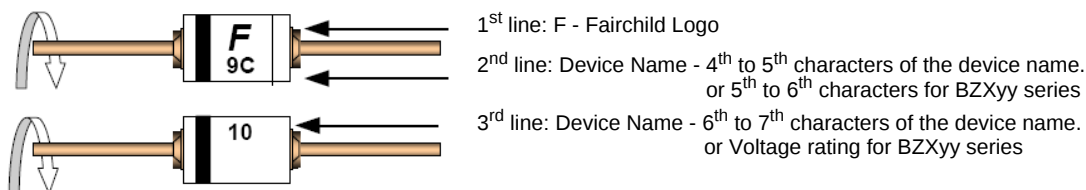
1. Zener Voltage (V_Z)

The zener voltage is measured with the device junction in the thermal equilibrium at the lead temperature (T_L) at $30^\circ C \pm 1^\circ C$ and 3/8" lead length.

Top Mark Information

Device	Line 1	Line 2	Line 3
BZX79C2V4	LOGO	9C	2V4
BZX79C2V7	LOGO	9C	2V7
BZX79C3V0	LOGO	9C	3V0
BZX79C3V3	LOGO	9C	3V3
BZX79C3V6	LOGO	9C	3V6
BZX79C3V9	LOGO	9C	3V9
BZX79C4V3	LOGO	9C	4V3
BZX79C4V7	LOGO	9C	4V7
BZX79C5V1	LOGO	9C	5V1
BZX79C5V6	LOGO	9C	5V6
BZX79C6V2	LOGO	9C	6V2
BZX79C6V8	LOGO	9C	6V8
BZX79C7V5	LOGO	9C	7V5
BZX79C8V2	LOGO	9C	8V2
BZX79C9V1	LOGO	9C	9V1
BZX79C10	LOGO	9C	10
BZX79C11	LOGO	9C	11
BZX79C12	LOGO	9C	12
BZX79C13	LOGO	9C	13
BZX79C15	LOGO	9C	15
BZX79C16	LOGO	9C	16
BZX79C18	LOGO	9C	18
BZX79C20	LOGO	9C	20
BZX79C22	LOGO	9C	22
BZX79C24	LOGO	9C	24
BZX79C27	LOGO	9C	27
BZX79C30	LOGO	9C	30
BZX79C33	LOGO	9C	33
BZX79C36	LOGO	9C	36
BZX79C39	LOGO	9C	39
BZX79C43	LOGO	9C	43
BZX79C47	LOGO	9C	47
BZX79C51	LOGO	9C	51
BZX79C56	LOGO	9C	56

Top Mark Information (Continued)



General Requirements:

- 1.0 Cathode Band
- 2.0 First Line: F - Fairchild Logo
- 3.0 Second Line: Device name - For 1Nxx series: 4th to 5th characters of the device name.
 For BZxx series: 5th to 6th characters of the device name.
- 4.0 Third Line: Device name - For 1Nxx series: 6th to 7th characters of the device name.
 For BZXyy series: Voltage rating
- 5.0 Devices shall be marked as required in the device specification (PID or FSC Test Spec).
- 6.0 Maximum no. of marking lines: 3
- 7.0 Maximum no. of digits per line: 2
- 8.0 FSC logo must be 20 % taller than the alphanumeric marking and should occupy the 2 characters of the specified line.
- 9.0 Marking Font: Arial (Except FSC Logo)
- 10.0 First character of each marking line must be aligned vertically.
- 11.0 All device markings must be based on Fairchild device specification.

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local
Sales Representative