

Characteristic		Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage		V_{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _R WM V _R	75	V
RMS Reverse Voltage		V _{R(RMS)}	53	V
Forward Continuous Current (Note 4)		I _{FM}	300	mA
Average Rectified Output Current (Note 4)		lo	150	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0μs @ t = 1.0s	I _{FSM}	2.0 1.0	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P_{D}	200	mW
Thermal Resistance Junction to Ambient Air (Note 4)	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	T_J,T_STG	-65 to +150	°C

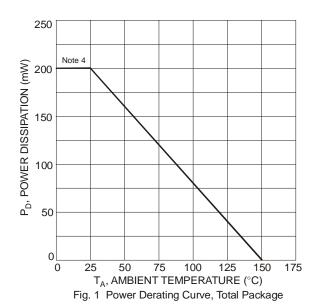
Electrical Characteristics @T_A = 25°C unless otherwise specified

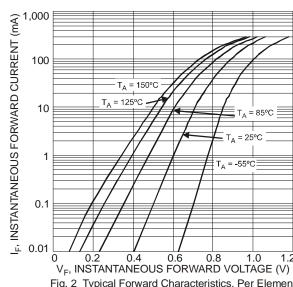
Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	$V_{(BR)R}$	75	_	V	$I_F = 2.5\mu A$
Forward Voltage	VF	_	0.715 0.855 1.0 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Reverse Current (Note 5)	I _R	_	2.5 50 30 25	μΑ μΑ μΑ nA	$V_R = 75V$ $V_R = 75V$, $T_J = 150$ °C $V_R = 25V$, $T_J = 150$ °C $V_R = 20V$
Total Capacitance	C _T	_	2.0	pF	$V_R = 0, f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

Notes:

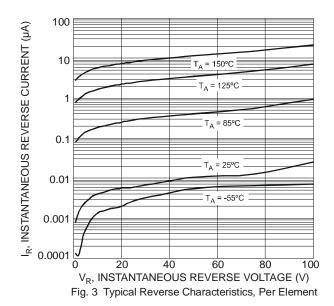
- 4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.

 5. Short duration pulse test used to minimize self-heating effect.









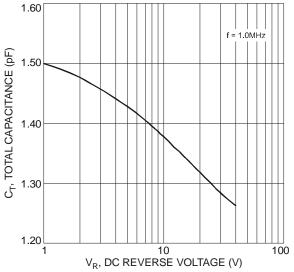
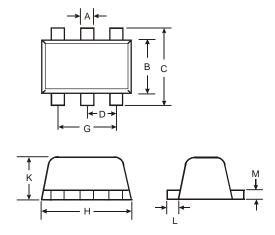


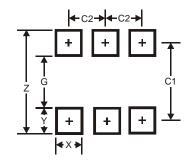
Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Package Outline Dimensions



SOT563				
Dim	Min	Max	Тур	
Α	0.15	0.30	0.20	
В	1.10	1.25	1.20	
С	1.55	1.70	1.60	
D		-	0.50	
G	0.90	1.10	1.00	
Н	1.50	1.70	1.60	
K	0.55	0.60	0.60	
L	0.10	0.30	0.20	
М	0.10	0.18	0.11	
All	All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Υ	0.5
C1	1.7
C2	0.5



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