Vishay Semiconductors

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 100 mA		V_{F}			1	V
	$I_F = 200 \text{ mA}$		V_{F}			1.25	V
Leakage current	V _R = 100 V	BAV19W-V	I _R			100	nA
	V _R = 100 V, T _j = 100 °C	BAV19W-V	I _R			15	μΑ
	V _R = 150 V	BAV20W-V	I _R			100	nA
	V _R = 150 V, T _j = 100 °C	BAV20W-V	I _R			15	μΑ
	V _R = 200 V	BAV21W-V	I _R			100	nA
	V _R = 200 V, T _j = 100 °C	BAV21W-V	I _R			15	μΑ
Dynamic forward resistance	I _F = 10 mA		r _f		5		Ω
Diode capacitace	V _R = 0, f = 1 MHz		C _D		1.5		pF
Reverse recovery time	$I_F = 30 \text{ mA}, I_R = 30 \text{ mA}, \\ i_R = 3 \text{ mA}, R_L = 100 \Omega$		t _{rr}			50	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

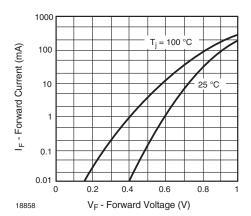


Fig. 1 - Forward Current vs. Forward Voltage

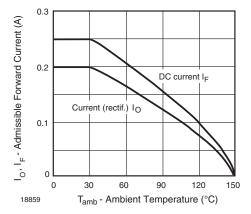


Fig. 2 - Admissible Forward Current vs. Ambient Temperature

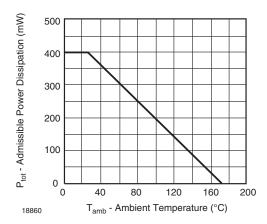


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

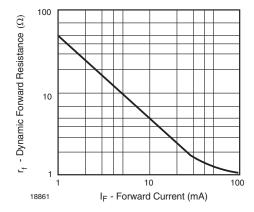
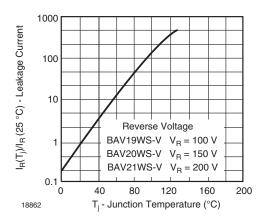
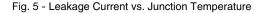


Fig. 4 - Dynamic Forward Resistance vs. Forward Current

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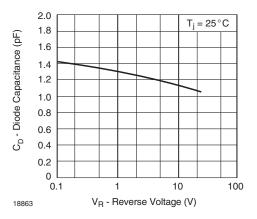


Fig. 6 - Capacitance vs. Reverse Voltage

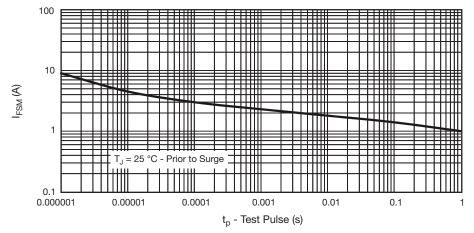
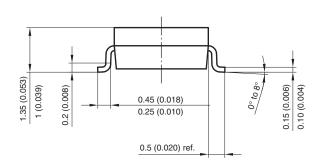
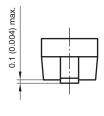


Fig. 7 - Non-Repetitive Peak Forward Current vs. Pulse Duration Maximum Admissible Values of Square Pulse

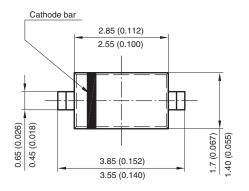
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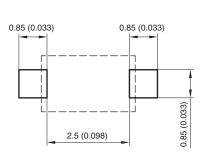
PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4)

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