Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

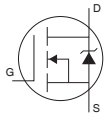
	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	30	—	—	V	V_{GS} = 0V, I_D = 250µA
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.03	—	V/°C	Reference to 25°C, I_D = 1mA
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	23	29	mΩ	V_{GS} = 10V, I_D = 6.9A ④
		—	32	46		V_{GS} = 4.5V, I_D = 5.5A ④
$V_{GS(th)}$	Gate Threshold Voltage	1.0	—	3.0	V	V_{DS} = V_{GS}, I_D = 250µA
gfs	Forward Transconductance	7.5	—	—	S	V_{DS} = 15V, I_D = 3.5A
I_{DSS}	Drain-to-Source Leakage Current	—	—	1.0	µA	V_{DS} = 24V, V_{GS} = 0V
		—	—	25		V_{DS} = 24V, V_{GS} = 0V, T_J = 125°C
I_{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	V_{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	100		V_{GS} = -20V

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge	—	22	33	nC	I_D = 3.5A
Q_{gs}	Gate-to-Source Charge	—	2.6	3.9		V_{DS} = 15V
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	6.8	10		V_{GS} = 10V ④
$t_{d(on)}$	Turn-On Delay Time	—	3.7	—	ns	V_{DD} = 15V
t_r	Rise Time	—	7.3	—		I_D = 3.5A
$t_{d(off)}$	Turn-Off Delay Time	—	21	—		R_G = 6.8Ω
t_f	Fall Time	—	11	—		V_{GS} =10V ④
C_{iss}	Input Capacitance	—	755	—	pF	V_{GS} = 0V
C_{oss}	Output Capacitance	—	310	—		V_{DS} = 25V
C_{rss}	Reverse Transfer Capacitance	—	120	—		f = 1.0MHz

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	3.0	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	58		
V_{SD}	Diode Forward Voltage	—	—	1.0	V	T_J = 25°C, I_S = 3.5A, V_{GS} = 0V ④
t_{rr}	Reverse Recovery Time	—	27	40	ns	T_J = 25°C,I_F = 3.5A
Q_{rr}	Reverse Recovery Charge	—	43	65	nC	di/dt = 100A/µs ④



Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

② Limited by T_{Jmax}, starting T_J = 25°C, L = 76mH, R_G = 50Ω, I_{AS} = 3.5A, V_{GS} =10V. Part not recommended for use above this value.

③ I_{SD} ≤ 3.5A, di/dt ≤ 590A/µs, V_{DD} ≤ $V_{(BR)DSS}$, T_J ≤ 175°C.

④ Pulse width ≤ 400µs; duty cycle ≤ 2%.

⑤ When mounted on 1 inch square copper board.

⑥ R_θ is measured at T_J of approximately 90°C.

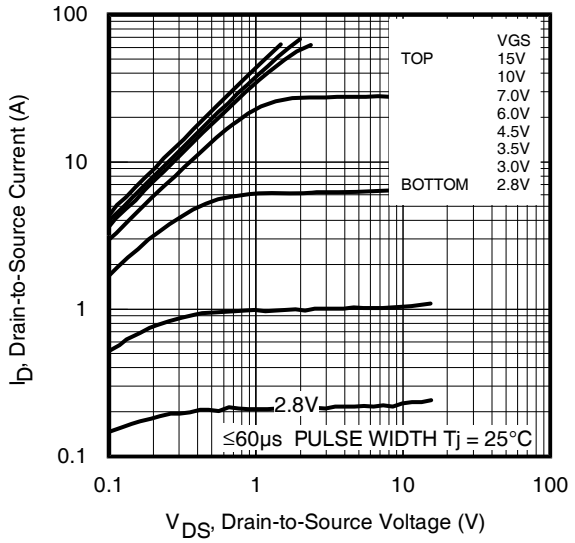


Fig 1. Typical Output Characteristics

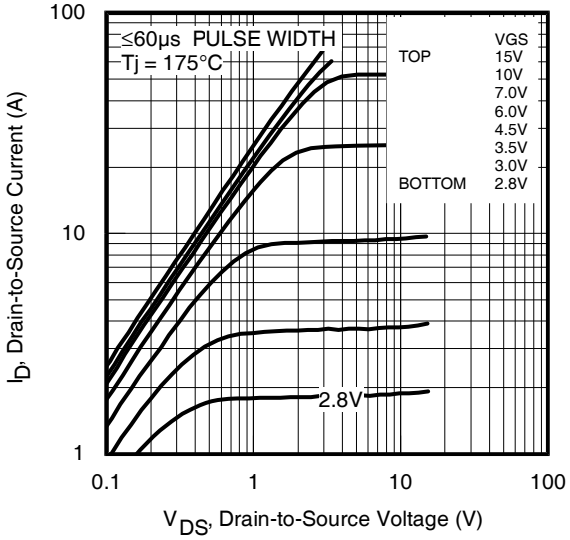


Fig 2. Typical Output Characteristics

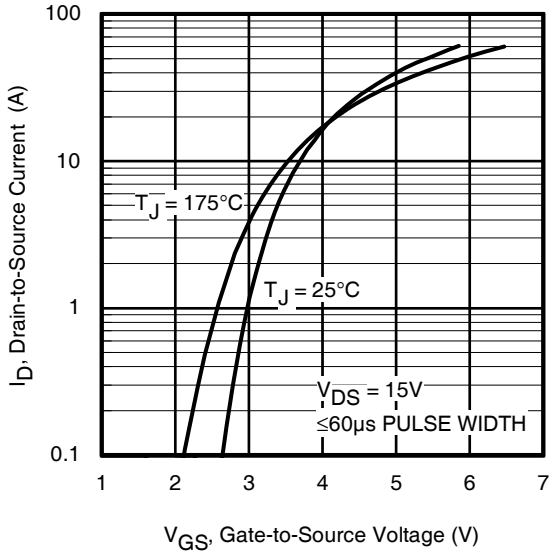


Fig 3. Typical Transfer Characteristics

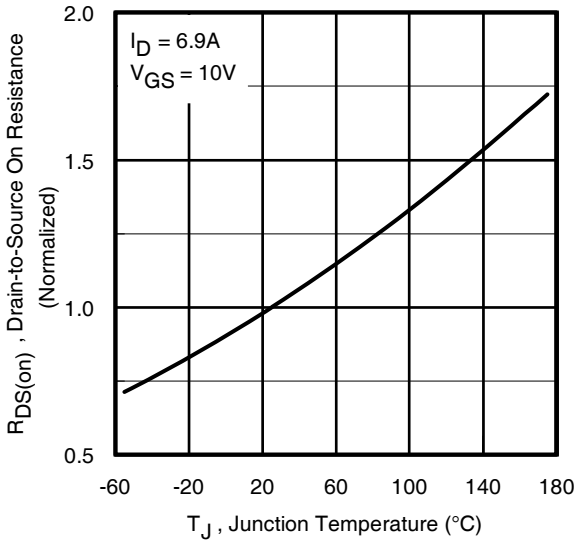


Fig 4. Normalized On-Resistance Vs. Temperature

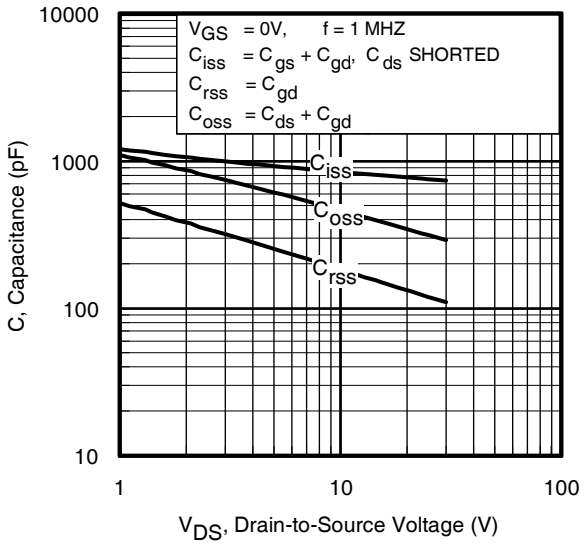


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

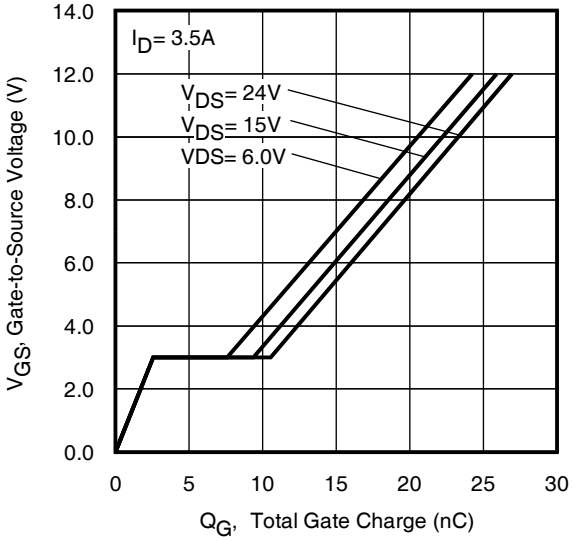


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

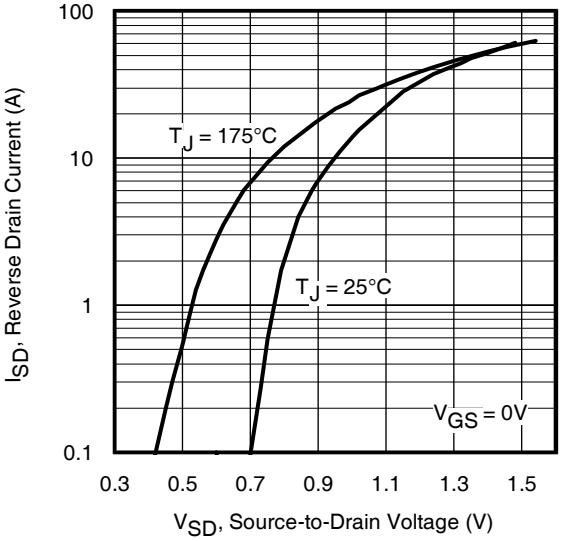


Fig 7. Typical Source-Drain Diode Forward Voltage

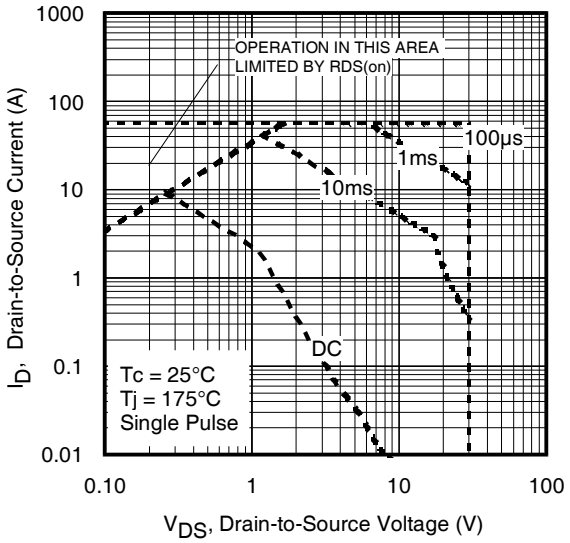


Fig 8. Maximum Safe Operating Area

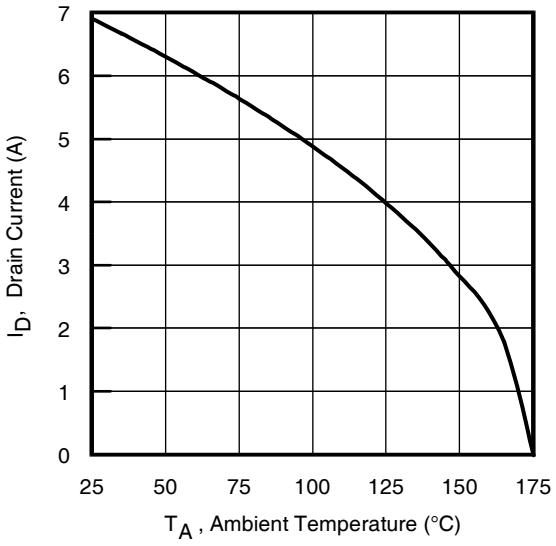


Fig 9. Maximum Drain Current Vs. Ambient Temperature

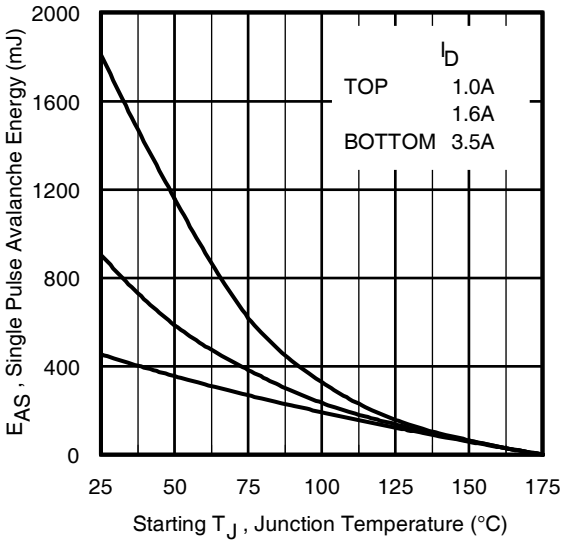


Fig 10. Maximum Avalanche Energy vs. DrainCurrent

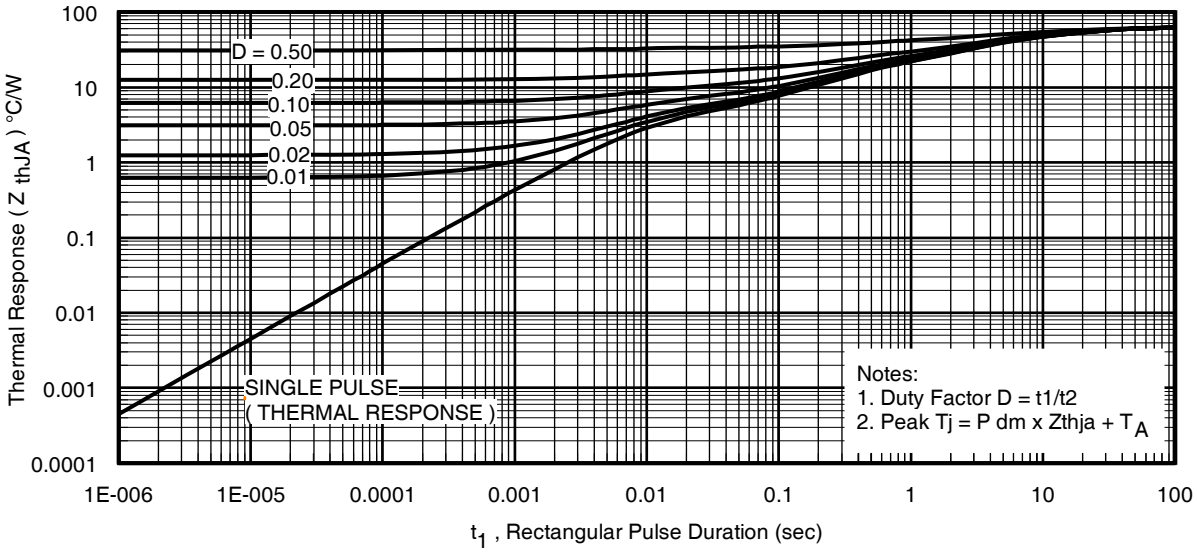


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

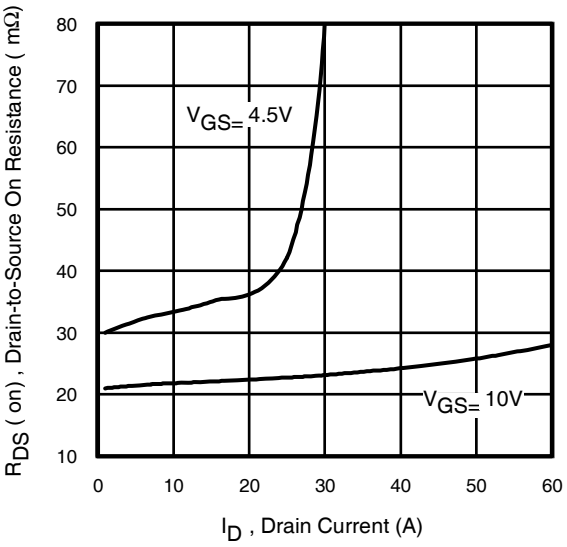


Fig 12. Typical On-Resistance Vs. Drain Current

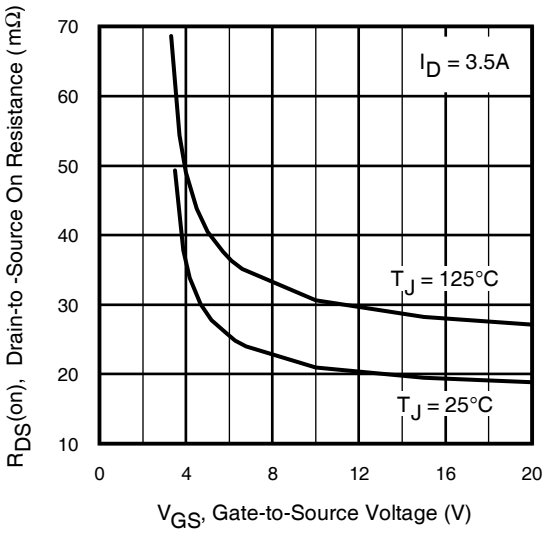


Fig 13. Typical On-Resistance Vs. Gate Voltage

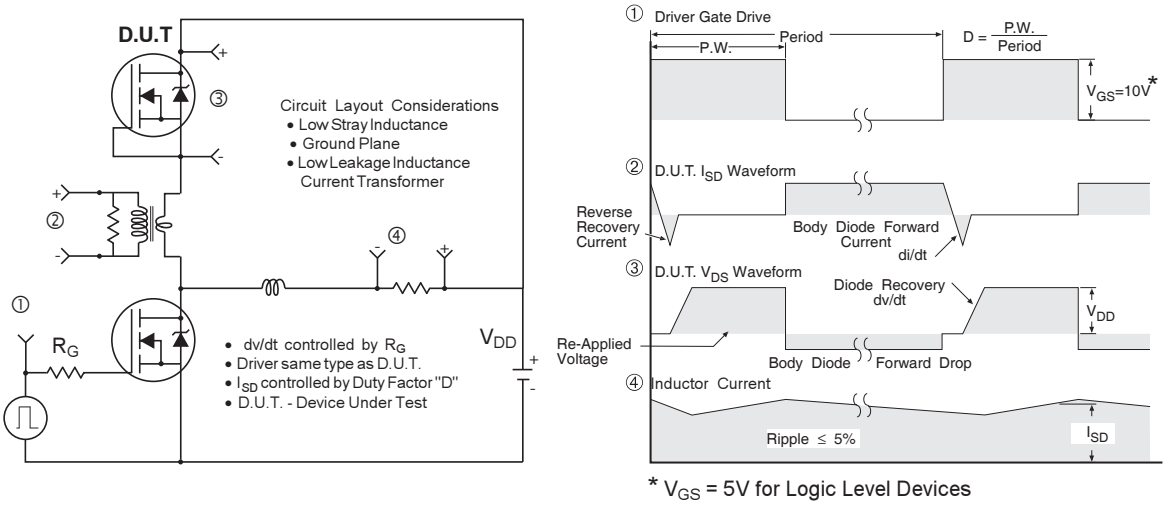


Fig 14. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

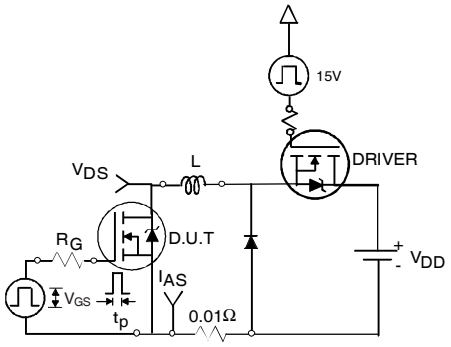


Fig 15a. Unclamped Inductive Test Circuit

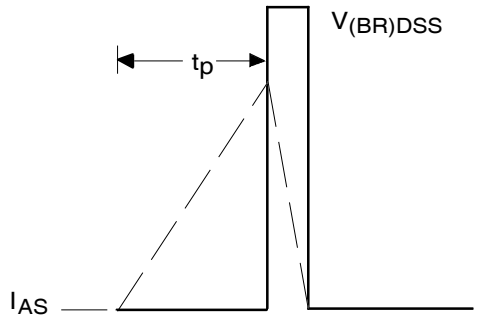


Fig 15b. Unclamped Inductive Waveforms

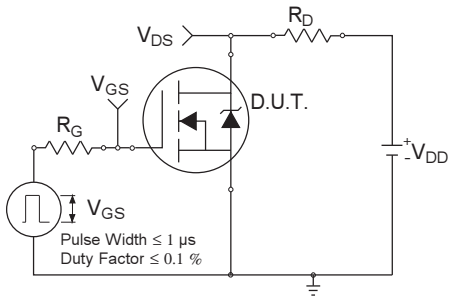


Fig 16a. Switching Time Test Circuit

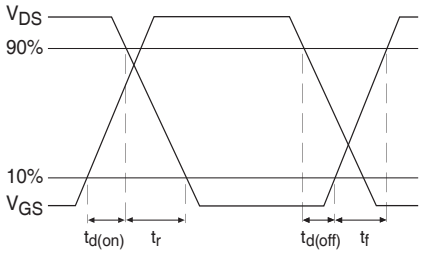


Fig 16b. Switching Time Waveforms

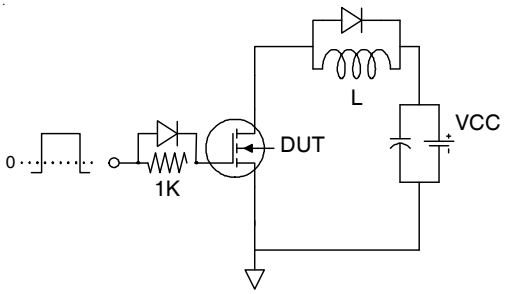


Fig 17a. Gate Charge Test Circuit

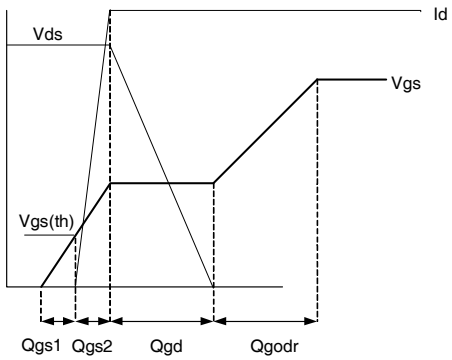
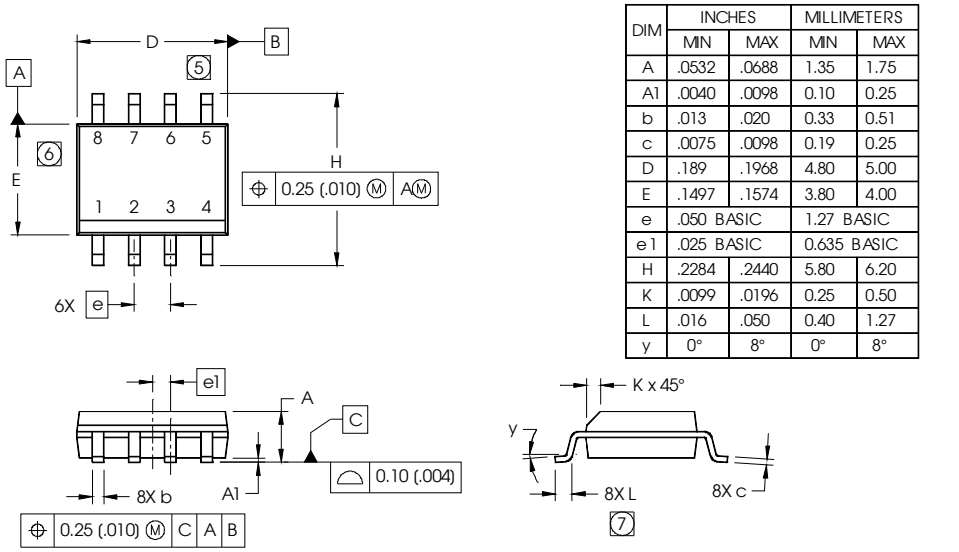


Fig 17b. Gate Charge Waveform

SO-8 Package Outline

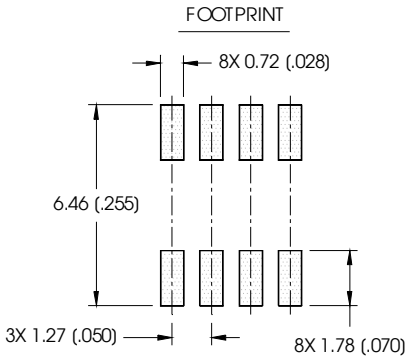
Dimensions are shown in millimeters (inches)



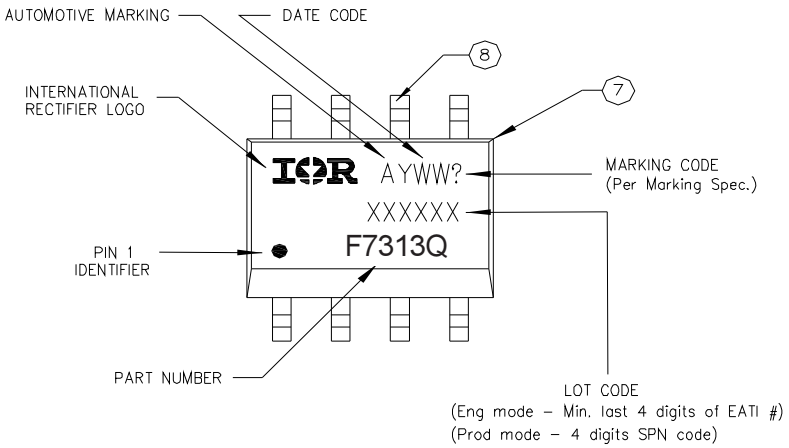
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°

NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.



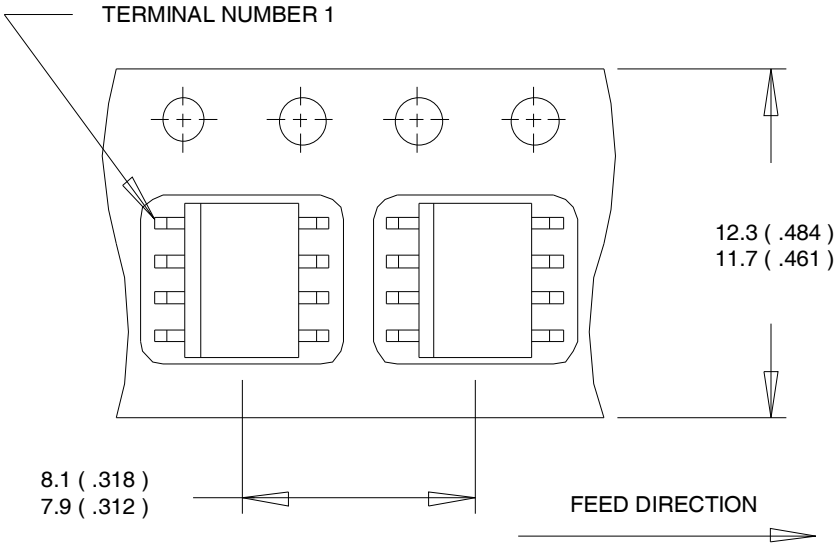
SO-8 Part Marking



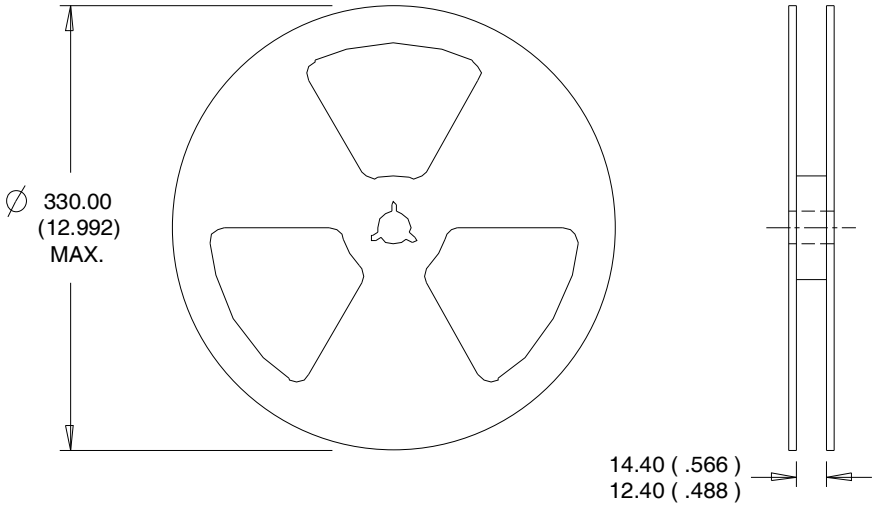
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

SO-8 Tape and Reel

Dimensions are shown in millimeters (inches)



NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Qualification Information†

Qualification Level		Automotive (per AEC-Q101) ††	
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		SO-8	MSL1
ESD	Machine Model	Class M1B (+/- 100 V)††† AEC-Q101-002	
	Human Body Model	Class H1A (+/- 500 V)††† AEC-Q101-001	
	Charged Device Model	Class C5 (+/- 2000 V)††† AEC-Q101-005	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site: http//www.irf.com/

†† Exceptions (if any) to AEC-Q101 requirements are noted in the qualification report.

††† Highest passing voltage

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http://www.irf.com/technical-info/

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101 N. Sepulveda Blvd., El Segundo, California 90245
Tel: (310) 252-7105

Revision History

Date	Comments
3/27/2014	• Added "Logic Level Gate Drive" bullet in the features section on page 1 • Updated part marking on page 7 • Updated data sheet with new IR corporate template

