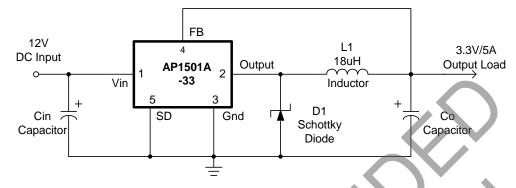
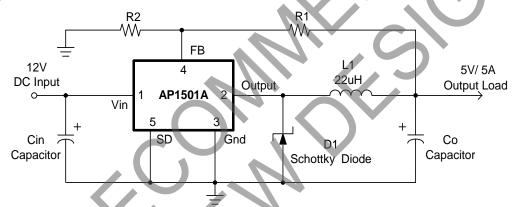


# **Typical Applications Circuit**

### (1) Fixed Type Circuit



#### (2) Adjustable Type Circuit

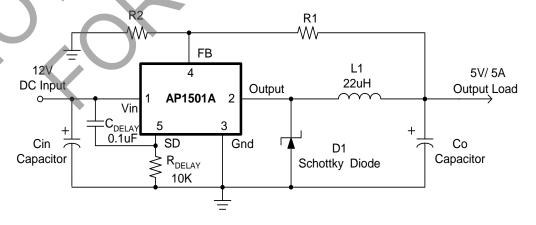


$$V_{OUT} = V_{FB} \times (1 + \frac{R1}{R2})$$

$$V_{FB} = 1.23V$$

$$R2 = 1k\Omega \text{ to } 3k\Omega$$

### (3) Delay Start Circuit

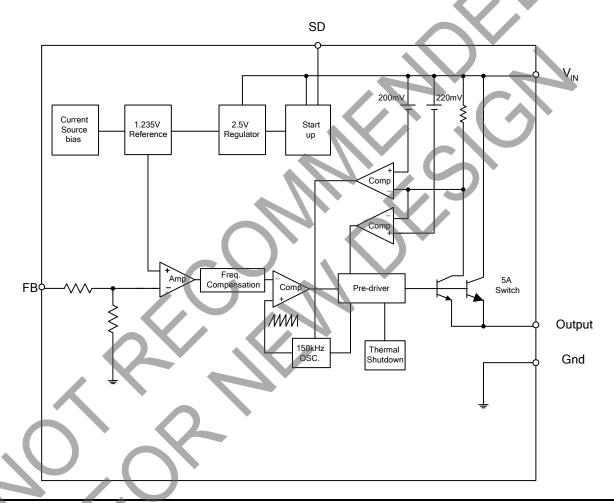




# **Pin Descriptions**

Pin Number	Pin Name	Description		
1	$V_{IN}$	Operating voltage input		
2	Output	Switching output		
3	Gnd	Ground		
4	FB	Output voltage feedback control		
5	SD	ON/OFF shutdown		

# **Functional Block Diagram**



# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit
ESD MM	Machine Model ESD Protection	400	V
Vcc	Supply Voltage	+45	V
$V_{SD}$	SD Pin Input Voltage	-0.3 to +40	V
$V_{FB}$	FB Pin Voltage	-0.3 to +40	V
V <sub>OUT</sub>	Output Voltage to Ground	-1	V
$P_{D}$	Power Dissipation	Internally limited	W
T <sub>ST</sub>	Storage Temperature	-65 to +150	°C
TJ	Operating Junction Temperature	-20 to +125	°C





AP1501A

# **Recommended Operating Conditions**

Symbol	Parameter	Rating	Unit
$V_{OP}$	Operating Voltage	4.5 to 40	V
T <sub>A</sub>	Operating Ambient Temperature	-20 to +85	°C

# Electrical Characteristics (All Output Voltage Versions)

Unless otherwise specified,  $V_{IN} = 12V$  for 3.3V, 5V, adjustable version and  $V_{IN} = 24V$  for the 12V version.  $I_{LOAD} = 0.5A$ . Specifications with **boldface type** are for full operating temperature range, the other type are for  $T_J = +25^{\circ}C$ .

Symbol	Parameter		Conditions	Min	Тур.	Max	Unit	
I <sub>FB</sub>	Feedback Bias Current		V <sub>FB</sub> = 1.3V (Adjustable version only)	+	40	60 100	nA	
fosc	Oscillator Frequen	су	_	127 110	150	173 173	kHz	
$V_{SAT}$	Saturation Voltage		I <sub>OUT</sub> = 5A No outside circuit V <sub>FB</sub> = 0V force driver on	<b>/</b> -	1.5	1.6 1.7	V	
DC	Max. Duty Cycle (0	ON)	V <sub>FB</sub> = 0V force driver on	-(	100	_	%	
DC	Min. Duty Cycle (C	)FF)	V <sub>FB</sub> = 12V force driver off	1	0	_	%	
I <sub>CL</sub>	Current Limit		Peak current No outside circuit	5.5	6.0	6.5 <b>7.5</b>	А	
ال	Output = 0V	Output Leakage	V <sub>FB</sub> = 0 force driver on No outside circuit V <sub>FB</sub> = 12V force driver off (Note 4)	1	_	200	μA	
"L	Output = -1V	Current	V <sub>IN</sub> = 40V	_	2	60	mA	
IQ	Quiescent Current		V <sub>FB</sub> = 12V force driver off	_	5	10	mA	
I <sub>STBY</sub>	Standby Quiescent Current		SD pin = 5V V <sub>IN</sub> = 40V	_	150	250 <b>300</b>	μА	
V <sub>IL</sub>			Low (regulator ON)	_		0.6		
VIH	SD Pin Logic Input Threshold Voltage		High (regulator OFF)	2.0	1.3	_	V	
lн	SD Pin Logic Input Current		V <sub>LOGIC</sub> = 2.5V (OFF)	_	15	25	μA	
ال	SD Pin Input Current		$V_{LOGIC} = 0.5V (ON)$	1	0.02	5	r	
Δ.,	Thermal Resistance	ce Junction-to-	TO263-5 (Note 5)	_	37	_	°C/W	
θJΑ	Ambient		TO220-5(R) (Note 5)	_	31	_	C/VV	
θус	Thermal Resistance	ce	TO263-5 (Note 5)	_	6	_	°C/W	
OJC .	Junction-to-Case		TO220-5(R) (Note 5)	_	5	_	C/ V V	

Notes:

<sup>4.</sup> FB pin removed from output and connected to 0V to force the output transistor switch ON. FB pin removed from output and connected to 12V for the 3.3V, 5V, and the ADJ. version, and 15V for the 12V version, to force the output transistor switch OFF.

<sup>5.</sup> Test condition: Device mounted with copper area of approximately 3inch<sup>2</sup>, 1oz, no air flow.





AP1501A

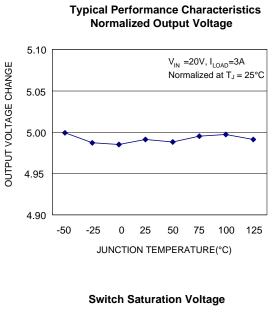
# **Electrical Characteristics** (continued)

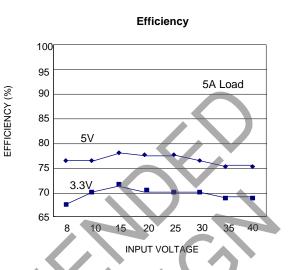
Specifications with **boldface type** are for full operating temperature range, the other type are for  $T_J = +25$ °C.

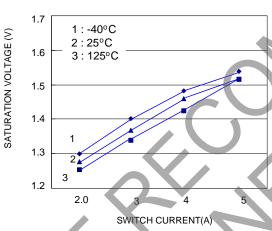
	Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
AP1501A-ADJ	V <sub>FB</sub>	Output Feedback	$5V \le V_{IN} \le 40V$ $0.2A \le I_{LOAD} \le 5A$ $V_{OUT}$ programmed for $3V$	1.193/ <b>1.18</b>	1.23	1.267/ <b>1.28</b>	V
	η	Efficiency	$V_{IN} = 12V$ , $I_{LOAD} = 5A$	-	72	_	%
AP1501A-3.3V	V <sub>OUT</sub>	Output Voltage	$5.5V \le V_{IN} \le 40V$ $0.2A \le I_{LOAD} \le 5A$	3.168/ <b>3.135</b>	3.3	3.432/ <b>3.465</b>	V
	η	Efficiency	$V_{IN} = 12V$ , $I_{LOAD} = 5A$		71		%
AP1501A-5V	V <sub>OUT</sub>	Output Voltage	$8V \le V_{IN} \le 40V$ $0.2A \le I_{LOAD} \le 5A$	4.8/ <b>4.75</b>	5	5.2/ <b>5.25</b>	V
	η	Efficiency	$V_{IN} = 12V$ , $I_{LOAD} = 5A$	_	78	_	%
AP1501A-12V	V <sub>OUT</sub>	Output Voltage	$15V \le V_{IN} \le 40V$ $0.2A \le I_{LOAD} \le 5A$	11.52/ <b>11.</b> 4	12	12.48/ <b>12.6</b>	V
	η	Efficiency	VIN = 16V ILOAD = 5A		88		%

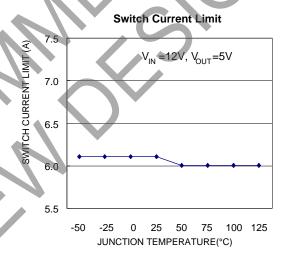


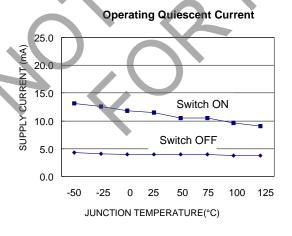
# **Typical Performance Characteristics**

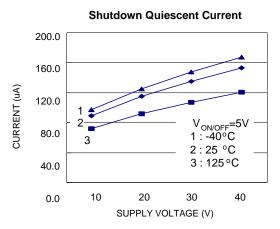








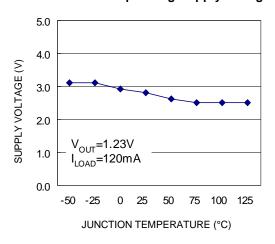




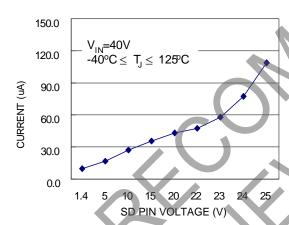


# **Typical Performance Characteristics (continued)**

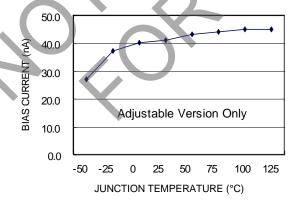
### **Minimum Operating Supply Voltage**



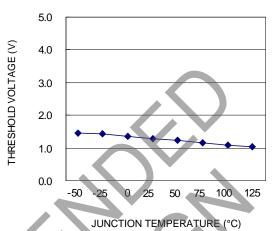
# SD Pin Current (Sinking)



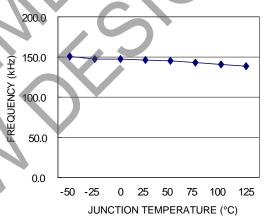
## **FB Pin Bias Current**



### **ON/OFF Threshold Voltage**



# Switch Frequency





# NOT RECOMMENDED FOR NEW DESIGN USE AP63357

AP1501A

## **Functional Description**

#### **Pin Functions**

 $+V_{IN}$ 

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be present at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

#### Ground

Circuit ground.

#### Output

Internal switch. The voltage at this pin switches between (+V<sub>IN</sub> - V<sub>SAT</sub>) and approximately -0.5V, with a duty cycle of approximately V<sub>OUT</sub> / V<sub>IN</sub>. To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be kept a minimum.

#### Feedback

Senses the regulated output voltage to complete the feedback loop.

#### ON/OFF

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 150µA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of 40V) shuts the regulator down. If this shutdown feature is not needed, the SD pin can be wired to the ground pin or it can be left open, in either case the regulator will be in the ON condition.

#### **Thermal Considerations**

The AP1501A is available in two packages, a 5-pin surface mount TO263-5, TO220-5 and TO220-5 (R).

The TO220-5 and TO220-5 (R) packages need a heat sink under most conditions. The size of the heat sink depends on the input voltage, the output voltage, the load current and the ambient temperature. The AP1501A junction temperature rises above ambient temperature for a 5A load and different input and output voltages. The data for these curves was taken with the AP1501A (TO220-5 and TO220-5 (R) packages) operating as a buck switching regulator in an ambient temperature of +25°C (still air). These temperature rise numbers are all approximate and there are many factors that can affect these temperatures. Higher ambient temperatures require more heat sinking.

The TO263-5 surface mount package tab is designed to be soldered to the copper on a printed circuit board. The copper and the board are the heat sink for this package and the other heat producing components, such as the catch diode and inductor. The PC board copper area that the package is soldered to should be at least 0.8 inch<sup>2</sup>, and ideally should have 2 or more square inches of 2 oz. Additional copper area improves the thermal characteristics, but with copper areas greater than approximately 6 inch<sup>2</sup>, only small improvements in heat dissipation are realized. If further thermal improvements are needed, double sided, multilayer PC boards with large copper areas and/or airflow are recommended.

The AP1501A (TO263-5 package) junction temperature rise above ambient temperature with a 2A load for various input and output voltages. This data was taken with the circuit operating as a buck switching regulator with all components mounted on a PC board to simulate the junction temperature under actual operating conditions. This curve can be used for a quick check for the approximate junction temperature for various conditions, but be aware that there are many factors that can affect the junction temperature. When load currents higher than 3A are used, double sided or multilayer PC boards with large copper areas and/or airflow might be needed, especially for high ambient temperatures and high output voltages.

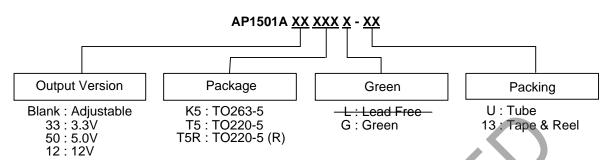
For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper should be used in the board layout. (Once exception to this is the Output pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

Package thermal resistance and junction temperature rise numbers are all approximate, and there are many factors that will affect these numbers. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multilayer board and the amount of solder on the board. The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving.

Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.



# **Ordering Information**



Device (Note 6)	Voltage	Package code	Package (Note 7)	Lead Free/ Green	Quantity		Number Suffix 13"Tape and reel	Status (Note 6)	Alternative
AP1501A-33K5G-13	33	K5	TO263-5	Green	800	NA	-13	NRND	
AP1501A-12K5G-13	12	K5	TO263-5	Green	800	NA	-13	NRND	AP63357
AP1501A-50K5G-13	50	K5	TO263-5	Green	800	NA	-13	NRND	AP63357
AP1501A-K5G-13	ADJ	K5	TO263-5	Green	800	NA	-13	NRND	AP63357
AP1501A-12T5RG-U	12	T5R	TO220-5 (R)	Green	50	-U	NA	End of Life	AP63357
AP1501A-33T5RG-U	33	T5R	TO220-5 (R)	Green	50	-U	NA	End of Life	AP63357
AP1501A-50T5RG-U	50	T5R	TO220-5 (R)	Green	50	-U	NA	End of Life	AP63357
AP1501A-T5RG-U	ADJ	T5R	TO220-5 (R)	Green	50	-U	NA	NRND	AP63357
AP1501A-12T5G-U	12	T5	TO220-5	Green	50	-U	NA	NRND	AP63357
AP1501A-33T5G-U	33	T5	TO220-5	Green	50	-U	NA	NRND	AP63357
AP1501A-50T5G-U	50	T5	TO220-5	Green	50	-U	NA	NRND	AP63357
AP1501A-T5G-U	ADJ	T5_	TO220-5	Green	50	-U	NA	NRND	AP63357
AP1501A-12K5G-U	12	K5	TO263-5	Green	50	-U	NA	End of Life	AP63357
AP1501A-33K5G-U	33	K5	TO263-5	Green	50	-U	NA	NRND	AP63357
AP1501A-50K5G-U	50	K5	TO263-5	Green	50	-U	NA	NRND	AP63357
AP1501A-K5G-U	ADJ	K5	TO263-5	Green	50	-U	NA	End of Life	AP63357

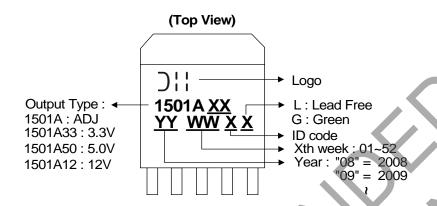
Notes: 6. All AP1501A-XXK5Lvariants are End of Life and the closest alternative is the AP1501A-XXK5G-13.
All AP1501A-XXT5RL and AP1501A-XXT5L variants are End of Life without any alternative.
All AP1501A-XXT5G-U (TO220-5) variants are Not Recommended for New Design (NRND) without any alternative.

7. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

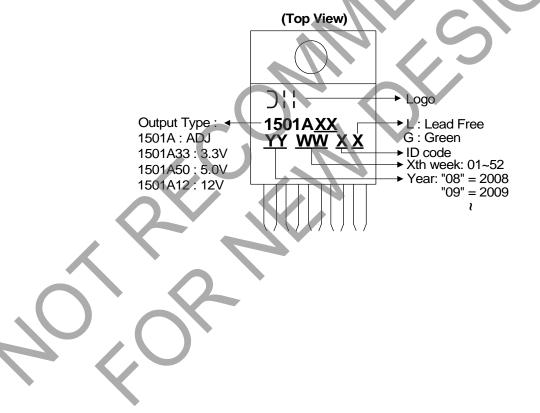


# **Marking Information** (Note 6)

### (1) TO263-5



### (2) TO220-5/TO220-5 (R)

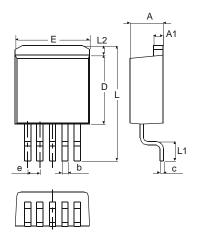




# **Package Outline Dimensions**

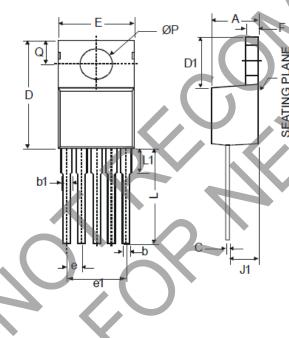
Please see http://www.diodes.com/package-outlines.html for the latest version.

### (1) Package Type: TO263-5



TO263-5					
Dim	Min	Max			
Α	4.07	4.85			
A1	1.14	1.40			
b	0.66	1.02			
С	0.36	0.64			
D	8.65	9.65			
E	9.78	10.54			
е	1.57	1.85			
	14.61	15.88			
L1	2.29	2.79			
L2	-	2.92			
All Dimensions in mm					

### (2) Package Type: TO220-5



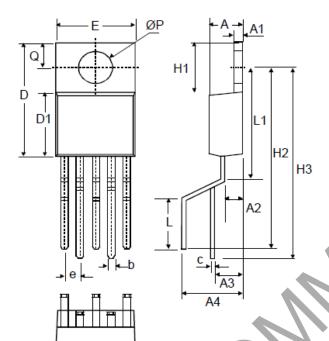
	TO220-	5		
Dim	Min	Max		
Α	3.55	4.85		
b	0.51	1.14		
b1	1.14	1.78		
C	0.31	1.14		
D	14.20	16.50		
D1	5.84	6.86		
Е	9.78	10.54		
е	1.6	1.8		
e1	6.6	7.0		
F	0.51	1.40		
J1	2.03	2.92		
L	12.72	14.72		
L1	3.66	6.35		
Р	3.53	4.09		
Q	2.54	3.43		
All Dimensions in mm				



# Package Outline Dimensions (continued)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (3) Package Type: TO220-5 (R)

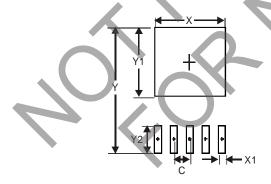


TO220-5(R)						
Dim	Min	Тур	Max			
Α	4.37	4.57	4.77			
A1	1.12	1.27	1.40			
A2	2.45	2.65	2.85			
A3	4.10	4.40	4.70			
A4	7.95	8.25	8.55			
b	0.64	0.79	0.94			
U	0.35	0.38	0.55			
Д	14.80	15.00	15.20			
D1	8.50	8.70	8.90			
е	-	1.70	-			
E	9.96	10.16	10.36			
H1	6.10	6.30	6.50			
H2	21.32	22.12	22.92			
Н3	24.15	24.95	25.75			
L		6.30	-			
L1	13.10	13.50	13.90			
P	3.64	3.84	4.04			
ď	2.55	2.75	2.95			
All D	All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

# (1) Package Type: TO263-5



Dimensions	Value (in mm)
Х	10.9
X1	1.05
Y	15.7
Y1	9.1
Y2	2.5
С	1.7



# NOT RECOMMENDED FOR NEW DESIGN USE AP63357

AP1501A

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