ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at T_C = 132 °C, rectangular waveform		80	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V_{RRM} applied	7000	
		10 ms sine or 6 ms rect. pulse		720	
Non-repetitive avalanche energy per leg	E_{AS}	T_J = 25 °C, I_{AS} = 1 A, L = 30 mH		15	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V_{FM} (1)	40 A	T_J = 25 °C	0.81	V
		80 A		1.00	
		40 A	T_J = 125 °C	0.67	
		80 A		0.82	
Maximum reverse leakage current per leg See fig. 2	I_{RM} (1)	T_J = 25 °C	V_R = Rated V_R	1.5	mA
		T_J = 125 °C		35	
Maximum junction capacitance per leg	C_T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/µs

Note

(1) Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_J, T_{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to case	per leg	R_{thJC}	DC operation, see fig. 4	0.85	°C/W
	per package		DC operation	0.42	
Typical thermal resistance, case to heatsink (D-61-8 only)		R_{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight				7.8	g
				0.28	oz.
Mounting torque	minimum		Recommended hardware 3M stainless screw	12 (10)	kgf · cm (lbf · in)
	maximum			24 (20)	
Marking device			Case style D-61	83CNQ080A	
				83CNQ100A	
			Case style D-61-8-SM	83CNQ080ASM	
				83CNQ100ASM	
			Case style D-61-8-SL	83CNQ080ASL	
				83CNQ100ASL	

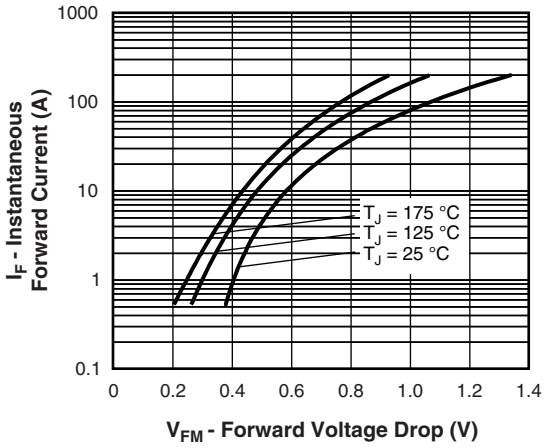


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

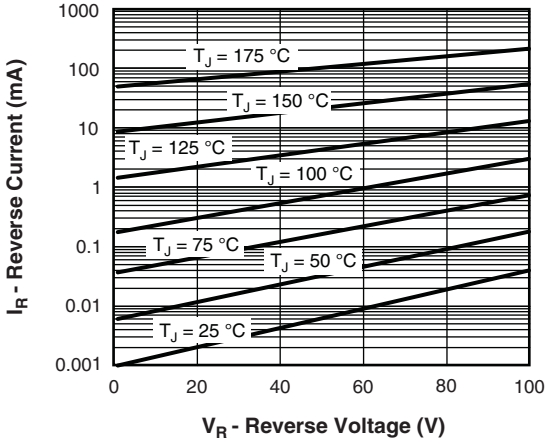


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

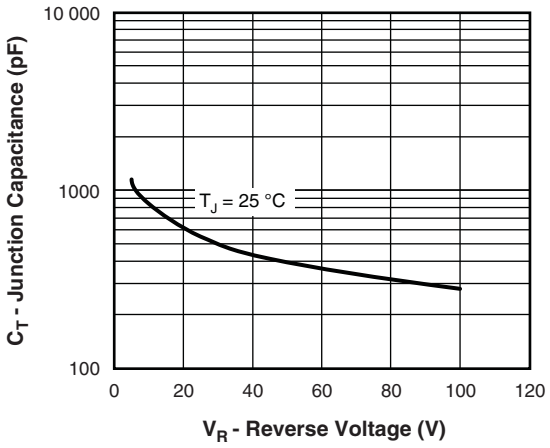


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

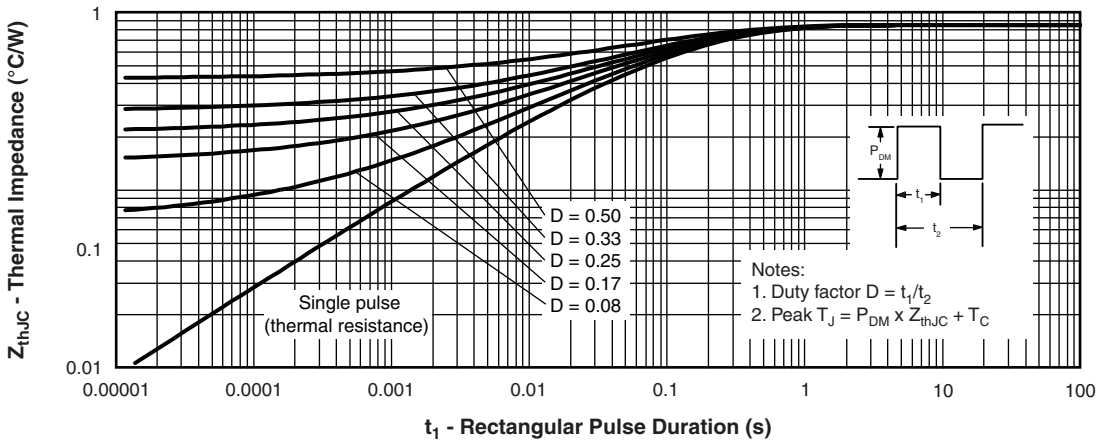


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

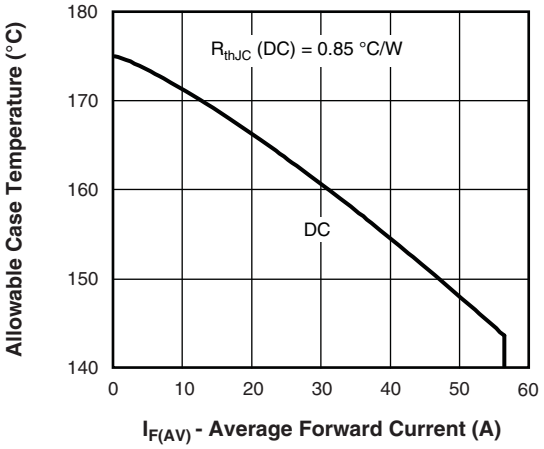


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

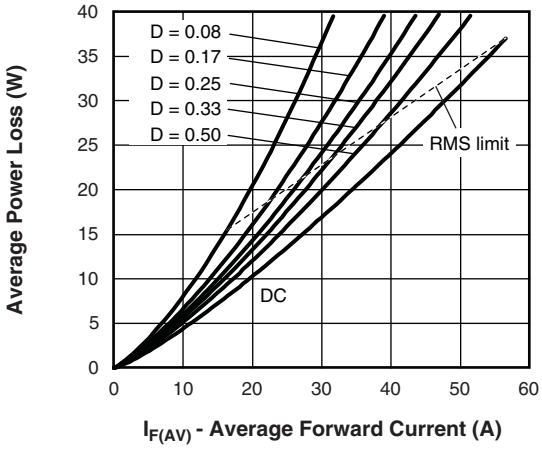


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

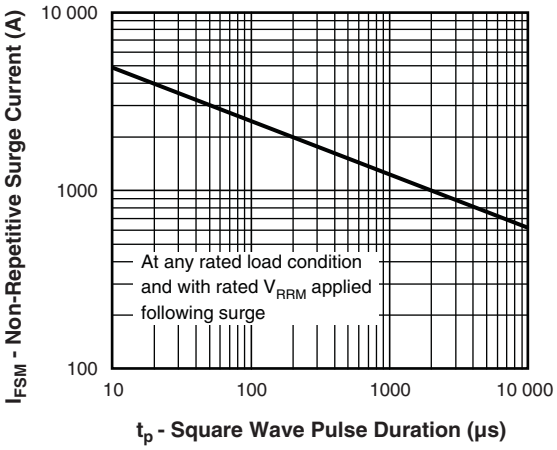


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

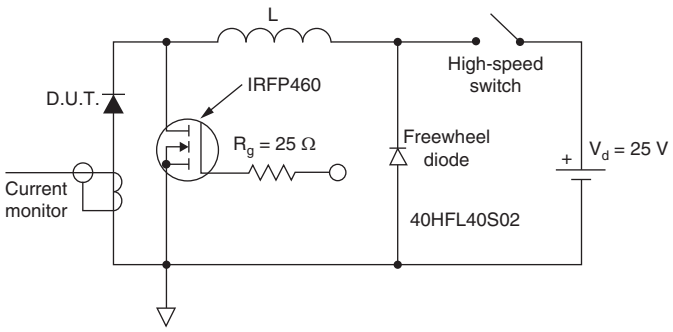
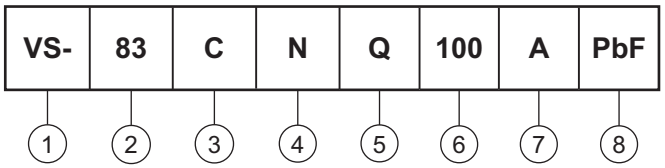


Fig. 8 - Unclamped Inductive Test Circuit

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (80 A)

3 - Circuit configuration:
C = common cathode

4 - Package:
N = D-61

5 - Schottky “Q” series

6 - Voltage ratings
080 = 80 V
100 = 100 V

7 - Package style:
- A = D-61-8
- ASM = D-61-8-SM
- ASL = D-61-8-SL

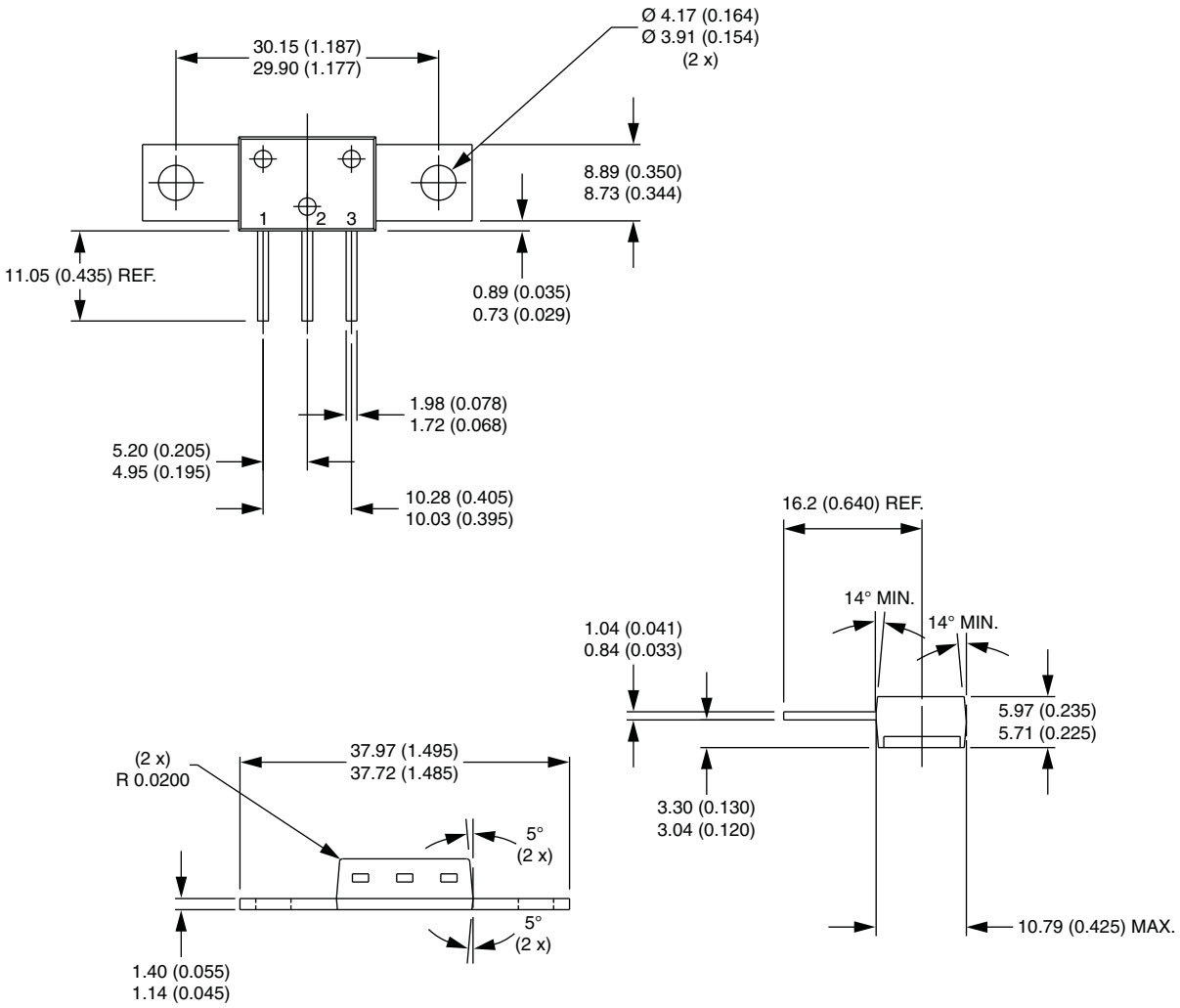
8 - - None = standard production
- PbF = lead (Pb)-free

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

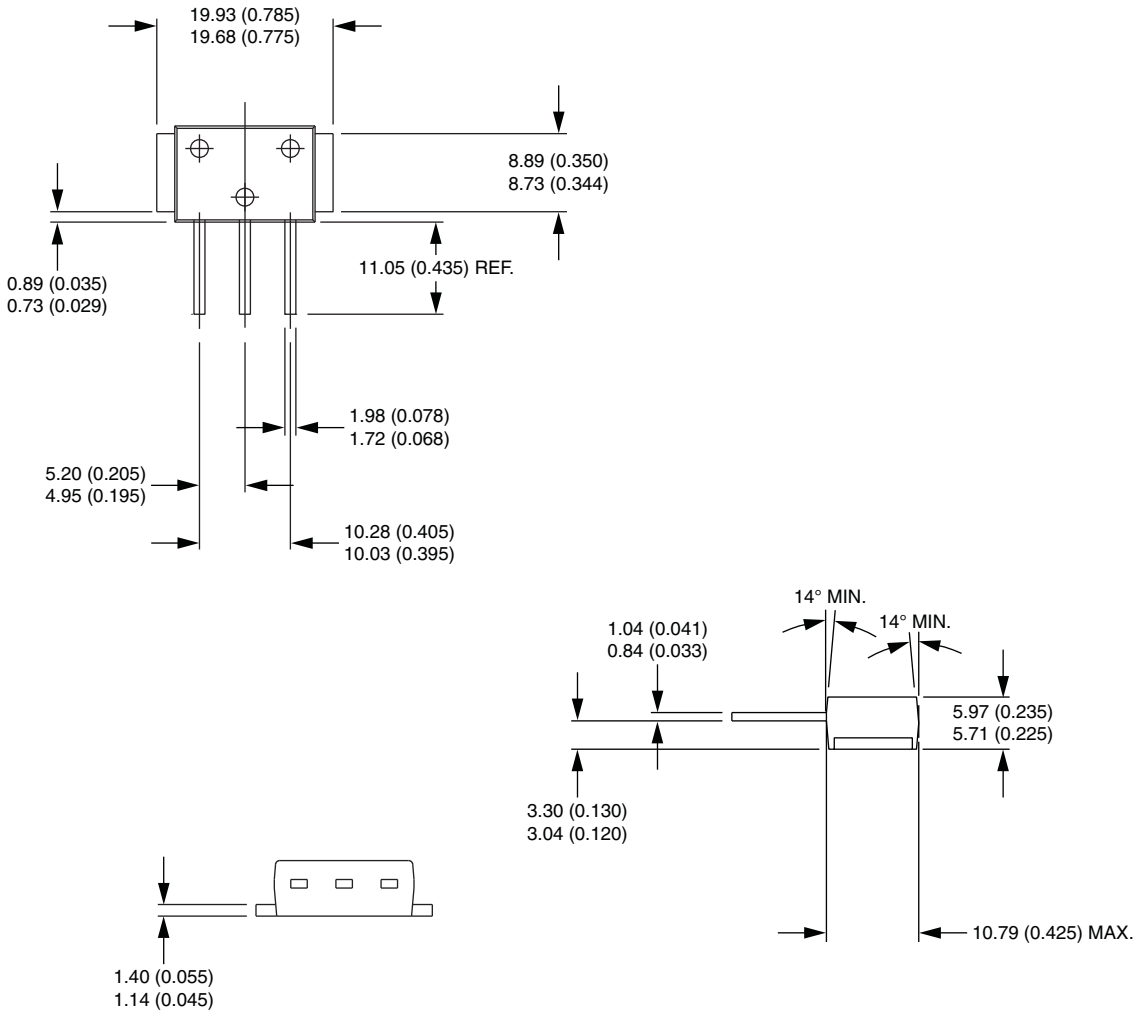
LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95354
Part marking information	www.vishay.com/doc?95356
SPICE model	www.vishay.com/doc?95290

D-61-8, D-61-8-SM, D-61-8-SL

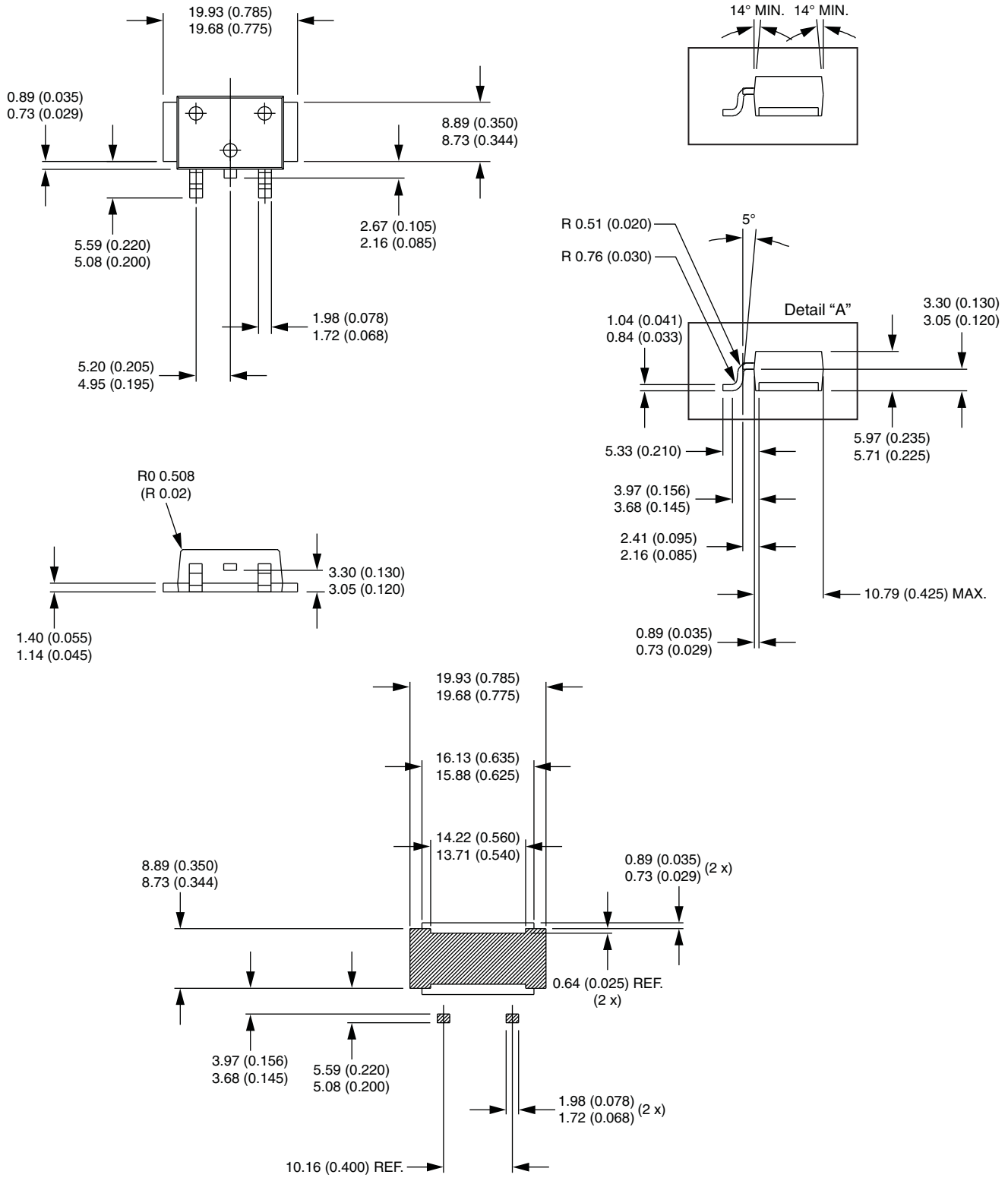
DIMENSIONS - D-61-8 in millimeters (inches)



DIMENSIONS - D-61-8-SM in millimeters (inches)



DIMENSIONS - D-61-8-SL in millimeters (inches)



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