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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS					
	V _{FM} ⁽¹⁾	3 A	T _{.1} = 25 °C	0.45	V				
Maximum forward voltage drop per leg		6 A	1j=25 0	0.52					
See fig. 1		3 A	T.I = 125 °C	0.35					
		6 A	1j = 125 C	0.46					
Maximum reverse	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	2	mA				
leakage current per leg See fig. 2		T _J = 125 °C	V _R = nateu V _R	50					
Threshold voltage	V _{F(TO)}	$T_{.1} = T_{.1}$ maximum	0.22	V					
Forward slope resistance	r _t	i j = i j maximum	32.86	mΩ					
Typical junction capacitance per leg	CT	$V_R = 5 V_{DC}$, (test signal ran	290	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 m	5.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C			
Maximum thermal resistance,	per leg	P	DC operation See fig. 4	4.7	°C/W			
junction to case	per device	R _{thJC}		2.35				
Approximate weight				0.3	g			
Approximate weight				0.01	oz.			
Marking device			Case style D-PAK (similar to TO-252AA)	6CWC	03FN			

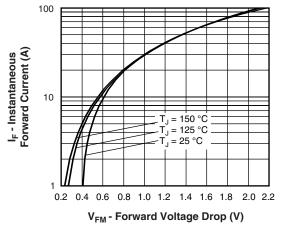
Note

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

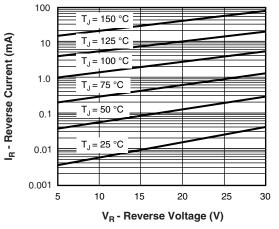


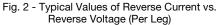
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Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





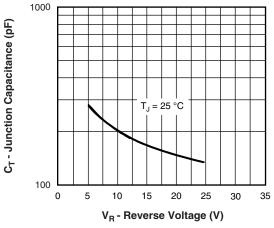


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

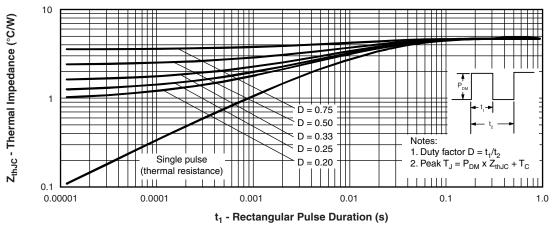


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

 Revision: 28-Jan-11
 3
 Document Number: 94247

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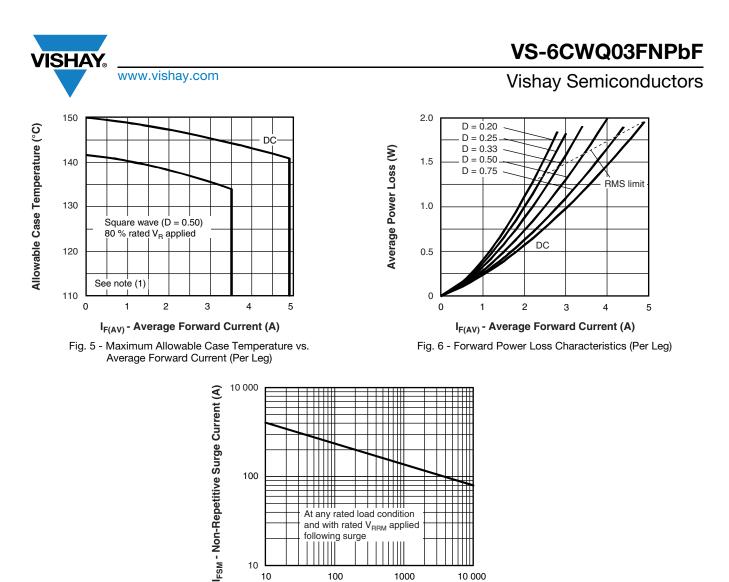




Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

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<sup>(1)</sup> Formula used: T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};

Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D) (see fig. 6);

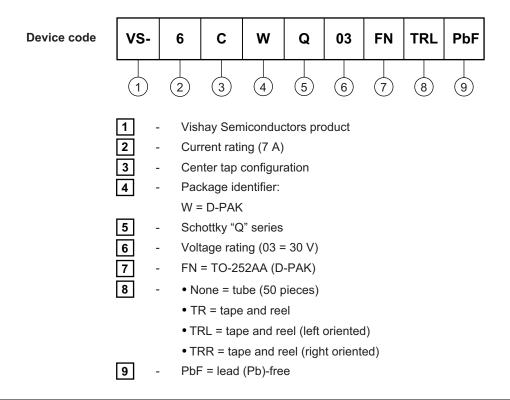
Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R
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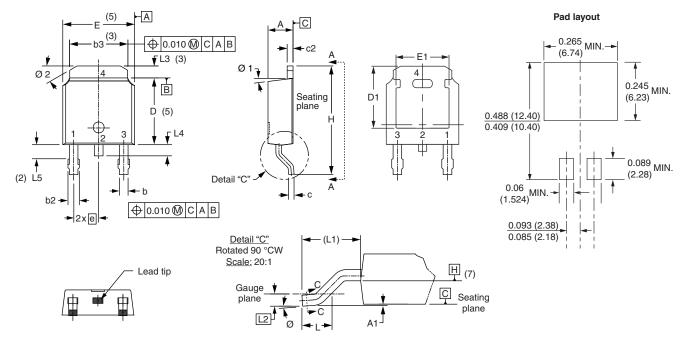
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
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SPICE model	www.vishay.com/doc?95437					





D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29 BSC		0.090 BSC		
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74 BSC		0.108 REF.		
b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.020 BSC		
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC outline TO-252AA

Document Number: 95016



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