

Vishay Semiconductors

### **ELECTRICAL SPECIFICATIONS**

PARAMETER	R SYMBOL TEST CONDITIONS		VALUES	UNITS	
	) ( (1)	20 A	T.I = 25 °C	0.81	V
Maximum forward voltage drop per leg		40 A	1j=25 C	0.98	
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	20 A	T 405.00	0.67	
		40 A	T <sub>J</sub> = 125 °C	0.81	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1	mA
See fig. 2		T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	11	
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = T <sub>J</sub> maximum -		0.71	V
Forward slope resistance	r <sub>t</sub>			0.43	mΩ
Maximum junction capacitance per leg	CT	$V_R$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1480	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

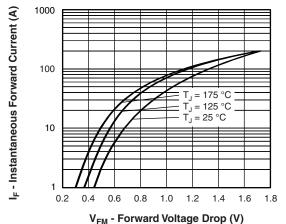
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

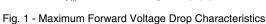
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ge	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistance junction to case per leg	Aximum thermal resistance, unction to case per leg			2.0		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf ⋅ cm	
	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	43CT	Q100	



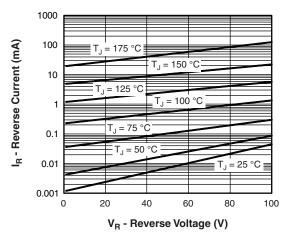
# VS-43CTQ100PbF, VS-43CTQ100-N3

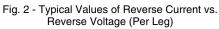
### **Vishay Semiconductors**











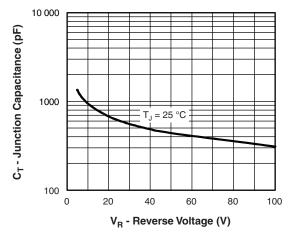
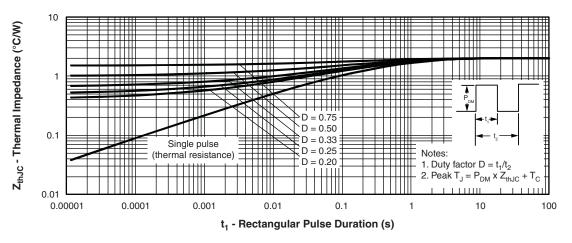


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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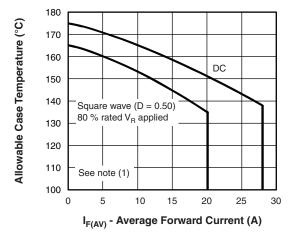
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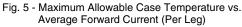
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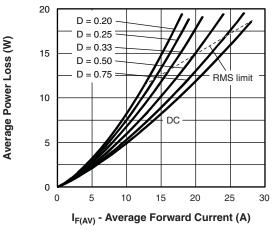


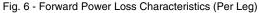
## VS-43CTQ100PbF, VS-43CTQ100-N3

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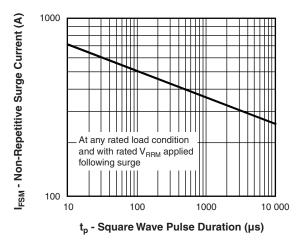


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

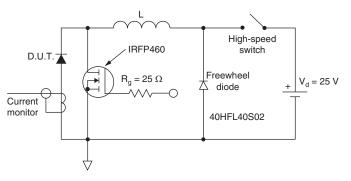


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$ 

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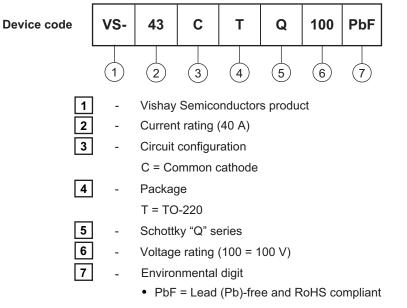
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# VS-43CTQ100PbF, VS-43CTQ100-N3



### **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**



-N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-43CTQ100PbF	50	1000	Antistatic plastic tube		
VS-43CTQ100-N3	50	1000	Antistatic plastic tube		

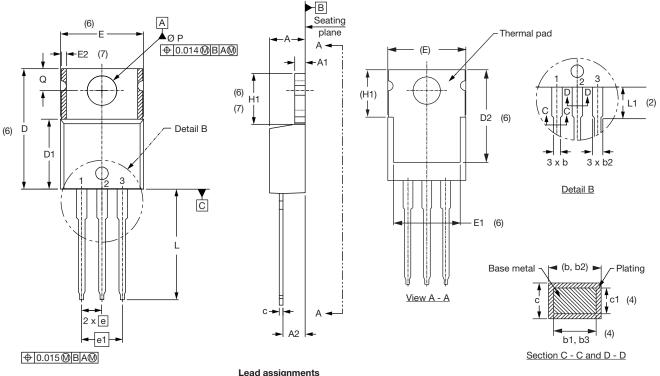
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
Part marking information	TO-220ABPbF	www.vishay.com/doc?95225		
	TO-220AB-N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95065		

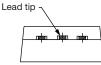


**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





ead.	assignments

**Diodes** 

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed  $0.127 \text{ mm} (0.005^{\circ})$  per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

SYMBOL		MILLIMETERS		INC	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Е		10.11	10.51	0.398	0.414	3, 6
E1		6.86	8.89	0.270	0.350	6
E2		-	0.76	-	0.030	7
е		2.41	2.67	0.095	0.105	
e1		4.88	5.28	0.192	0.208	
H1		6.09	6.48	0.240	0.255	6, 7
L		13.52	14.02	0.532	0.552	
L1		3.32	3.82	0.131	0.150	2
ØΡ	)	3.54	3.73	0.139	0.147	
Q		2.60	3.00	0.102	0.118	
θ		90° to 93°		90° t	o 93°	
θ		90° t	o 93°		90° t	90° to 93°

Conforms to JEDEC outline TO-220AB

- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

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