CAN-Transceiver

TLE 6250 G

TLE 6250 G V33

TLE 6250 R V33

Final Data Sheet

Features

- CAN data transmission rate up to 1 MBaud
- Receive-only Mode and Stand-by Mode
- Suitable for 12 V and 24 V applications
- Excellent EMC performance (very high immunity and very low emission)
- Version for 5 V and 3.3 V micro controllers
- Bus pins are short circuit proof to ground and battery voltage
- Overtemperature protection
- Very wide temperature range (- 40°C up to 150°C)

P-DSO-8-3

Type	Ordering Code	Package
TLE 6250 G	Q67006-A9427	P-DSO-8-3
TLE 6250 C	Q67000-A9594	(chip)
TLE 6250 G V33	Q67006-A9523	P-DSO-8-3
TLE 6250 C V33	Q67000-A9538	(chip)
TLE 6250 R V33	Q67006-A9639	P-DSO-8-3

Description

The HS CAN-transceiver familiy TLE 6250 (TLE 6250 G, TLE 6250 G V33, TLE 6250 R V33) are monolithic integrated circuits that are available as bare die as well as in a P-DSO-8-3 package. The ICs are optimized for high speed differential mode data transmission in automotive and industrial applications and they are compatible to ISO/ DIS 11898. They work as an interface between the CAN protocol controller and the physical differential bus in both, 12 V and 24 V systems.

The ICs are based on the **S**mart **P**ower **T**echnology SPT® which allows bipolar and CMOS control circuitry in accordance with DMOS power devices existing on the same monolithic circuit. The TLE 6250 G is designed to withstand the severe conditions of automotive applications and provides excellent EMC performance.

Note:
There are three versions available (refer to next page).

TLE 6250 G

5V logic I/O version: RxD,TxD, INH, RM. TwoControl pins (RM, INH) and 3 operation modes: Normal Mode, Stand-by Mode and Receive Only Mode.

TLE 6250 G V33

3.3V logic I/O version (logic I/O voltage adaptive to V_{33} pin within the range 3.3V to 5V): RxD, TxD, INH. One control pin (INH) and two operation modes: Normal Mode and Standby Mode.

TLE 6250 R V33

3.3V logic I/O version (logic I/O voltage adaptive to V_{33} pin within the range 3.3V to 5V): RxD, TxD, RM. One control pin (RM) and two operation modes: Normal Mode and Receive Only Mode.

Pin Configuration

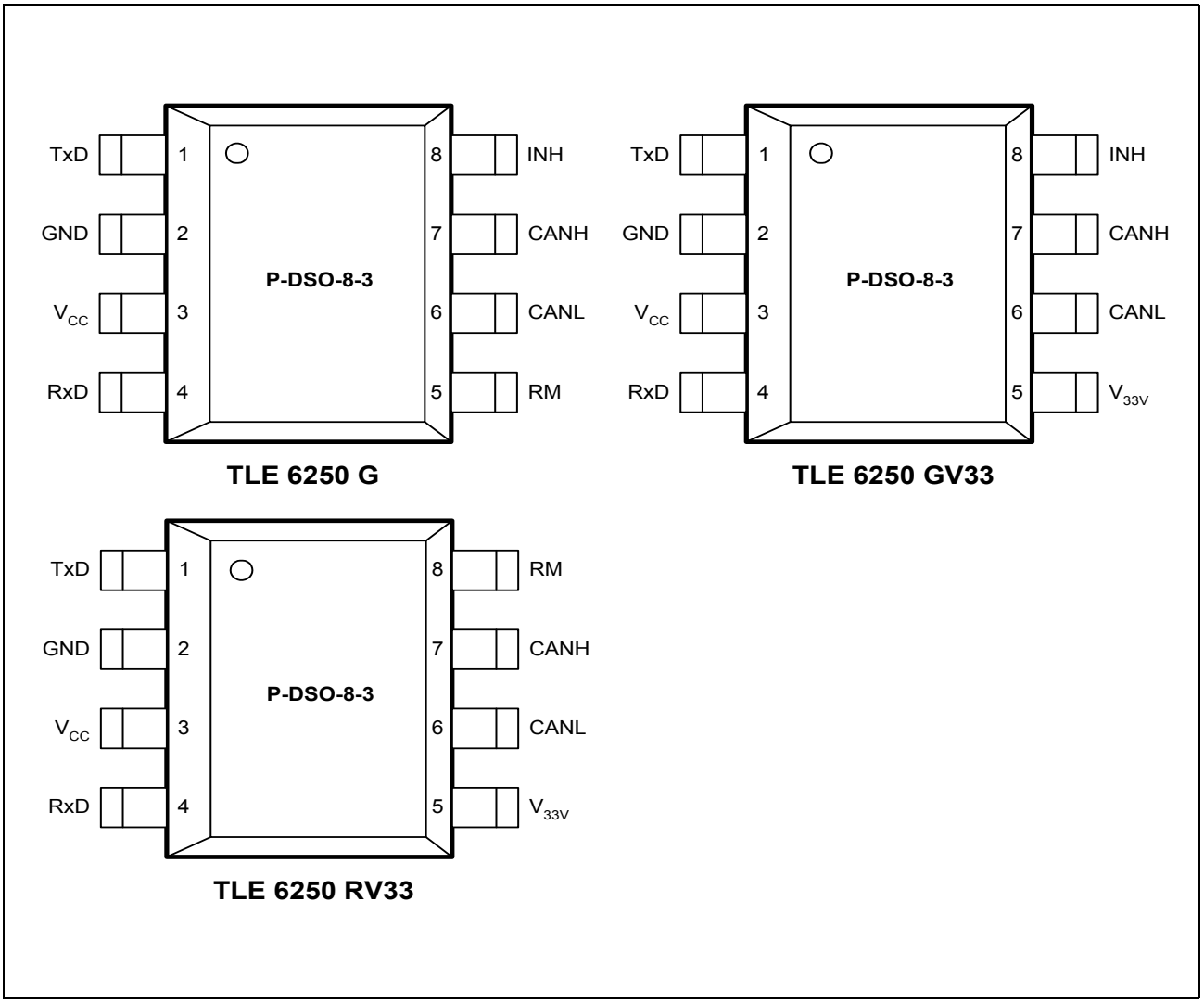


Figure 1 **Pin Configuration** (top view)

Pin Definitions and Functions

Pin No.	Symbol	Function

TLE 6250 G

1	**TxD**	**CAN transmit data input;** 20 kΩ pull up, LOW in dominant state
2	**GND**	**Ground;**
3	**V_{CC}**	**5 V Supply input;**
4	**RxD**	**CAN receive data output;** LOW in dominant state, integrated pull up
5	**RM**	**Receive-only input;** control input, 20 kΩ pull up, set LOW to activate RxD-only mode
6	**CANL**	**Low line I/O;** LOW in dominant state
7	**CANH**	**High line I/O;** HIGH in dominant state
8	**INH**	**Inhibit Input;** control input, 20 kΩ pull up, set LOW for normal mode

TLE 6250 G V33

1	**TxD**	**CAN transmit data input;** 20 kΩ pull up, LOW in dominant state
2	**GND**	**Ground;**
3	**V_{CC}**	**5 V Supply input;**
4	**RxD**	**CAN receive data output;** LOW in dominant state, integrated pull up
5	**V_{33V}**	**Logic supply input;** 3.3 V OR 5V microcontroller logic supply can be connected here! The digital I/Os of the TLE6250GV33 adopt to the connected microcontroller logic supply at V_{33V}
6	**CANL**	**Low line I/O;** LOW in dominant state
7	**CANH**	**High line I/O;** HIGH in dominant state
8	**INH**	**Inhibit Input;** control input, 20 kΩ pull up, set LOW for normal mode

Pin No.	Symbol	Function

TLE 6250 R V33

1	**TxD**	**CAN transmit data input;** 20 kΩ pull up, LOW in dominant state
2	**GND**	**Ground;**
3	**V_{CC}**	**5 V Supply input;**
4	**RxD**	**CAN receive data output;** LOW in dominant state, integrated pull up
5	**V_{33V}**	**Logic supply input;** 3.3 V OR 5V microcontroller logic supply can be connected here! The digital I/Os of the TLE6250RV33 adopt to the connected microcontroller logic supply at V_{33V}
6	**CANL**	**Low line I/O;** LOW in dominant state
7	**CANH**	**High line I/O;** HIGH in dominant state
8	**RM**	**Receive-only input;** control input, 20 kΩ pull up, set LOW to activate RxD-only mode

Functional Block Diagram

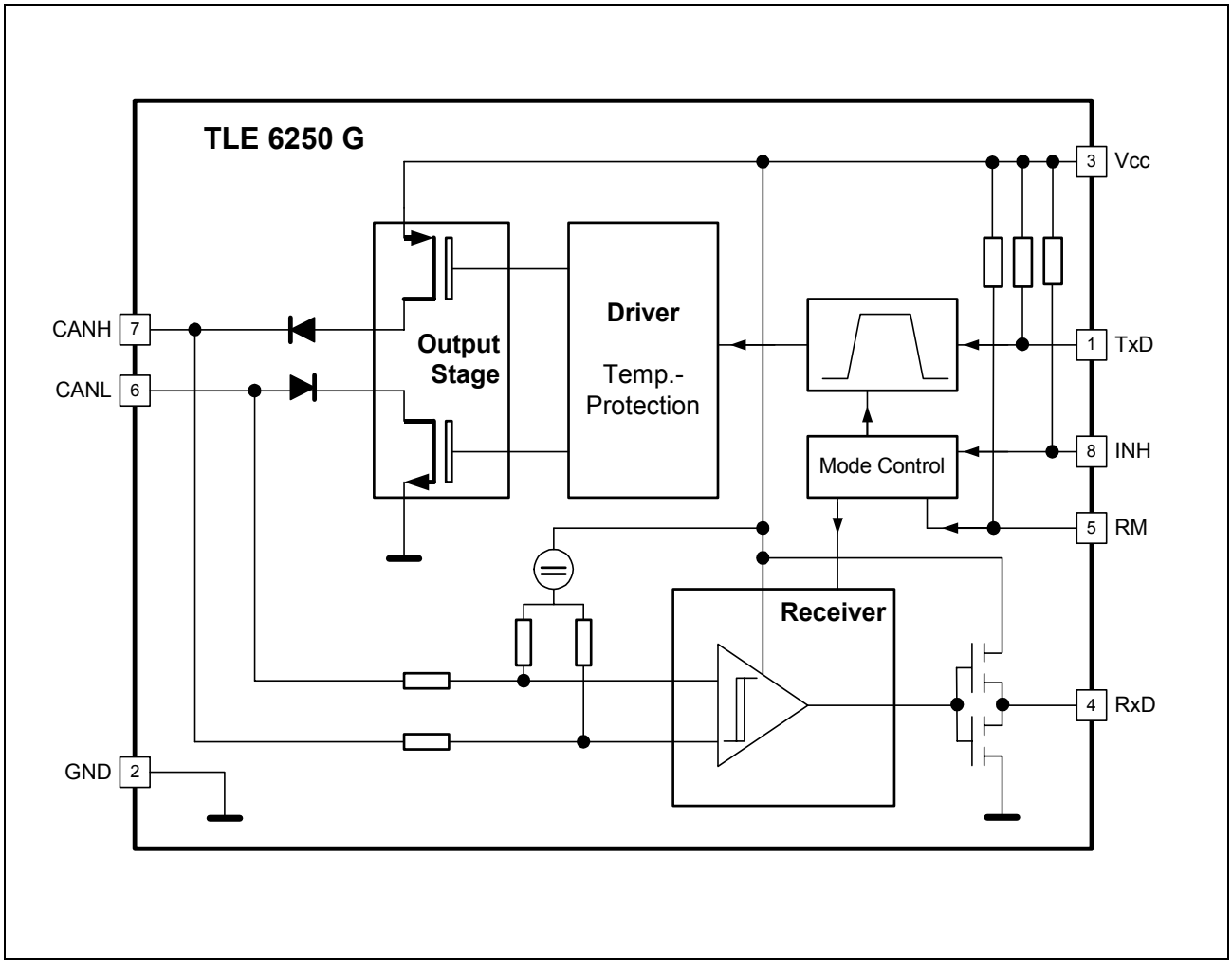


Figure 2 Block Diagram TLE 6250 G

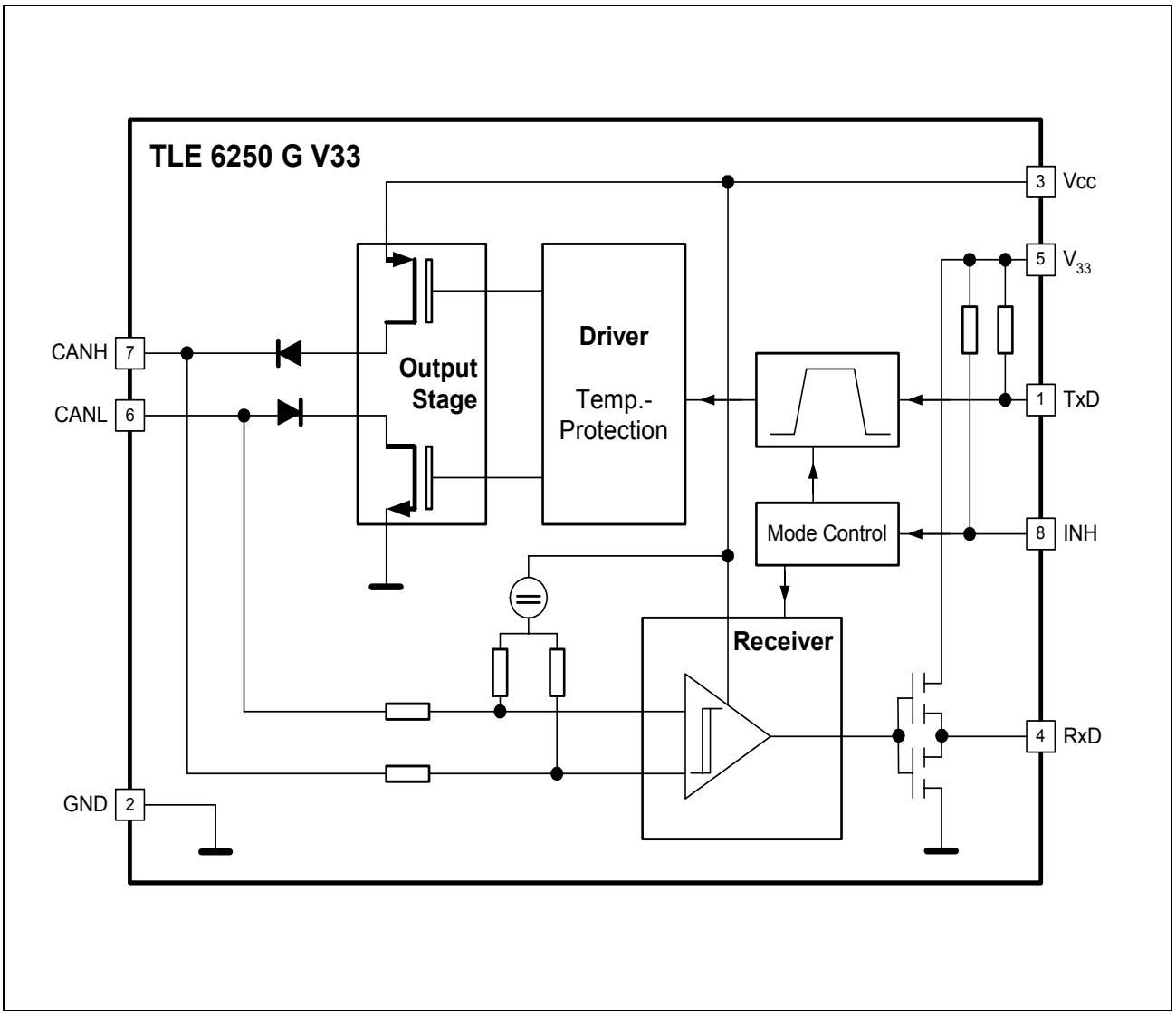


Figure 3 Block Diagram TLE 6250 G V33

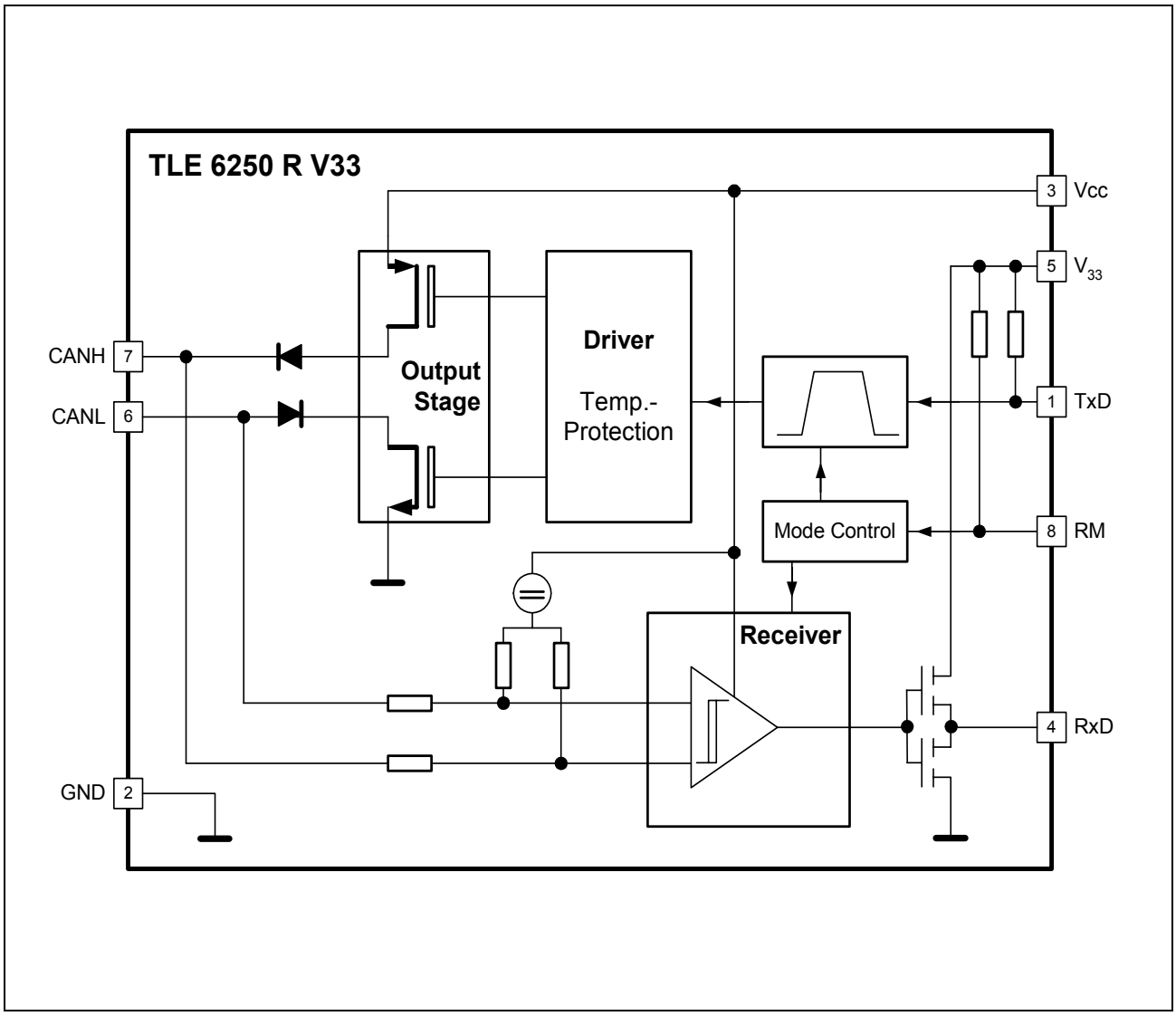


Figure 4 Block Diagram TLE 6250 R V33

Application Information

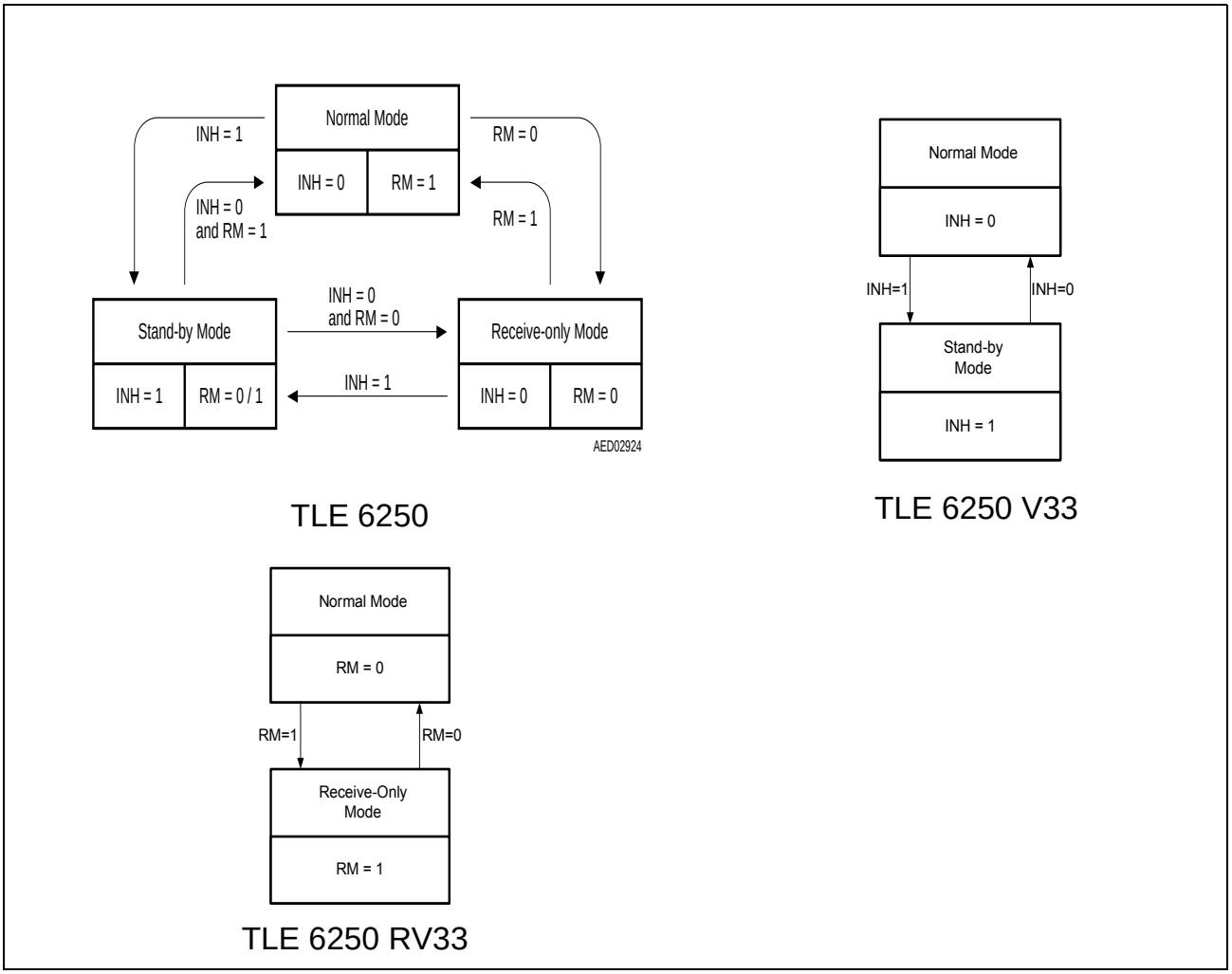


Figure 5 Mode State Diagram

Both, the TLE 6250 G as well as the TLE 6250 C offer three different operation modes (see **Figure 5**), controlled by the INH and RM pin. The TLE 6250 G V33 and the TLE 6250 R V33 offer only two modes, controlled by the INH (GV33) and RM (RV33) pin respectively.

In the normal mode the device is able to receive and to transmit messages whereas in the receive-only mode signals at the TxD input are not transmitted to the CAN bus. The receive-only mode can be used for diagnostic purposes (to check the bus connections between the nodes) as well as to prevent the bus being blocked by a faulty permanent dominant TxD input signal. The stand-by mode is a low power mode that disables both, the receiver as well as the transmitter.

In case the receive-only feature is not used the RM pin has to be left open. When the stand-by mode is not used the INH pin has to be connected to ground level in order to switch the TLE 6250 G in normal mode.

Application Information for the 3.3V Versions

The TLE 6250 G V33 and TLE 6250 R V33 can be used for both; 3.3V and 5V microcontroller logic supply, as shown below in **Figure 6** for the TLE 6250 G V33. Don´t apply external resistors between the power supply and this pin. This may cause a voltage drop and so reduce the available voltage at this pin.

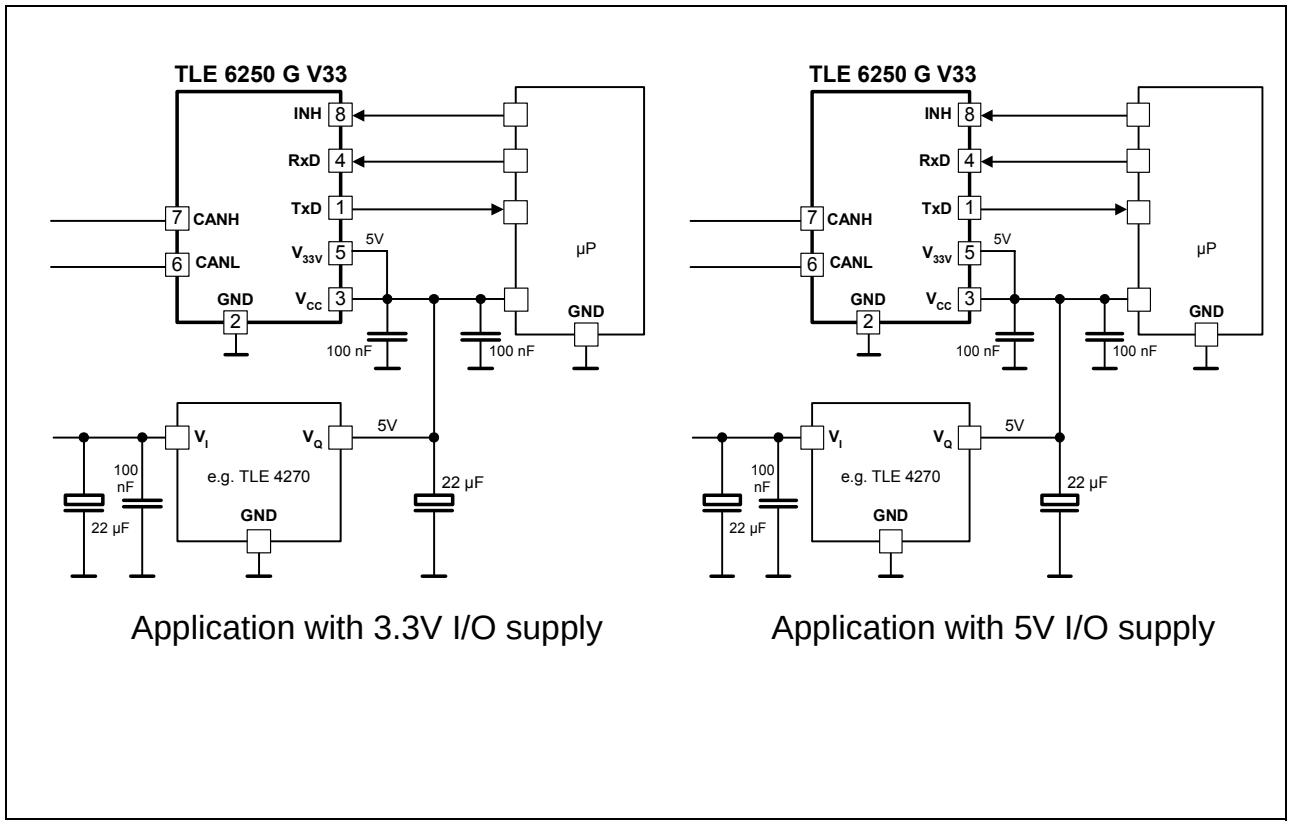


Figure 6 Application Circuits TLE 6250 G V33 used for 3.3V and 5V logic

Electrical Characteristics TLE6250 G (5V Version)

Electrical Characteristics

Absolute Maximum Ratings

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Voltages					
Supply voltage	V_{CC}	– 0.3	6.5	V	–
CAN input voltage (CANH, CANL)	$V_{CANH/L}$	– 40	40	V	–
Logic voltages at INH, RM, TxD, RxD	V_I	– 0.3	V_{CC}	V	0 V < V_{CC} < 5.5 V
Electrostatic discharge voltage at CANH,CANL	V_{ESD}	– 6	6	kV	human body model (100 pF via 1.5 kΩ)
Electrostatic discharge voltage	V_{ESD}	– 2	2	kV	human body model (100 pF via 1.5 kΩ)
Temperatures					
Junction temperature	T_j	– 40	160	°C	–

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Supply voltage	V_{CC}	4.5	5.5	V	–
Junction temperature	T_j	– 40	150	°C	–

Thermal Resistances

Junction ambient	$R_{thj\text{-}a}$	–	185	K/W	–

Thermal Shutdown (junction temperature)

Thermal shutdown temperature	T_{jsD}	160	200	°C	10 °C hysteresis

Electrical Characteristics

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Current Consumption						
Current consumption	I_{CC}	–	6	10	mA	recessive state; $V_{TxD} = V_{CC}$
Current consumption	I_{CC}	–	45	70	mA	dominant state; V_{TxD} = 0 V
Current consumption	I_{CC}	–	6	10	mA	receive-only mode; RM = low
Current consumption	$I_{CC,stb}$	–	1	10	µA	stand-by mode; TxD = RM = high
Receiver Output R×D						
HIGH level output current	$I_{RD,H}$	–	-4	-2	mA	$V_{RD} = 0.8 \times V_{CC}$, V_{diff} < 0.4 V note 1)
LOW level output current	$I_{RD,L}$	2	4	–	mA	$V_{RD} = 0.2 \times V_{CC}$, V_{diff} > 1 V note 1)
Transmission Input T×D						
HIGH level input voltage threshold	$V_{TD,H}$	–	0.5× V_{CC}	0.7× V_{CC}	V	recessive state;
LOW level input voltage threshold	$V_{TD,L}$	0.3× V_{CC}	0.4× V_{CC}	–	V	dominant state
TxD pull up resistance	R_{TD}	10	25	50	kΩ	–

note1) $V_{diff} = V_{CANH} - V_{CANL}$

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Inhibit Input (pin INH)						
HIGH level input voltage threshold	$V_{INH,H}$	–	0.5× V_{CC}	0.7× V_{CC}	V	stand-by mode;
LOW level input voltage threshold	$V_{INH,L}$	0.3× V_{CC}	0.4× V_{CC}	–	V	normal mode
INH pull up resistance	R_{INH}	10	25	50	kΩ	–
Receive only Input (pin RM) (5V version only)						
HIGH level input voltage threshold	$V_{RM,H}$	–	0.5× V_{CC}	0.7× V_{CC}	V	normal mode;
LOW level input voltage threshold	$V_{RM,L}$	0.3× V_{CC}	0.4× V_{CC}	–	V	receive-only mode
RM pull up resistance	R_{RM}	10	25	50	kΩ	–

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Receiver						
Differential receiver threshold voltage, recessive to dominant edge	$V_{diff,d}$	–	0.75	0.90	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Differential receiver threshold voltage dominant to recessive edge	$V_{diff,r}$	0.50	0.60	–	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Common Mode Range	CMR	-20	–	25	V	V_{CC} = 5V
Differential receiver hysteresis	$V_{diff,hys}$	–	150	–	mV	–
CANH, CANL input resistance	R_i	10	20	30	kΩ	recessive state
Differential input resistance	R_{diff}	20	40	60	kΩ	recessive state

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Transmitter						
CANL/CANH recessive output voltage	$V_{CANL/H}$	0.4 × V_{CC}	–	0.6 × V_{CC}	V	V_{TxD} = V_{CC}
CANH, CANL recessive output voltage difference V_{diff} = V_{CANH} – V_{CANL} no load; (see note 2)	V_{diff}	- 1	–	0.05	V	V_{TxD} = V_{CC}
CANL dominant output voltage	V_{CANL}	–	–	2.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH dominant output voltage	V_{CANH}	2.8	–	–	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH, CANL dominant output voltage difference V_{diff} = V_{CANH} – V_{CANL}	V_{diff}	1.5	–	3.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANL short circuit current	I_{CANLsc}	50	120	200	mA	$V_{CANLshort}$ = 18 V
		–	150	–	mA	$V_{CANLshort}$ = 36 V
CANH short circuit current	I_{CANHsc}	-200	-120	-50	mA	$V_{CANHshort}$ = 0 V
CANH short circuit current	I_{CANHsc}	–	-120	–	mA	$V_{CANHshort}$ = -5 V
Output current	$I_{CANH,lk}$	-50	-300	-400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -7 V
		-50	-100	-150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -2 V
Output current	$I_{CANH,lk}$	50	280	400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 7 V
		50	100	150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 2 V

note 2) deviation from ISO/DIS 11898

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Dynamic CAN-Transceiver Characteristics						
Propagation delay TxD-to-RxD LOW (recessive to dominant)	$t_{d(L),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD-to-RxD HIGH (dominant to recessive)	$t_{d(H),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD LOW to bus dominant	$t_{d(L),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay TxD HIGH to bus recessive	$t_{d(H),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay bus dominant to RxD LOW	$t_{d(L),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay bus recessive to RxD HIGH	$t_{d(H),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF

Electrical Characteristics TLE6250 GV33 (3.3V Version) (INH pin)

Electrical Characteristics

Absolute Maximum Ratings

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Voltages					
Supply voltage	V_{CC}	– 0.3	6.5	V	–
3.3 V supply	V_{33V}	– 0.3	5.5	V	–
CAN input voltage (CANH, CANL)	$V_{CANH/L}$	– 40	40	V	–
Logic voltages at INH, RM, TxD, RxD	V_I	– 0.3	V_{CC}	V	0 V < V_{CC} < 5.5 V
Electrostatic discharge voltage at CANH,CANL	V_{ESD}	– 6	6	kV	human body model (100 pF via 1.5 kΩ)
Electrostatic discharge voltage	V_{ESD}	– 2	2	kV	human body model (100 pF via 1.5 kΩ)
Temperatures					
Junction temperature	T_j	– 40	160	°C	–

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Supply voltage	V_{CC}	4.5	5.5	V	–
3.3 V supply voltage	V_{33V}	3.0	5.5	V	–
Junction temperature	T_j	– 40	150	°C	–

Thermal Resistances

Junction ambient	R_{thj-a}	–	185	K/W	–

Thermal Shutdown (junction temperature)

Thermal shutdown temperature	T_{jsD}	160	200	°C	10 °C hysteresis

Electrical Characteristics

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Current Consumption (3.3V version)						
Current consumption	I_{CC+33V}	–	6	10	mA	recessive state; $V_{TxD} = V_{33V}$
Current consumption	I_{CC+33V}	–	45	70	mA	dominant state; V_{TxD} = 0 V
Current consumption	I_{33V}	–	–	2	mA	
Current consumption	$I_{CC+33V,stb}$	–	1	10	µA	stand-by mode TxD = high
Receiver Output R×D						
HIGH level output current	$I_{RD,H}$	–	-2	-1	mA	$V_{RD} = 0.8 \times V_{33V}$, V_{diff} < 0.4 V note 1)
LOW level output current	$I_{RD,L}$	1	2	–	mA	$V_{RD} = 0.2 \times V_{33V}$, V_{diff} > 1 V note 1)
Transmission Input T×D						
HIGH level input voltage threshold	$V_{TD,H}$	–	0.55× V_{33V}	0.7× V_{33V}	V	recessive state;
LOW level input voltage threshold	$V_{TD,L}$	0.3× V_{33V}	0.45× V_{33V}	–	V	dominant state;
TxD pull up resistance	R_{TD}	10	25	50	kΩ	–

note1) $V_{diff} = V_{CANH} - V_{CANL}$

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Inhibit Input (pin INH)						
HIGH level input voltage threshold	$V_{INH,H}$	–	0.55× V_{33V}	0.7× V_{33V}	V	stand-by mode;
LOW level input voltage threshold	$V_{INH,L}$	0.3× V_{33V}	0.45× V_{33V}	–	V	normal mode;
INH pull up resistance	R_{INH}	10	25	50	kΩ	–

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Receiver						
Differential receiver threshold voltage, recessive to dominant edge	$V_{diff,d}$	–	0.75	0.90	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Differential receiver threshold voltage dominant to recessive edge	$V_{diff,r}$	0.50	0.60	–	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Common Mode Range	CMR	-20	–	25	V	V_{CC} = 5V
Differential receiver hysteresis	$V_{diff,hys}$	–	150	–	mV	–
CANH, CANL input resistance	R_i	10	20	30	kΩ	recessive state
Differential input resistance	R_{diff}	20	40	60	kΩ	recessive state

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Transmitter						
CANL/CANH recessive output voltage	$V_{CANL/H}$	0.4 × V_{CC}	–	0.6 × V_{CC}	V	V_{TxD} = V_{33V}
CANH, CANL recessive output voltage difference V_{diff} = V_{CANH} – V_{CANL} no load; (see note 2)	V_{diff}	- 1	–	0.05	V	V_{TxD} = V_{33V}
CANL dominant output voltage	V_{CANL}	–	–	2.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH dominant output voltage	V_{CANH}	2.8	–	–	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH, CANL dominant output voltage difference V_{diff} = V_{CANH} – V_{CANL}	V_{diff}	1.5	–	3.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANL short circuit current	I_{CANLsc}	50	120	200	mA	$V_{CANLshort}$ = 18 V
		–	150	–	mA	$V_{CANLshort}$ = 36 V
CANH short circuit current	I_{CANHsc}	-200	-120	-50	mA	$V_{CANHshort}$ = 0 V
CANH short circuit current	I_{CANHsc}	–	-120	–	mA	$V_{CANHshort}$ = -5 V
Output current	$I_{CANH,lk}$	-50	-300	-400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -7 V
		-50	-100	-150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -2 V
Output current	$I_{CANH,lk}$	50	280	300	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 7 V
		50	100	150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 2 V

note 2) deviation from ISO/DIS 11898

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Dynamic CAN-Transceiver Characteristics						
Propagation delay TxD-to-RxD LOW (recessive to dominant)	$t_{d(L),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD-to-RxD HIGH (dominant to recessive)	$t_{d(H),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD LOW to bus dominant	$t_{d(L),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay TxD HIGH to bus recessive	$t_{d(H),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay bus dominant to RxD LOW	$t_{d(L),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay bus recessive to RxD HIGH	$t_{d(H),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF

Electrical Characteristics TLE6250 RV33 (3.3V Version) (RM pin)

Electrical Characteristics

Absolute Maximum Ratings

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Voltages					
Supply voltage	V_{CC}	– 0.3	6.5	V	–
3.3 V supply	V_{33V}	– 0.3	5.5	V	–
CAN input voltage (CANH, CANL)	$V_{CANH/L}$	– 40	40	V	–
Logic voltages at INH, RM, TxD, RxD	V_I	– 0.3	V_{CC}	V	0 V < V_{CC} < 5.5 V
Electrostatic discharge voltage at CANH,CANL	V_{ESD}	– 6	6	kV	human body model (100 pF via 1.5 kΩ)
Electrostatic discharge voltage	V_{ESD}	– 2	2	kV	human body model (100 pF via 1.5 kΩ)
Temperatures					
Junction temperature	T_j	– 40	160	°C	–

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.

Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Supply voltage	V_{CC}	4.5	5.5	V	–
3.3 V supply voltage	V_{33V}	3.0	5.5	V	–
Junction temperature	T_j	– 40	150	°C	–

Thermal Resistances

Junction ambient	R_{thj-a}	–	185	K/W	–

Thermal Shutdown (junction temperature)

Thermal shutdown temperature	T_{jsD}	160	200	°C	10 °C hysteresis

Electrical Characteristics

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Current Consumption (3.3V version)						
Current consumption	I_{CC+33V}	–	6	10	mA	recessive state; $V_{TxD} = V_{33V}$
Current consumption	I_{CC+33V}	–	45	70	mA	dominant state; V_{TxD} = 0 V
Current consumption	I_{33V}	–	–	2	mA	
Current consumption	$I_{CC+33V,stb}$	–	1	10	µA	stand-by mode TxD = high
Receiver Output R×D						
HIGH level output current	$I_{RD,H}$	–	-2	-1	mA	$V_{RD} = 0.8 \times V_{33V}$, V_{diff} < 0.4 V note 1)
LOW level output current	$I_{RD,L}$	1	2	–	mA	$V_{RD} = 0.2 \times V_{33V}$, V_{diff} > 1 V note 1)
Transmission Input T×D						
HIGH level input voltage threshold	$V_{TD,H}$	–	0.55× V_{33V}	0.7× V_{33V}	V	recessive state;
LOW level input voltage threshold	$V_{TD,L}$	0.3× V_{33V}	0.45× V_{33V}	–	V	dominant state;
TxD pull up resistance	R_{TD}	10	25	50	kΩ	–

note1) $V_{diff} = V_{CANH} - V_{CANL}$

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Receive only Input (pin RM)						
HIGH level input voltage threshold	$V_{RM,H}$	–	0.55× V_{33}	0.7× V_{33}	V	normal mode;
LOW level input voltage threshold	$V_{RM,L}$	0.3× V_{33}	0.45× V_{33}	–	V	receive-only mode
RM pull up resistance	R_{RM}	10	25	50	kΩ	–

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Receiver						
Differential receiver threshold voltage, recessive to dominant edge	$V_{diff,d}$	–	0.75	0.90	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Differential receiver threshold voltage dominant to recessive edge	$V_{diff,r}$	0.50	0.60	–	V	– 20 V < (V_{CANH}, V_{CANL}) < 25 V V_{diff} = V_{CANH} – V_{CANL}
Common Mode Range	CMR	-20	–	25	V	V_{CC} = 5V
Differential receiver hysteresis	$V_{diff,hys}$	–	150	–	mV	–
CANH, CANL input resistance	R_i	10	20	30	kΩ	recessive state
Differential input resistance	R_{diff}	20	40	60	kΩ	recessive state

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Bus Transmitter						
CANL/CANH recessive output voltage	$V_{CANL/H}$	0.4 × V_{CC}	–	0.6 × V_{CC}	V	V_{TxD} = V_{33V}
CANH, CANL recessive output voltage difference V_{diff} = V_{CANH} – V_{CANL} no load; (see note 2)	V_{diff}	- 1	–	0.05	V	V_{TxD} = V_{33V}
CANL dominant output voltage	V_{CANL}	–	–	2.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH dominant output voltage	V_{CANH}	2.8	–	–	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANH, CANL dominant output voltage difference V_{diff} = V_{CANH} – V_{CANL}	V_{diff}	1.5	–	3.0	V	V_{TxD} = 0 V; V_{CC} = 5 V
CANL short circuit current	I_{CANLsc}	50	120	200	mA	$V_{CANLshort}$ = 18 V
		–	150	–	mA	$V_{CANLshort}$ = 36 V
CANH short circuit current	I_{CANHsc}	-200	-120	-50	mA	$V_{CANHshort}$ = 0 V
CANH short circuit current	I_{CANHsc}	–	-120	–	mA	$V_{CANHshort}$ = -5 V
Output current	$I_{CANH,lk}$	-50	-300	-400	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -7 V
		-50	-100	-150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = -2 V
Output current	$I_{CANH,lk}$	50	280	300	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 7 V
		50	100	150	µA	V_{CC} = 0 V, V_{CANH} = V_{CANL} = 2 V

note 2) deviation from ISO/DIS 11898

Electrical Characteristics (cont'd)

4.5 V < V_{CC} < 5.5 V; (3.0 V < V_{33V} < 3.6 V for 3.3 V version); R_L = 60 Ω; V_{INH} < $V_{INH,ON}$; – 40 °C < T_j < 150 °C; all voltages with respect to ground; positive current flowing into pin; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Remarks
		min.	typ.	max.		
Dynamic CAN-Transceiver Characteristics						
Propagation delay TxD-to-RxD LOW (recessive to dominant)	$t_{d(L),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD-to-RxD HIGH (dominant to recessive)	$t_{d(H),TR}$	–	150	280	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay TxD LOW to bus dominant	$t_{d(L),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay TxD HIGH to bus recessive	$t_{d(H),T}$	–	100	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V
Propagation delay bus dominant to RxD LOW	$t_{d(L),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF
Propagation delay bus recessive to RxD HIGH	$t_{d(H),R}$	–	50	140	ns	C_L = 47 pF; R_L = 60 Ω; V_{CC} = 5 V; C_{RxD} = 20 pF

Diagrams

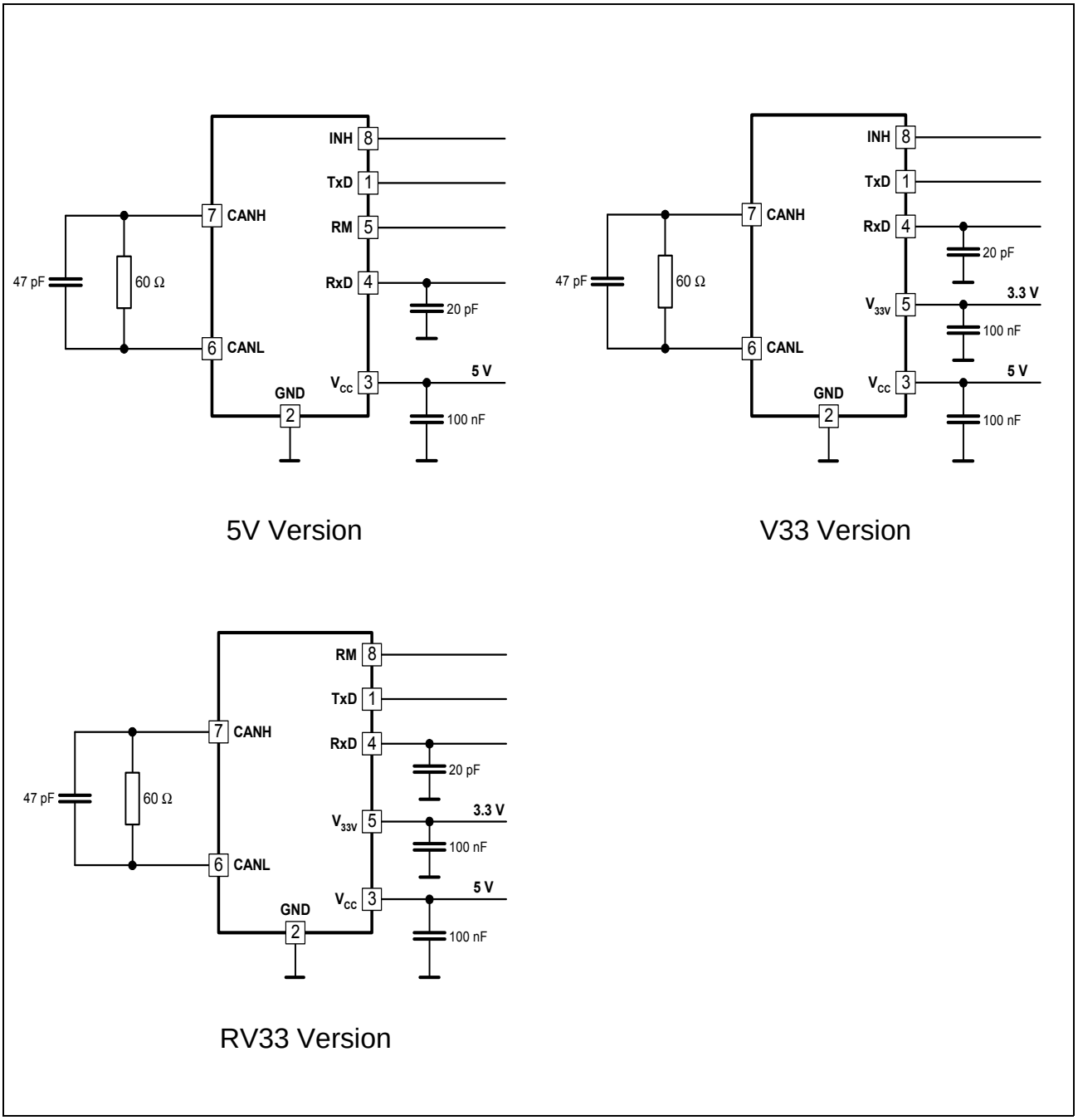


Figure 7 Test Circuits for Dynamic Characteristics

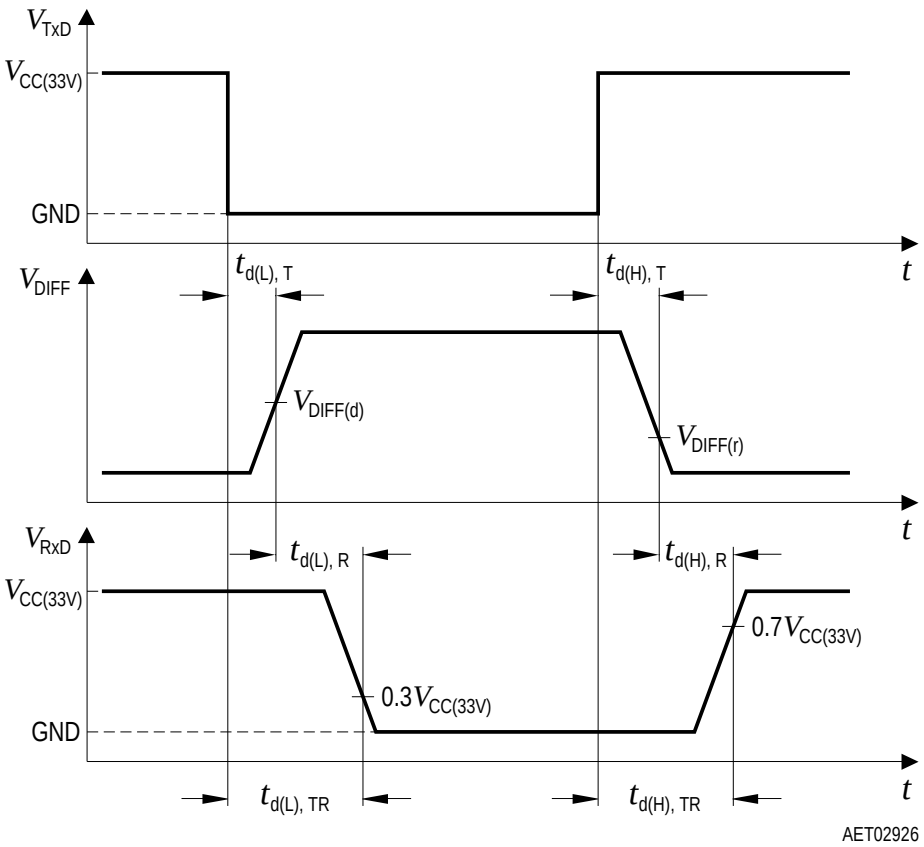


Figure 8 Timing Diagrams for Dynamic Characteristics

Application

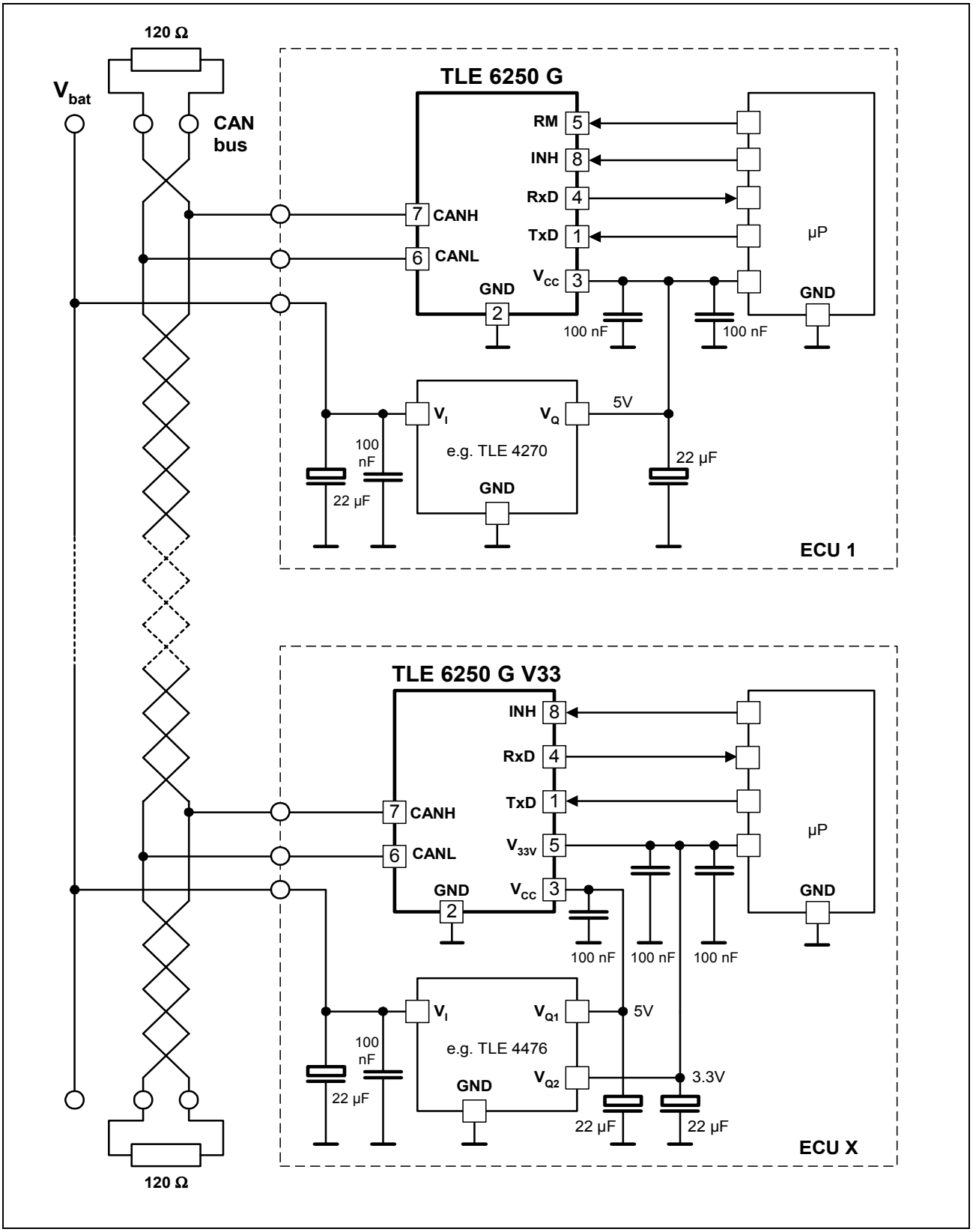


Figure 9 Application Circuit TLE 6250 with TLE 6250 V33

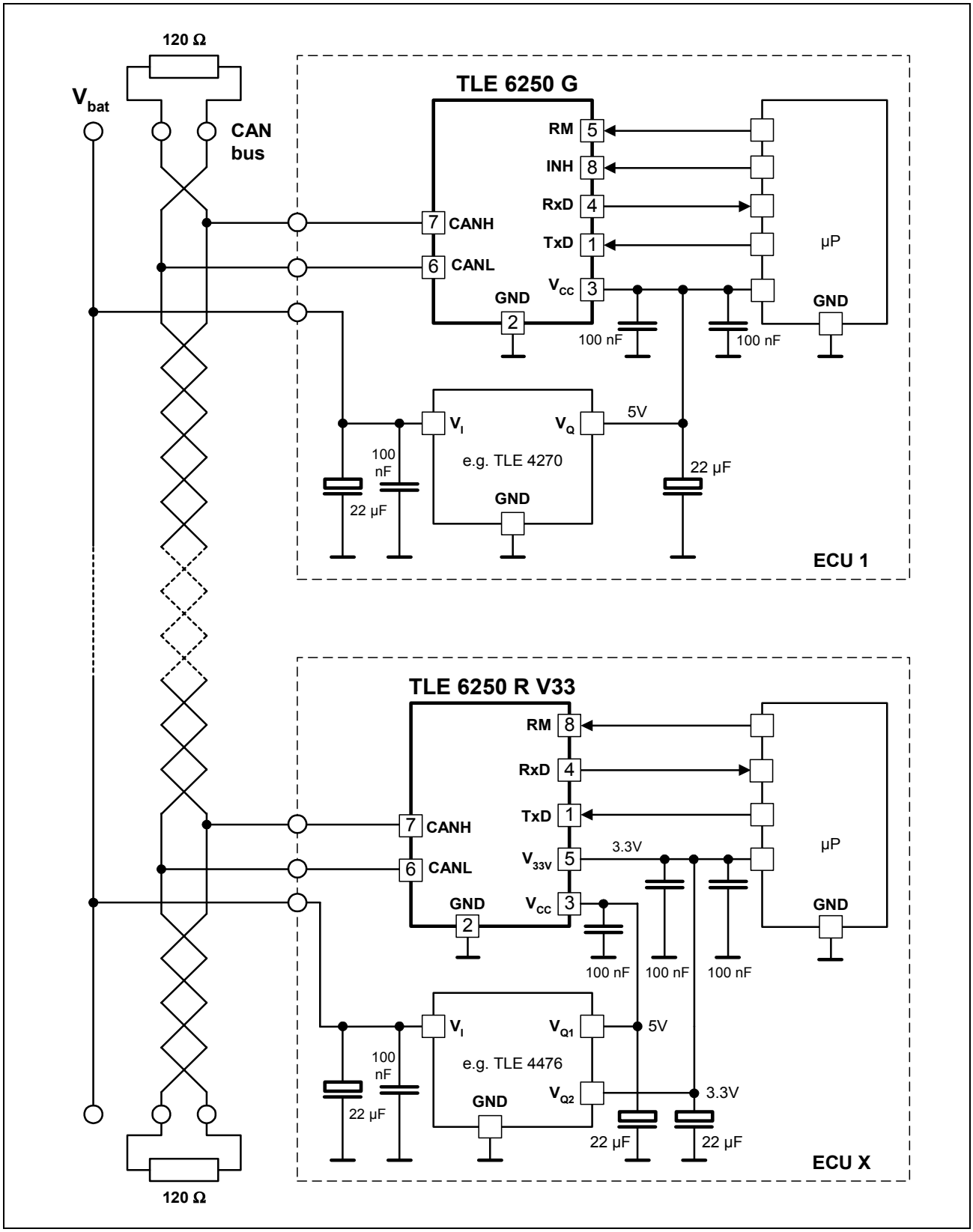
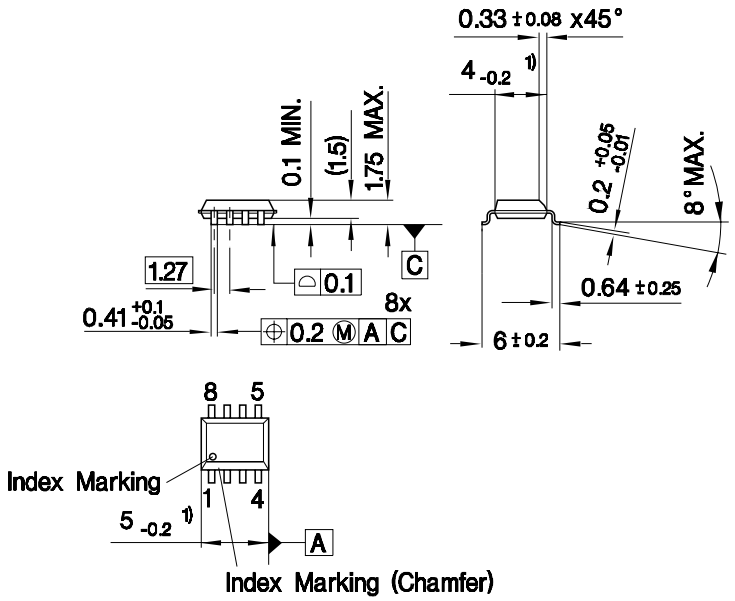


Figure 10 Application Circuit TLE 6250 with TLE 6250 RV33

Package Outlines

P-DSO-8-3
(Plastic Dual Small Outline Package)



1) Does not include plastic or metal protrusion of 0.15 max. per side

GPS09032

Sorts of Packing
Package outlines for tubes, trays etc. are contained in our Data Book “Package Information”
SMD = Surface Mounted Device

Dimensions in mm

Edition 2002-10-08

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