### PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM44602	TQFN-2.6×1.8-16L	-40°C to +85°C	SGM44602YTQA16/TR	44602 XXXXX	Tape and Reel, 3000

#### **MARKING INFORMATION**

NOTE: XXXXX = Date Code and Vendor Code.

XXXXX
Vendor Code
Date Code - Week
Date Code - Year

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

#### ABSOLUTE MAXIMUM RATINGS

V+ to GND	0V to 6V
Analog, Digital Voltage Range	0.3V to (V <sub>+</sub> ) + 0.3V
Continuous Current NO, NC, or COM	±100mA
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C

#### RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range .....-40°C to +85°C

#### **OVERSTRESS CAUTION**

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

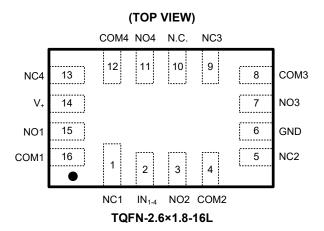
#### **ESD SENSITIVITY CAUTION**

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

#### **DISCLAIMER**

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

## **PIN CONFIGURATION**



## **PIN DESCRIPTION**

PIN	NAME	FUNCTION
1, 5, 9, 13	NC <sub>X</sub>	Normally-Closed Terminal.
2	IN <sub>1-4</sub>	Digital Control Pin to Connect the COM Terminal to the NO or NC Terminals.
6	GND	Ground.
10	N.C.	Not Internally Connected.
14	V <sub>+</sub>	Power Supply.
15, 3, 7, 11	NO <sub>X</sub>	Normally-Open Terminal.
16, 4, 8, 12	COM <sub>X</sub>	Common Terminal.

## **FUNCTION TABLE**

IN <sub>1-4</sub>	FUNCTION					
II <b>V</b> 1-4	NC1, 2, 3 and 4	NO1, 2, 3 and 4				
0	ON	OFF				
1	OFF	ON				

## **ELECTRICAL CHARACTERISTICS**

 $(V_{+} = 4.5V \text{ to } 5.5V, \text{ GND} = 0V, V_{IH} = 1.6V, V_{IL} = 0.5V, \text{ Full} = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}.$  Typical values are at  $V_{+} = 5V, T_{A} = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$ 

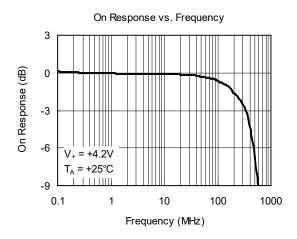
PARAMETER	SYMBOL	CONDITIONS	3	TEMP	MIN	TYP	MAX	UNITS
Analog Switch								
Analog Signal Range	$V_{NO},V_{NC},V_{COM}$				0		V+	V
On Resistance		$V_{+} = 4.5V$ , $0V \le V_{NO}$ or $V_{NC} \le V_{+}$ , $I_{COM} = -100mA$ , Test Circuit 1		+25°C		4	6	0
On-Resistance	$R_{ON}$			Full			7	Ω
On-Resistance Match between	A.D.	$V_{+} = 4.5V, \ 0V \le V_{NO} \ or \ V_{NC} \le V_{NC}$	V <sub>+</sub> ,	+25°C		0.4	2.5	Ω
Channels	$\Delta R_{ON}$	I <sub>COM</sub> = -100mA, Test Circuit 1		Full			3	77
On-Resistance Flatness	R <sub>FLAT(ON)</sub>	$V_{+} = 4.5V, \ 0V \le V_{NO} \ or \ V_{NC} \le V_{NC}$	V <sub>+</sub> ,	+25°C		2	3	Ω
On-registance riatiless	T-LAT(ON)	I <sub>COM</sub> = -100mA, Test Circuit 1		Full			3.5	32
Source Off Leakage Current	I <sub>NC(OFF)</sub> , I <sub>NO(OFF)</sub>	$V_{+} = 5.5V$ , $V_{NO}$ or $V_{NC} = 3.3V/0$ $V_{COM} = 0.3V/3.3V$	0.3V,	Full			1	μΑ
Channel On Leakage Current	$I_{NC(ON)}, I_{NO(ON)}, I_{COM(ON)}$	$V_{+} = 5.5V$ , $V_{COM} = 0.3V/3.3V$ , $V_{NO}$ or $V_{NC} = 0.3V/3.3V$ or floating the state of the state o	iting	Full			1	μΑ
Digital Inputs								
Input High Voltage	V <sub>INH</sub>			Full	1.6			V
Input Low Voltage	V <sub>INL</sub>		Full			0.5	V	
Input Leakage Current	I <sub>IN</sub>	V <sub>+</sub> = 5.5V, V <sub>IN</sub> = 0V or 5.5V		Full			1	μA
Dynamic Characteristics								
Turn-On Time	t <sub>ON</sub>	$V_{NC}$ or $V_{NO} = 3V$ , $R_{L} = 300\Omega$ , $C_{R} = 300\Omega$	C <sub>L</sub> = 35pF,	+25°C		32		ns
Turn-Off Time	t <sub>OFF</sub>	Test Circuit 2		+25°C		26		ns
Break-Before-Make Time Delay	t <sub>D</sub>	$V_{NC}$ or $V_{NO}$ = 3V, $R_L$ = 300 $\Omega$ , $C_L$ Test Circuit 4	C <sub>L</sub> = 35pF,	+25°C		15.5		ns
Charge Injection	Q	$V_G = GND, R_G = 0\Omega, C_L = 1nF$ $Q = C_L \times V_{OUT}, Test Circuit 3$	,	+25°C		4.8		рС
Off Isolation		Signal = 0dBm, $R_L = 50\Omega$ ,	1MHz	+25°C		-75		dB
On isolation	O <sub>ISO</sub>	Test Circuit 5	10MHz	+25°C		-55		ив
Channel-to-Channel Crosstalk	· ·	Signal = 0dBm, $R_L = 50\Omega$ ,	1MHz	+25°C		-100		dB
Channel-to-Channel Crosstalk	$X_{TALK}$	Test Circuit 6	10MHz	+25°C		-60		ив
-3dB Bandwidth	BW	Signal = 0dBm, $R_L$ = 50 $\Omega$ , Test Circuit 7		+25°C		300		MHz
Channel On Capacitance	$C_{NC(ON)}, C_{NO(ON)}, \ C_{COM(ON)}$			+25°C		45		pF
Power Requirements								
Power Supply Range	V <sub>+</sub>			Full	1.8		5.5	V
Power Supply Current	I <sub>+</sub>	V <sub>+</sub> = 5.5V, V <sub>IN</sub> = 0V or V <sub>+</sub>		Full			1	μΑ

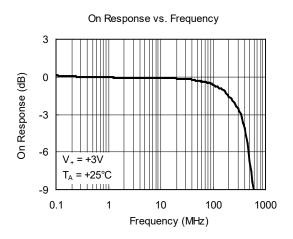
# **ELECTRICAL CHARACTERISTICS (continued)**

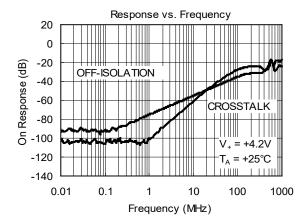
 $(V_{+} = 2.7V \text{ to } 3.6V, V_{IH} = 1.6V, V_{IL} = 0.4V, \text{ Full} = -40^{\circ}\text{C to } +85^{\circ}\text{C}. \text{ Typical values are at } V_{+} = 3V, T_{A} = +25^{\circ}\text{C}, \text{ unless otherwise noted.})$ 

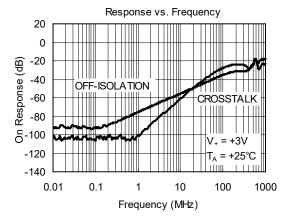
PARAMETER	SYMBOL	CONDITIONS		TEMP	MIN	TYP	MAX	UNITS
Analog Switch								
Analog Signal Range	$V_{NO}, V_{NC}, V_{COM}$		Full	0		V+	V	
On-Resistance	R <sub>ON</sub>	$V_{+} = 2.7V, \ 0V \le V_{NO} \ \text{or} \ V_{NC} \le V_{+},$		+25°C		10	15	Ω
On-Resistance	Kon	I <sub>COM</sub> = -10mA, Test Circuit 1	I <sub>COM</sub> = -10mA, Test Circuit 1				18	
On-Resistance Match between	$\Delta R_{ON}$	$V_{+} = 2.7V, \ 0V \le V_{NO} \ or \ V_{NC} \le V_{NC}$	/ <sub>+</sub> ,	+25°C		1	3	Ω
Channels	ΔKON	I <sub>COM</sub> = -100mA, Test Circuit 1		Full			4	12
On-Resistance Flatness	P	$V_{+} = 2.7V, \ 0V \le V_{NO} \ or \ V_{NC} \le V_{NC}$	<b>/</b> +,	+25°C		6	9	Ω
On-ivesistance matriess	$R_{FLAT(ON)}$	I <sub>COM</sub> = -100mA, Test Circuit 1		Full			12	12
Source Off Leakage Current	I <sub>NC(OFF)</sub> , I <sub>NO(OFF)</sub>	$V_{+} = 3.6V$ , $V_{NO}$ or $V_{NC} = 3.3V/0$ $V_{COM} = 0.3V/3.3V$	).3V,	Full			1	μΑ
Channel On Leakage Current	I <sub>NC(ON)</sub> , I <sub>NO(ON)</sub> , I <sub>COM(ON)</sub>	$V_{+} = 3.6V$ , $V_{COM} = 0.3V/3.3V$ , $V_{NO}$ or $V_{NC} = 0.3V/3.3V$ or floating the state of the state o	Full			1	μΑ	
Digital Inputs					•	•		
Input High Voltage	V <sub>INH</sub>			Full	1.5			V
Input Low Voltage	V <sub>INL</sub>						0.4	V
Input Leakage Current	I <sub>IN</sub>	V <sub>+</sub> = 2.7V, V <sub>IN</sub> = 0V or 2.7V		Full			1	μA
Dynamic Characteristics								
Turn-On Time	t <sub>on</sub>	$V_{NC}$ or $V_{NO} = 1.5V$ , $R_{L} = 300\Omega$	, C <sub>L</sub> = 35pF,	+25°C		34		Ns
Turn-Off Time	t <sub>OFF</sub>	Test Circuit 2		+25°C		40		Ns
Break-Before-Make Time Delay	t <sub>D</sub>	$V_{NC}$ or $V_{NO}$ = 1.5V, $R_L$ = 300 $\Omega$ Test Circuit 4	, C <sub>L</sub> = 35pF,	+25°C		20		Ns
Charge Injection	Q	$V_G$ = GND, $R_G$ = 0 $\Omega$ , $C_L$ = 1.0 $Q$ = $C_L$ × $V_{OUT}$ , Test Circuit 3	nF,	+25°C		3.4		рС
Off In a lation		Signal = 0dBm, $R_L = 50\Omega$ ,	1MHz	+25°C		-75		-ID
Off Isolation	O <sub>ISO</sub>	Test Circuit 5	10MHz	+25°C		-55		dB
Channel-to-Channel	V	Signal = 0dBm, $R_L = 50\Omega$ ,	1MHz	+25°C		-100		٩D
Crosstalk	X <sub>TALK</sub>	Test Circuit 6	10MHz	+25°C		-60		dB
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega$ , Tes	st Circuit 7	+25°C	_	300		MHz
Channel On Capacitance	$C_{NC(ON)}, C_{NO(ON)}, \ C_{COM(ON)}$			+25°C		45		pF

## TYPICAL PERFORMANCE CHARACTERISTICS

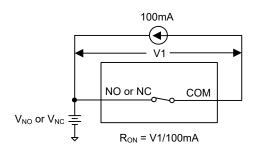




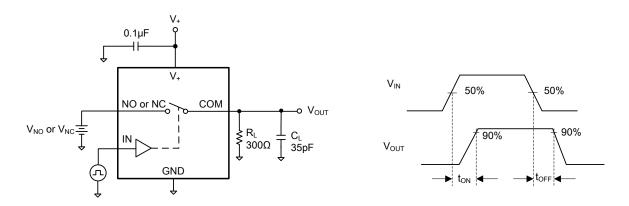




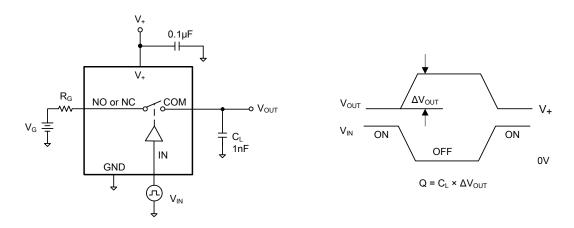
## **TEST CIRCUITS**



Test Circuit 1. On Resistance

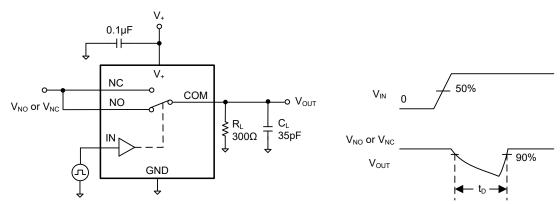


Test Circuit 2. Switching Times ( $t_{\text{ON}}$ ,  $t_{\text{OFF}}$ )

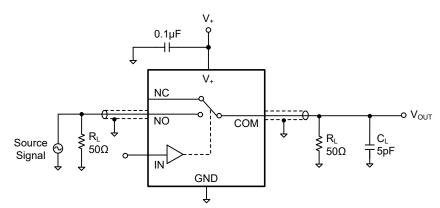


**Test Circuit 3. Charge Injection** 

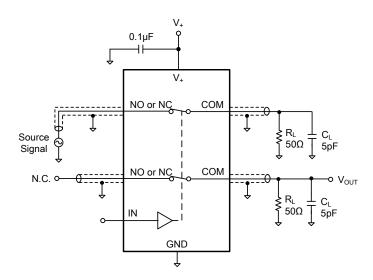
# **TEST CIRCUITS (continued)**



Test Circuit 4. Break-Before-Make Time Delay (t<sub>D</sub>)



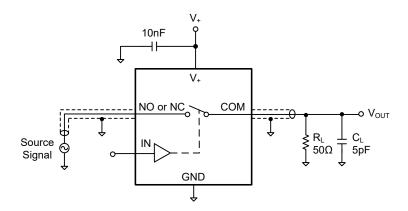
**Test Circuit 5. Off Isolation** 



Channel To Channel Crosstalk = -20 × log  $\frac{V_{NO} \text{ or } V_{NC}}{V_{OUT}}$ 

Test Circuit 6. Channel-to-Channel Crosstalk

# **TEST CIRCUITS (continued)**



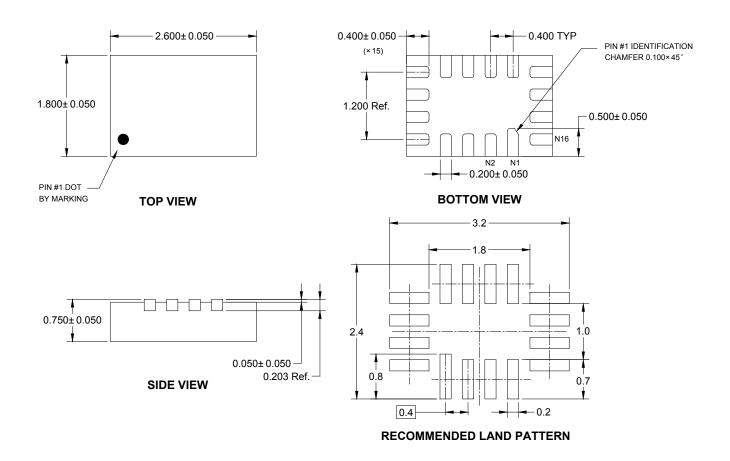
Test Circuit 7. -3dB Bandwidth

## **REVISION HISTORY**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

MAY 2016 – REV.A.3 to REV.A.4	Page
Updated Tape and Reel Information section	11
JANUARY 2013 – REV.A.2 to REV.A.3	Page
Added Recommended Land Pattern Information	10
Added Tape and Reel Information	11, 12
MAY 2011 - REV.A.1 to REV.A.2	Page
Updated Package Description	All
DECEMBER 2008 – REV.A to REV.A.1	Page
Changed Absolute Maximum Ratings section	2
Changes from Original (MAY 2008) to REV.A	Page
Changed from product preview to production data	All

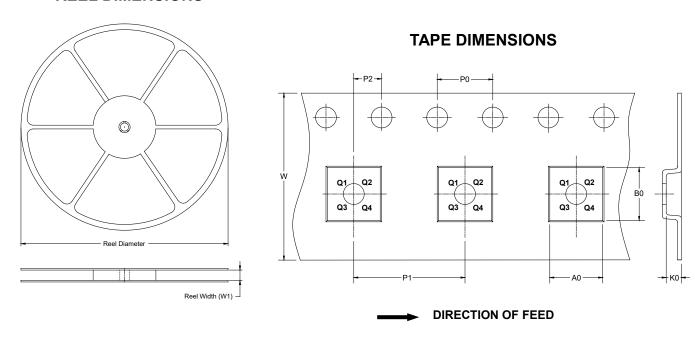
# PACKAGE OUTLINE DIMENSIONS TQFN-2.6×1.8-16L



NOTE: All linear dimensions are in millimeters.

## TAPE AND REEL INFORMATION

#### **REEL DIMENSIONS**

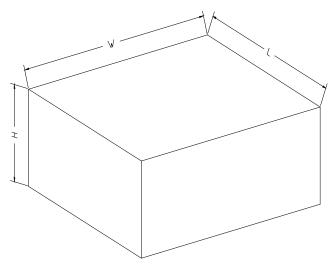


NOTE: The picture is only for reference. Please make the object as the standard.

#### **KEY PARAMETER LIST OF TAPE AND REEL**

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TQFN-2.6×1.8-16L	7"	9.0	2.01	2.81	0.93	4.0	4.0	2.0	8.0	Q1

## **CARTON BOX DIMENSIONS**



NOTE: The picture is only for reference. Please make the object as the standard.

## **KEY PARAMETER LIST OF CARTON BOX**

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton	•
7" (Option)	368	227	224	8	
7"	442	410	224	18	DD0002