

Monolithic Voltage-Controlled Oscillators

ABSOLUTE MAXIMUM RATINGS

V_{CC} to GND-0.3V to +6V
 TUNE, $\overline{\text{SHDN}}$ to GND-0.3V to (V_{CC} + 0.3V)
 OUT to GND-0.3V to (V_{CC} + 0.6V)
 Continuous Power Dissipation (T_A = +70°C)
 8-Pin μMAX (derate 5.7mW/°C above T_A = +70°C).....457mW

Operating Temperature Range-40°C to +85°C
 Junction Temperature+150°C
 Storage Temperature Range-65°C to +150°C
 Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(Typical Operating Circuit, V_{CC} = +2.7V to +5.5V, V_{TUNE} = 1.4V, V $\overline{\text{SHDN}}$ = 2V, OUT = unconnected, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at V_{CC} = +3V, T_A = +25°C.) (Note 1)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage		2.7		5.5	V
Supply Current			8	11.5	mA
Shutdown Supply Current	V $\overline{\text{SHDN}}$ ≤ 0.6V		0.1	5	μA
$\overline{\text{SHDN}}$ Input Voltage Low				0.6	V
$\overline{\text{SHDN}}$ Input Voltage High		2.0			V
$\overline{\text{SHDN}}$ Input Current Low	V $\overline{\text{SHDN}}$ ≤ 0.6V	-0.5		0.5	μA
$\overline{\text{SHDN}}$ Input Current High	V $\overline{\text{SHDN}}$ ≥ 2.0V	-0.5		0.5	μA
TUNE Input Current	0.4V ≤ V _{TUNE} ≤ 2.4V		0.01		nA

AC ELECTRICAL CHARACTERISTICS

(Typical Operating Circuit, V_{CC} = +2.7V to +5.5V, V_{TUNE} = 0.4V to 2.4V, V $\overline{\text{SHDN}}$ = 2V, T_A = +25°C, unless otherwise noted. Typical values measured at V_{CC} = +3V.) (Note 1)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Oscillator Frequency Range	MAX2622, V _{TUNE} = 0.4V to 2.4V, T _A = -40°C to +85°C	855		881	MHz
	MAX2623, V _{TUNE} = 0.4V to 2.4V, T _A = -20°C to +75°C	885		950	
	MAX2624, V _{TUNE} = 0.4V to 2.4V, T _A = -40°C to +85°C	947		998	
Phase Noise	f _{OFFSET} = 100kHz		-101		dBc/Hz
	f _{OFFSET} = 1MHz		-119		
Noise Floor			-151		dBm/Hz
Maximum Tuning Gain (Note 2)	V _{TUNE} = 0.4V to 2.4V		75	100	MHz/V
Output Power	V _{TUNE} = 0.4V (Note 3)		-3		dBm
Return Loss (Note 3)			-10		dB
Harmonics			-27		dBc
Load Pulling	VSWR = 2:1, all phases		0.75		MHz _{p-p}
Supply Pushing	V _{CC} stepped from 2.8V to 3.3V		280		kHz/V

Note 1: Specifications are production tested at T_A = +25°C. Limits over temperature are guaranteed by design and characterization.

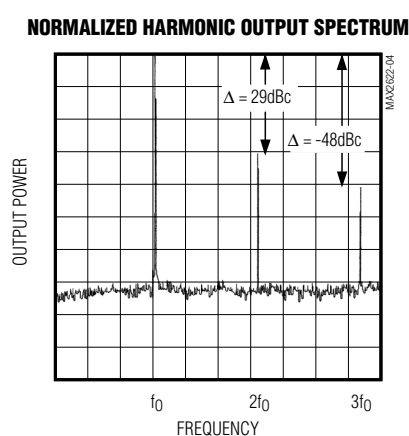
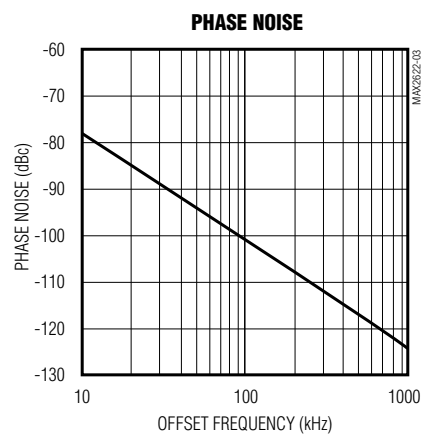
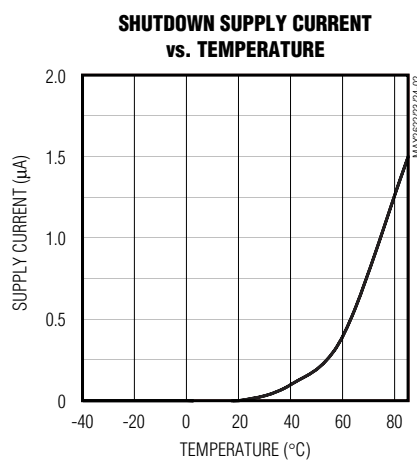
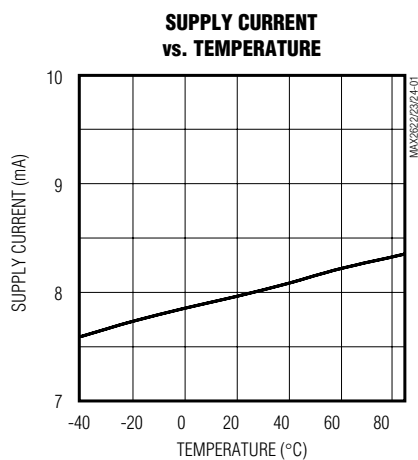
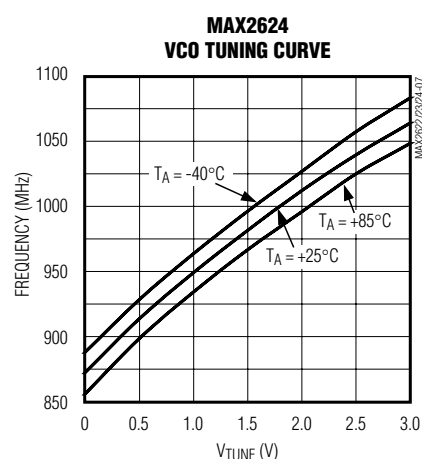
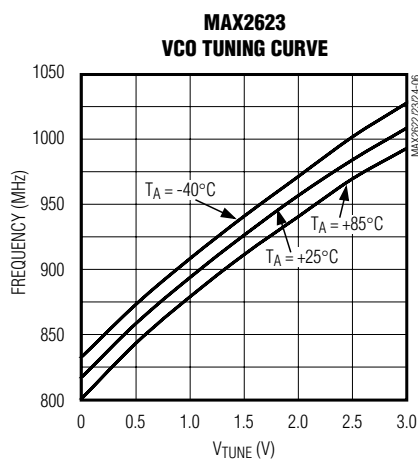
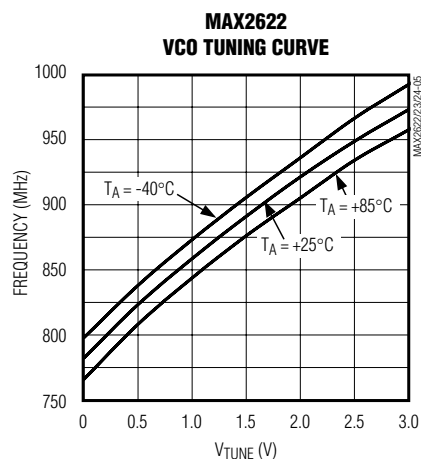
Note 2: Tuning gain is measured at V_{TUNE} = 0.4V with a 0.2V step to 0.6V. At low V_{TUNE}, tuning gain is highest.

Note 3: Measurements taken on MAX2622_EV kit.

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Typical Operating Characteristics

($V_{CC} = +3.0V$, $V_{TUNE} = 0.4V$ to $2.4V$, $V_{SHDN} = 2V$, $T_A = +25^\circ C$, unless otherwise noted.)



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Pin Description

PIN	NAME	FUNCTION
1	N.C.	No Connection. Not internally connected.
2	TUNE	Oscillator Frequency Tuning Voltage Input. High-impedance input with a voltage input range of 0.4V (low frequency) to 2.4V (high frequency) adjustment.
3	GND	Ground Connection for Oscillator and Biasing. Requires a low-inductance connection to the circuit board ground plane.
4	$\overline{\text{SHDN}}$	Shutdown Logic Input. A high-impedance input logic level low disables the device and reduces supply current to 0.1 μ A. A logic level high enables the device.
5	VCC	Output Buffer DC Supply Voltage Connection. Bypass with a 220pF capacitor to GND for best high-frequency performance.
6	VCC	Bias and Oscillator DC Supply Voltage Connection. Bypass with a 220pF capacitor to GND for low noise and low spurious content performance from the oscillator.
7	OUT	Buffered Oscillator Output
8	GND	Ground Connection for Output Buffer. Requires a low-inductance connection to the circuit board ground plane.

Detailed Description

Oscillator

The MAX2622/MAX2623/MAX2624 VCOs are implemented as an LC oscillator topology, integrating all of the tank components on-chip. This fully monolithic approach provides an extremely easy-to-use VCO, equivalent to a VCO module. The frequency is controlled by a voltage applied to the TUNE pin, which is internally connected to the varactor. The VCO core uses a differential topology to provide a stable frequency versus supply voltage and improve the immunity to load variations. In addition, there is a buffer amplifier following the oscillator core to provide added isolation from load variations and to boost the output power.

Output Buffer

The oscillator signal from the core drives an output buffer amplifier. The amplifier is constructed as a common-emitter stage with an integrated on-chip reactive output match. No external DC blocking capacitor is required, eliminating the need for any external components. The output amplifier has its own VCC and GND pins to minimize load-pulling effects. The amplifier boosts the oscillator signal to a level suitable for driving most RF mixers.

Applications Information

Tune Input

The tuning input is typically connected to the output of the PLL loop filter. The loop filter is presumed to provide an appropriately low-impedance source. It may incorporate an extra RC filter stage to reduce high-frequency noise and spurious signals. Any excess noise on the tuning input is directly translated into FM noise, which can degrade the phase-noise performance of the oscillator. Therefore, it is important to minimize the noise introduced on the tuning input. A simple RC filter with low corner frequency is needed during testing in order to filter the noise present on the voltage source driving the tuning line.

Layout Issues

Always use controlled impedance lines (microstrip, coplanar waveguide, etc.) for high-frequency signals. Always place decoupling capacitors as close to the VCC pins as possible; for long VCC lines, it may be necessary to add additional decoupling capacitors located further from the device. Always provide a low-inductance path to ground, and keep GND vias as close to the device as possible. Thermal reliefs on GND pads are **not** recommended.

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MAX2622/MAX2623/MAX2624

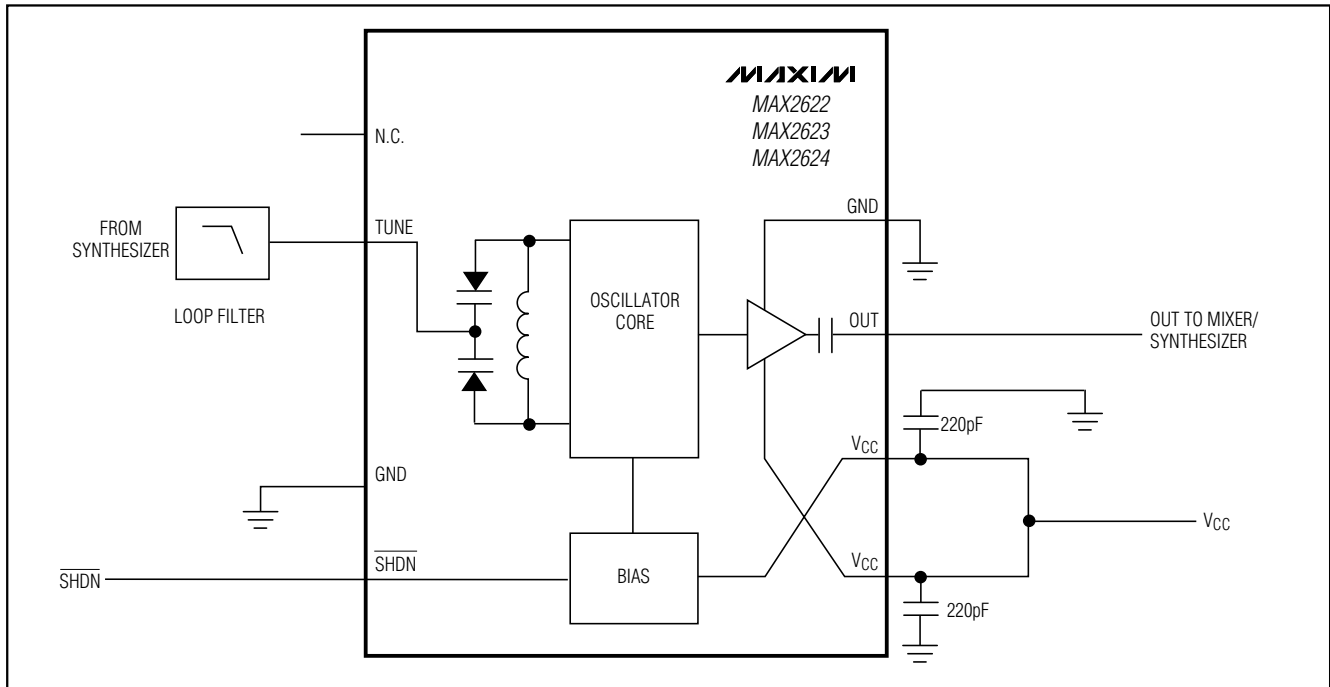
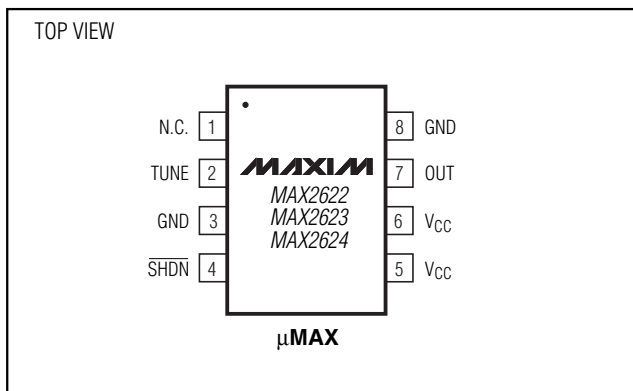


Figure 1. Typical Application Circuit

Pin Configuration



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Package Information

TOP VIEW

BOTTOM VIEW

FRONT VIEW

SIDE VIEW

	INCHES		MILLIMETERS		JEDEC			
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	0.037	0.043	0.94	1.10	---	0.043	---	1.10
A1	0.002	0.006	0.05	0.15	0.002	0.006	0.05	0.15
B	0.010	0.014	0.25	0.36	0.010	0.016	0.25	0.40
C	0.005	0.007	0.13	0.18	0.005	0.009	0.13	0.23
D	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
e	0.0256	BSC	0.65	BSC	0.0256	BSC	0.64	BSC
E	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
H	0.188	0.198	4.78	5.03	0.193	BSC	4.9	BSC
L	0.016	0.026	0.41	0.66	0.016	0.027	0.40	0.70
α	0°	6°	0°	6°	0°	6°	0°	6°
*X	0.087	0.099	2.210	2.515				
*Y	0.062	0.074	1.575	1.880				

* EXPOSED PAD (Note 5)

8LUMAXD EPS

NOTES:

1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15 MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187.
5. DIMENSIONS X & Y APPLY TO EXPOSED PAD (EP) VERSIONS ONLY. SEE INDIVIDUAL PRODUCT DATASHEET TO DETERMINE IF A PRODUCT USES EXPOSED PAD PACKAGE.
6. EXPOSED PAD FLUSH WITH BOTTOM OF PACKAGE WITHIN .002".

PROPRIETARY INFORMATION

TITLE:
PACKAGE OUTLINE, 8L uMAX WITH EP OPTION

APPROVAL	DOCUMENT CONTROL NO. 21-0036	REV H	1/1
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