

# TTL Compatible CMOS Analog Switches

## ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to V-  
V+ (DG300–DG303) .....36V  
V+ (DG300A–DG303A) .....44V  
GND .....25V  
Digital Inputs, V<sub>S</sub>, V<sub>D</sub>, (Note 1).....-4V to (V+ + 4V) or  
30mA, whichever occurs first  
Current, Any Terminal Except S or D .....30mA  
Continuous Current, S or D .....30mA  
(pulsed at 1ms, 10% duty cycle max) .....100mA  
Storage Temperature (A & B suffix).....-65°C to +150°C  
(C suffix) .....-65°C to +125°C

Operating Temperature (A suffix) .....-55°C to +125°C  
(B suffix) .....-25°C to +85°C  
(C suffix) .....0°C to +70°C  
Lead Temperature (soldering 10s) .....+300°C  
Power Dissipation\*  
Cerdip (K) (derate 11mW/°C above +75°C) .....825mW  
Plastic DIP (J) (derate 6.5mW/°C above +25°C) .....470mW  
Metal Can (A) (derate 6mW/°C above +75°C) .....450mW

\*Device mounted with all leads soldered or welded to PC board.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

(V+ = +15V, V- = -15V, GND = 0V, T<sub>A</sub> = +25°C, unless otherwise noted.)

|         | PARAMETER                      | SYMBOL                                  | TEST CONDITIONS                                                                               | DG300–DG303A<br>DG300A–DG303AA |          |     | DG300–DG303B/C<br>DG300A–DG303AB/C |          |     | UNITS |
|---------|--------------------------------|-----------------------------------------|-----------------------------------------------------------------------------------------------|--------------------------------|----------|-----|------------------------------------|----------|-----|-------|
|         |                                |                                         |                                                                                               | MIN                            | TYP      | MAX | MIN                                | TYP      | MAX |       |
|         |                                |                                         |                                                                                               | (Note 2)                       | (Note 3) |     | (Note 2)                           | (Note 3) |     |       |
| SWITCH  | Analog Signal Range            | V <sub>ANALOG</sub>                     | I <sub>S</sub> = 10mA, V <sub>IN</sub> = 0.8V or 4.0V                                         | -15                            |          | +15 | -15                                |          | +15 | V     |
|         | Drain-Source ON-Resistance     | R <sub>DS(ON)</sub>                     | I <sub>S</sub> = -10mA, V <sub>D</sub> = 10V<br>I <sub>S</sub> = -10mA, V <sub>D</sub> = -10V |                                | 30       | 50  |                                    | 30       | 50  | Ω     |
|         | Source OFF-Leakage Current     | I <sub>S(OFF)</sub>                     | V <sub>IN</sub> = 0.8V or<br>V <sub>IN</sub> = 0.8V                                           |                                | 0.1      | 1   |                                    | 0.1      | 5   | nA    |
|         | Drain OFF-Leakage Current      | I <sub>D(OFF)</sub>                     | V <sub>S</sub> = -14V, V <sub>D</sub> = 14V<br>V <sub>S</sub> = 14V, V <sub>D</sub> = -14V    | -1                             | -0.1     |     | -5                                 | -0.1     |     | nA    |
|         | Drain ON-Leakage Current       | I <sub>D(ON)</sub>                      | V <sub>D</sub> = V <sub>S</sub> = 14V<br>V <sub>D</sub> = V <sub>S</sub> = -14V               |                                | 0.1      | 2   |                                    | 0.1      | 5   | nA    |
|         |                                |                                         |                                                                                               | -2                             | -0.1     |     | -5                                 | -0.1     |     |       |
|         |                                |                                         |                                                                                               |                                |          |     |                                    |          |     |       |
| INPUT   | Input Current/<br>Voltage High | I <sub>INH</sub>                        | V <sub>IN</sub> = 5.0V<br>V <sub>IN</sub> = 15V                                               | -1                             | -0.001   |     | -1                                 | -0.001   |     | μA    |
|         | Input Current/<br>Voltage Low  | I <sub>INL</sub>                        | V <sub>IN</sub> = 0V                                                                          | -1                             | -0.001   |     | -1                                 | -0.001   |     | μA    |
| DYNAMIC | Turn-ON Time                   | t <sub>ON</sub>                         | See Switching Time Test Circuit                                                               |                                | 150      | 300 |                                    | 150      | 300 | ns    |
|         | Turn-OFF Time                  | t <sub>OFF</sub>                        |                                                                                               |                                | 130      | 250 |                                    | 130      | 250 | ns    |
|         | Break-Before-Make Interval     | t <sub>ON</sub> - t <sub>OFF</sub>      | See Break-Before-Make Time Test Circuit, DG301(A)/DG303(A) only                               |                                | 50       |     |                                    | 50       |     | ns    |
|         | Charge Injection               | Q                                       | C <sub>L</sub> = 10nF, R <sub>GEN</sub> = 0Ω, V <sub>GEN</sub> = 0V                           |                                | 12       |     |                                    | 12       |     | pC    |
|         | Source OFF-Capacitance         | C <sub>S(OFF)</sub>                     | f = 1MHz, V <sub>IN</sub> = 0.8V or<br>V <sub>IN</sub> = 4.0V                                 |                                | 14       |     |                                    | 14       |     | pF    |
|         | Drain OFF-Capacitance          | C <sub>D(OFF)</sub>                     |                                                                                               |                                | 14       |     |                                    | 14       |     | pF    |
|         | Channel ON-Capacitance         | C <sub>D(ON)</sub> + C <sub>S(ON)</sub> |                                                                                               |                                | 40       |     |                                    | 40       |     | pF    |
|         | Input Capacitance              | C <sub>IN</sub>                         | f = 1MHz                                                                                      |                                | 6        |     |                                    | 6        |     | pF    |
|         |                                |                                         |                                                                                               |                                | 7        |     |                                    | 7        |     |       |
|         | Off-Isolation (Note 4)         |                                         | V <sub>IN</sub> = 0V, R <sub>L</sub> = 1kΩ                                                    |                                | 62       |     |                                    | 62       |     | dB    |
|         | Crosstalk (Channel-to-Channel) |                                         | V <sub>S</sub> = 1V <sub>RMS</sub> , f = 500kHz                                               |                                | 74       |     |                                    | 74       |     | dB    |

# TTL Compatible CMOS Analog Switches

## ELECTRICAL CHARACTERISTICS (continued)

(V+ = +15V, V- = -15V, GND = 0V, TA = +25°C, unless otherwise noted.)

|        | PARAMETER               | SYMBOL | TEST CONDITIONS                          | DG300–DG303A<br>DG300A–DG303AA |        |     | DG300–DG303B/C<br>DG300A–DG303AB/C |        |     | UNITS |
|--------|-------------------------|--------|------------------------------------------|--------------------------------|--------|-----|------------------------------------|--------|-----|-------|
|        |                         |        |                                          | MIN                            | TYP    | MAX | MIN                                | TYP    | MAX |       |
| SUPPLY | Positive Supply Current | I+     | VIN = 4V (one input)<br>(all others = 0) |                                | 0.23   | 0.5 |                                    | 0.23   | 0.5 | mA    |
|        | Negative Supply Current | I-     |                                          | -10                            | -0.001 |     | -10                                | -0.001 |     | μA    |
|        | Positive Supply Current | I+     | VIN = 0.8V (all inputs)                  |                                | 0.001  | 10  |                                    | 0.001  | 10  | μA    |
|        | Negative Supply Current | I-     |                                          | -10                            | -0.001 |     | -10                                | -0.001 |     | μA    |

## ELECTRICAL CHARACTERISTICS (Over Temperature)

(V+ = +15V, V- = -15V, GND = 0V, TA = Over Temperature Range, unless otherwise noted.)

|         | PARAMETER                   | SYMBOL  | TEST CONDITIONS                          | DG300–DG303A<br>DG300A–DG303AA |     |     | DG300–DG303B/C<br>DG300A–DG303AB/C |     |     | UNITS |
|---------|-----------------------------|---------|------------------------------------------|--------------------------------|-----|-----|------------------------------------|-----|-----|-------|
|         |                             |         |                                          | MIN                            | TYP | MAX | MIN                                | TYP | MAX |       |
| SWITCH  | Analog Signal Range         | VANALOG | IS = -10mA, VIN = 0.8V or 4.0V           | -15                            |     | +15 | -15                                |     | +15 | V     |
|         | Drain-Source ON-Resistance  | RDS(ON) | IS = -10mA, VD = 10V                     |                                |     | 75  |                                    |     | 75  | Ω     |
|         |                             |         | IS = -10mA, VD = -10V                    |                                |     | 75  |                                    |     | 75  |       |
|         | Source OFF-Leakage Current  | IS(OFF) | VIN = 0.8V or<br>VIN = 0.8V              |                                |     | 100 |                                    |     | 100 | nA    |
|         | Drain OFF-Leakage Current   | ID(OFF) | VS = -14V, VD = 14V                      | -100                           |     |     | -100                               |     |     | nA    |
|         |                             |         | VS = -14V, VD = 14V                      |                                |     | 100 |                                    |     | 100 | nA    |
|         | Drain ON-Leakage Current    | ID(ON)  | VS = 14V, VD = -14V                      | -100                           |     |     | -100                               |     |     | nA    |
|         |                             |         | VD = VS = 14V                            |                                |     | 200 |                                    |     | 200 | nA    |
| INPUT   | Input Current/ Voltage High | IINH    | VIN = 5.0V                               | -1                             |     |     | -10                                |     |     | μA    |
|         |                             |         | VIN = 15V                                |                                |     | 1   |                                    |     | 10  |       |
| SUPPLY  | Input Current/ Voltage Low  | IINL    | VIN = 0V                                 | -1                             |     |     | -10                                |     |     | μA    |
|         | Positive Supply Current     | I+      | VIN = 4V (one input)<br>(all others = 0) |                                |     | 1   |                                    |     | 1   | mA    |
| DYNAMIC | Negative Supply Current     | I-      |                                          | -100                           |     |     | -200                               |     |     | mA    |
|         | Positive Supply Current     | I+      | VIN = 0.8V (all inputs)                  |                                |     | 100 |                                    |     | 200 | μA    |
|         | Negative Supply Current     | I-      |                                          | -100                           |     |     | -200                               |     |     | μA    |
|         | Turn-ON Time                | tON     | See Switching Time Test Circuit          |                                |     | 500 |                                    |     |     | ns    |
|         | Turn-OFF Time               | tOFF    |                                          |                                |     | 450 |                                    |     |     | ns    |

DG300(A)/DG301(A)/DG302(A)/DG303(A)

# TTL Compatible CMOS Analog Switches

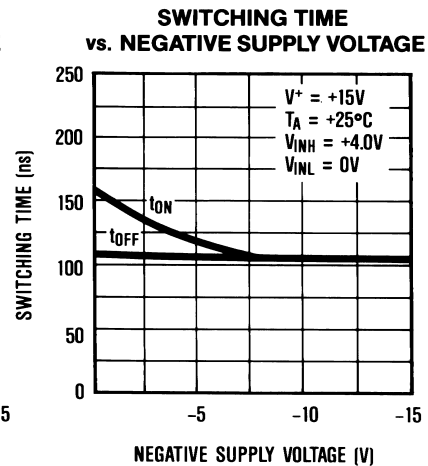
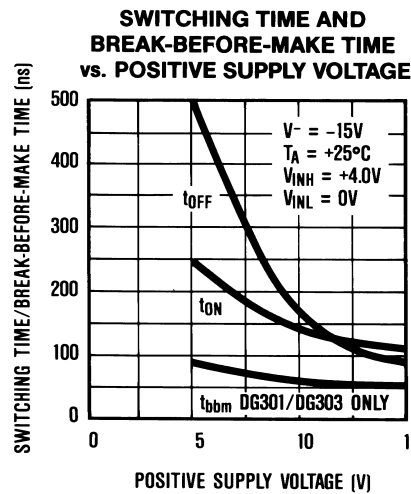
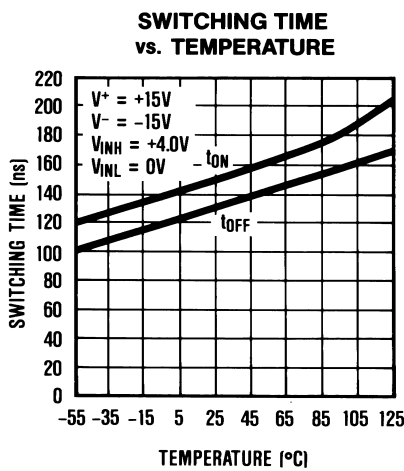
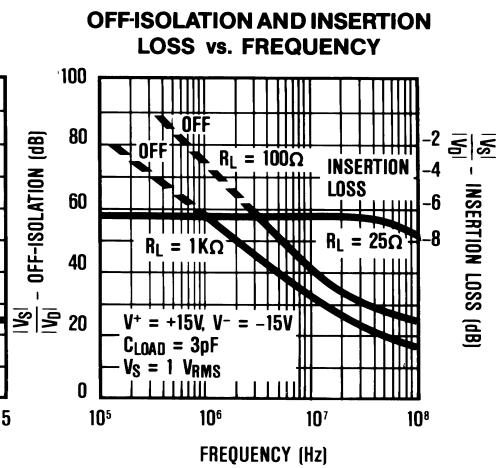
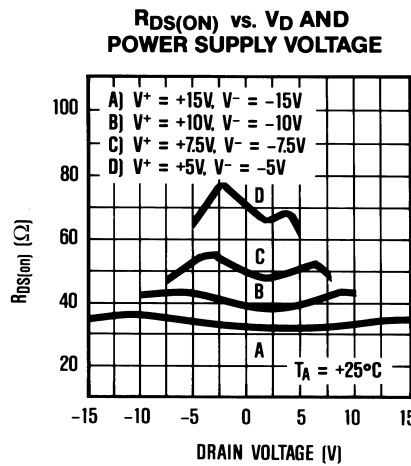
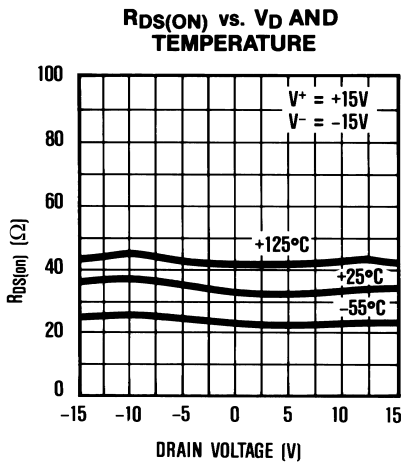
## ELECTRICAL CHARACTERISTICS (Over Temperature) (continued)

( $V^+ = +15V$ ,  $V^- = -15V$ , GND = 0V,  $T_A$  = Over Temperature Range, unless otherwise noted.)

- Note 1:** Signals on  $S_X$ ,  $D_X$ , or  $IN_X$  exceeding  $V^+$  or  $V^-$  are clamped by internal diodes. Limit diode forward current to maximum current ratings.
- Note 2:** The algebraic convention whereby the most negative value is a minimum, and the most positive value is a maximum is used in this data sheet.
- Note 3:** Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
- Note 4:** OFF-isolation =  $20 \log \frac{V_S}{V_D}$ ,  $V_S$  = input to OFF switch,  $V_D$  = output.

## Typical Operating Characteristics

( $T_A = +25^\circ C$ , unless otherwise noted.)



# TTL Compatible CMOS Analog Switches

## Test Circuits

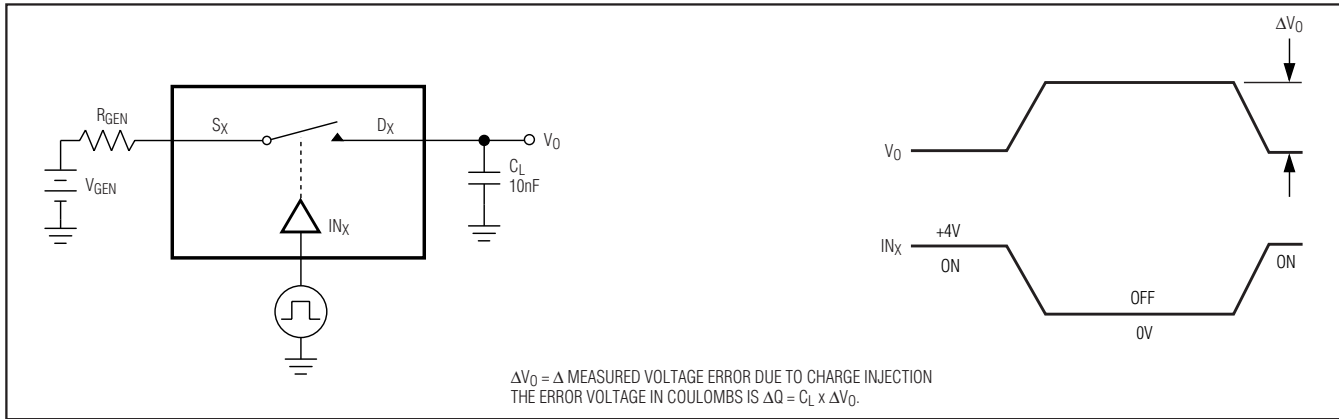


Figure 1. Charge Injection Test Circuit

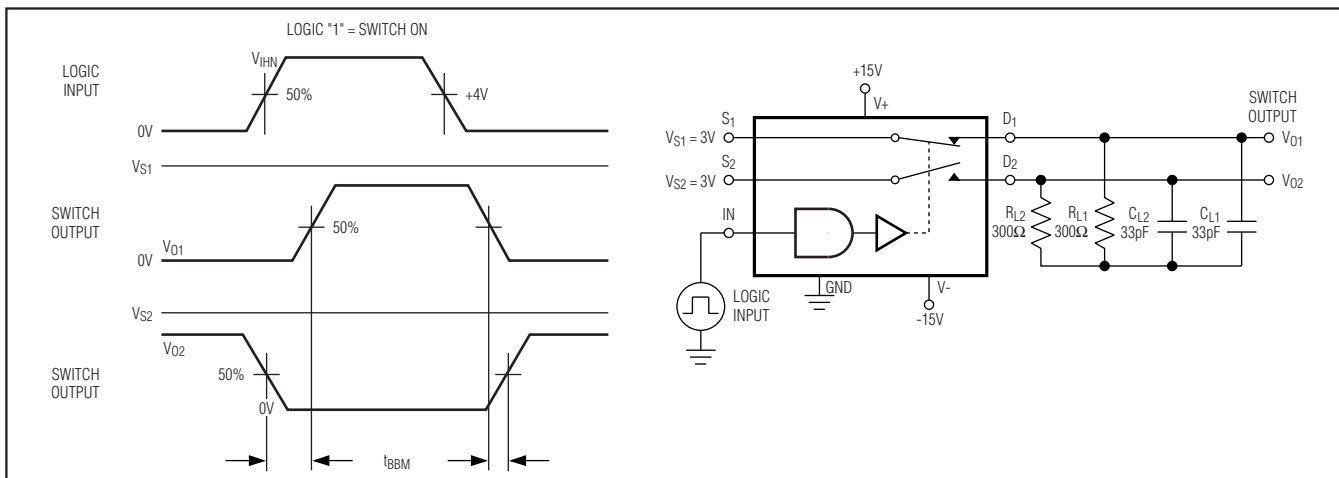


Figure 2. Break-Before-Make Time Test Circuit SPDT (DG301(A), DG303(A))

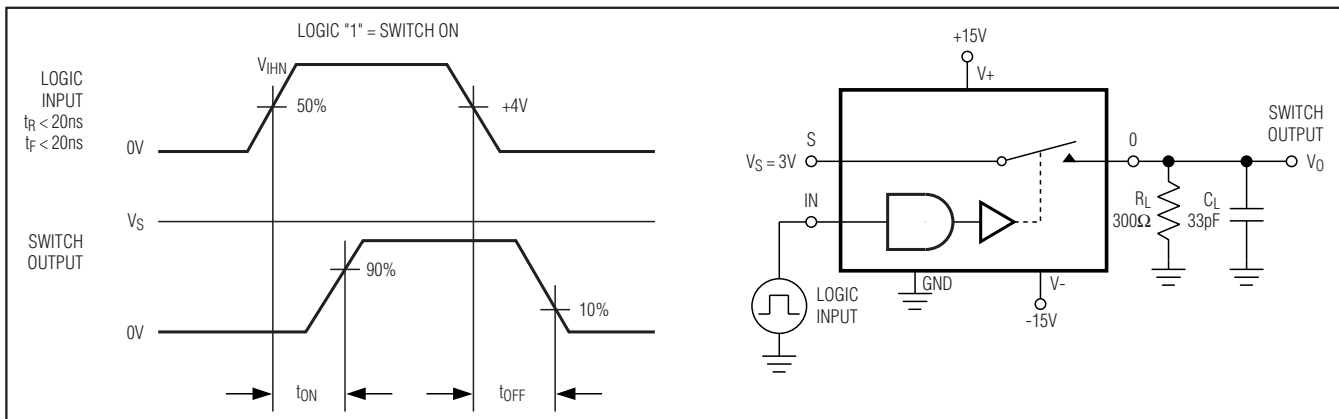


Figure 3. Switching Time Test Circuit

# TTL Compatible CMOS Analog Switches

Table 1. Typical Single Supply Parameters

| PARAMETER                           |                       | V+ SUPPLY VOLTAGE (V- = 0V) |             |             |             |
|-------------------------------------|-----------------------|-----------------------------|-------------|-------------|-------------|
|                                     |                       | +10V                        | +15V        | +20V        | +30V        |
| Switching Time ( $R_L = 1k\Omega$ ) | $t_{ON}$              | 190ns                       | 150ns       | 110ns       | 70ns        |
|                                     | $t_{OFF}$             | 40ns                        | 40ns        | 40ns        | 40ns        |
| On-Resistance                       | $V_{SIGNAL} = +1V$    | 71 $\Omega$                 | 51 $\Omega$ | 42 $\Omega$ | 31 $\Omega$ |
|                                     | $V_{SIGNAL} = V+ / 2$ | 77 $\Omega$                 | 54 $\Omega$ | 43 $\Omega$ | 30 $\Omega$ |
|                                     | $V_{SIGNAL} = V+$     | 84 $\Omega$                 | 63 $\Omega$ | 54 $\Omega$ | 43 $\Omega$ |
| Input Logic Levels                  |                       | 0.8V, 4.0V                  | 0.8V, 4.0V  | 0.8V, 4.0V  | 0.8V, 4.5V  |

## Applications Information

All DG300 family switches will operate with  $\pm 5V$  to  $\pm 15V$  power supplies. They can also be used with single-ended power supplies ranging from +10V to +30V where the V-terminal is connected to ground. In either case, analog signals ranging from V+ to V- can be switched.

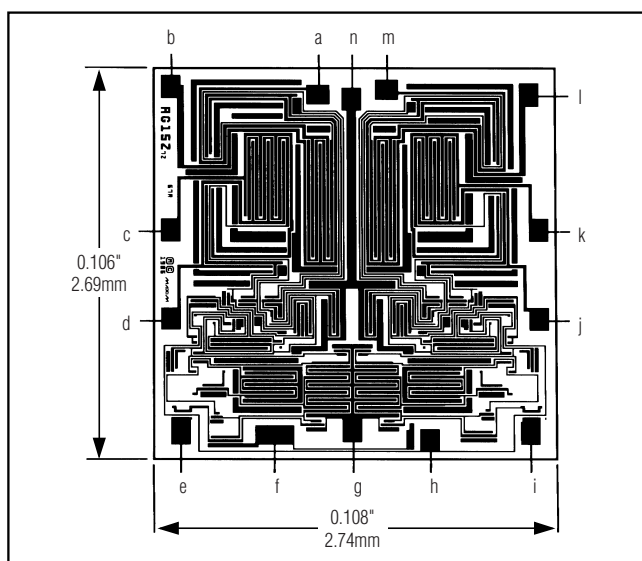
The on-resistance variation with analog signal and supply voltage is shown in the *Typical Operating Characteristics*. The temperature coefficient of  $R_{ON}$  is typically 0.5%/°C. Typical on-resistance matching from channel to channel is 10%. In addition, Table 1 outlines some typical parameters for single-supply operation.

Table 2. Charge Injection ( $\pm 15V$  Supplies)

| ANALOG INPUT (V) | INJECTED Q (pC) |
|------------------|-----------------|
| +10              | 4               |
| +5               | 8               |
| 0                | 12              |
| -5               | 8               |
| -10              | 5               |

The charge injection test circuit is shown in Figure 1. Table 2 lists the typical injected charge for DG300 series switches with various input voltages.

## Chip Topography



| DIE PAD | DG300<br>DG300A | DG301<br>DG301A | DG302/DG303<br>DG302A/DG303A |
|---------|-----------------|-----------------|------------------------------|
| a       | N.C.            | N.C.            | S3                           |
| b       | D1              | D1              | D3                           |
| c       | N.C.            | S1              | D1                           |
| d       | S1              | N.C.            | S1                           |
| e       | IN1             | IN1             | IN1                          |
| f       | N.C.            | N.C.            | N.C.                         |
| g       | GND             | GND             | GND                          |
| h       | V-              | V-              | V-                           |
| i       | IN2             | N.C.            | IN2                          |
| j       | S2              | N.C.            | S2                           |
| k       | N.C.            | N.C.            | D2                           |
| l       | D2              | S2              | D4                           |
| m       | N.C.            | D2              | S4                           |
| n       | V+              | V+              | V+                           |

# TTL Compatible CMOS Analog Switches

## Ordering Information (continued)

| PART            | TEMP RANGE      | PIN-PACKAGE         |
|-----------------|-----------------|---------------------|
| DG300AC/D       | 0°C to +70°C    | Dice                |
| DG300ACJ        | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG300ACWE       | 0°C to +70°C    | 16 Lead Wide SO     |
| DG300ACK        | 0°C to +70°C    | 14 Lead Cerdip      |
| DG300ABWE       | -25°C to +85°C  | 16 Lead Wide SO     |
| DG300ABK        | -25°C to +85°C  | 14 Lead Cerdip      |
| DG300ABA        | -25°C to +85°C  | 10 Lead Metal Can   |
| <b>DG301C/D</b> | 0°C to +70°C    | Dice                |
| DG301CJ         | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG301CWE        | 0°C to +70°C    | 16 Lead Wide SO     |
| DG301CK         | 0°C to +70°C    | 14 Lead Cerdip      |
| DG301BWE        | -25°C to +85°C  | 16 Lead Wide SO     |
| DG301BK         | -25°C to +85°C  | 14 Lead Cerdip      |
| DG301BA         | -25°C to +85°C  | 10 Lead Metal Can   |
| DG301AK         | -55°C to +125°C | 14 Lead Cerdip      |
| DG301AA         | -55°C to +125°C | 10 Lead Metal Can   |
| DG301AC/D       | 0°C to +70°C    | Dice                |
| DG301ACJ        | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG301ACWE       | 0°C to +70°C    | 16 Lead Wide SO     |
| DG301ACK        | 0°C to +70°C    | 14 Lead Cerdip      |
| DG301ABWE       | -25°C to +85°C  | 16 Lead Wide SO     |
| DG301ABK        | -25°C to +85°C  | 14 Lead Cerdip      |
| DG301ABA        | -25°C to +85°C  | 10 Lead Metal Can   |
| <b>DG302C/D</b> | 0°C to +70°C    | Dice                |
| DG302CJ         | 0°C to +70°C    | 14 Lead Plastic DIP |

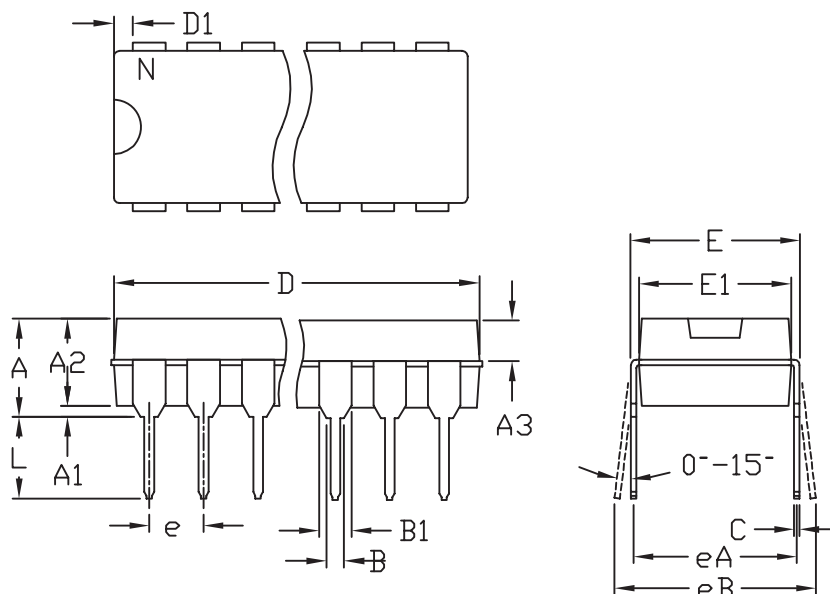
| PART            | TEMP RANGE      | PIN-PACKAGE         |
|-----------------|-----------------|---------------------|
| DG302CWE        | 0°C to +70°C    | 16 Lead Wide SO     |
| DG302CK         | 0°C to +70°C    | 14 Lead Cerdip      |
| DG302BWE        | -25°C to +85°C  | 16 Lead Wide SO     |
| DG302BK         | -25°C to +85°C  | 14 Lead Cerdip      |
| DG302AK         | -55°C to +125°C | 14 Lead Cerdip      |
| DG302AC/D       | 0°C to +70°C    | Dice                |
| DG302ACJ        | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG302ACWE       | 0°C to +70°C    | 16 Lead Wide SO     |
| DG302ACK        | 0°C to +70°C    | 14 Lead Cerdip      |
| DG302ABWE       | -25°C to +85°C  | 16 Lead Wide SO     |
| DG302ABK        | -25°C to +85°C  | 14 Lead Cerdip      |
| <b>DG303C/D</b> | 0°C to +70°C    | Dice                |
| DG303CJ         | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG303CWE        | 0°C to +70°C    | 16 Lead Wide SO     |
| DG303CK         | 0°C to +70°C    | 14 Lead Cerdip      |
| DG303BWE        | -25°C to +85°C  | 16 Lead Wide SO     |
| DG303BK         | -25°C to +85°C  | 14 Lead Cerdip      |
| DG303AK         | -55°C to +125°C | 14 Lead Cerdip      |
| DG303AC/D       | 0°C to +70°C    | Dice                |
| DG303ACJ        | 0°C to +70°C    | 14 Lead Plastic DIP |
| DG303ACWE       | 0°C to +70°C    | 16 Lead Wide SO     |
| DG303ACK        | 0°C to +70°C    | 14 Lead Cerdip      |
| DG303ABWE       | -25°C to +85°C  | 16 Lead Wide SO     |
| DG303ABK        | -25°C to +85°C  | 14 Lead Cerdip      |

DG300(A)/DG301(A)/DG302(A)/DG303(A)

# TTL Compatible CMOS Analog Switches

## Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)



PDIPNEPS

|    | INCHES |       | MILLIMETERS |       |
|----|--------|-------|-------------|-------|
|    | MIN    | MAX   | MIN         | MAX   |
| A  | ---    | 0.180 | ---         | 4.572 |
| A1 | 0.015  | ---   | 0.38        | ---   |
| A2 | 0.125  | 0.175 | 3.18        | 4.45  |
| A3 | 0.055  | 0.080 | 1.40        | 2.03  |
| B  | 0.015  | 0.022 | 0.381       | 0.56  |
| B1 | 0.045  | 0.065 | 1.14        | 1.65  |
| C  | 0.008  | 0.014 | 0.2         | 0.355 |
| D1 | 0.005  | 0.080 | 0.13        | 2.03  |
| E  | 0.300  | 0.325 | 7.62        | 8.26  |
| E1 | 0.240  | 0.310 | 6.10        | 7.87  |
| e  | 0.100  | BSC.  | 2.54        | BSC.  |
| eA | 0.300  | BSC.  | 7.62        | BSC.  |
| eB | 0.400  | BSC.  | 10.16       | BSC.  |
| L  | 0.115  | 0.150 | 2.921       | 3.81  |

|   | INCHES |       | MILLIMETERS |       |    |       |
|---|--------|-------|-------------|-------|----|-------|
|   | MIN    | MAX   | MIN         | MAX   | N  | MS001 |
| D | 0.348  | 0.390 | 8.84        | 9.91  | 8  | AB    |
| D | 0.735  | 0.765 | 18.67       | 19.43 | 14 | AC    |
| D | 0.745  | 0.765 | 18.92       | 19.43 | 16 | AA    |
| D | 0.885  | 0.915 | 22.48       | 23.24 | 18 | AD    |
| D | 1.015  | 1.045 | 25.78       | 26.54 | 20 | AE    |
| D | 1.14   | 1.265 | 28.96       | 32.13 | 24 | AF    |
| D | 1.360  | 1.380 | 34.54       | 35.05 | 28 | *5    |

### NOTES:

1. D&E DO NOT INCLUDE MOLD FLASH
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
3. CONTROLLING DIMENSION: MILLIMETER
4. MEETS JEDEC MS001-XX AS SHOWN IN ABOVE TABLE
5. SIMILAR TO JEDEC MO-058AB
6. N = NUMBER OF PINS

**MAXIM**  
120 SAN GABRIEL DR. SUNNYVALE CA 94086 FAX (408) 737 7194  
PROPRIETARY INFORMATION

PACKAGE FAMILY OUTLINE: PDIP .300"  
TITLE

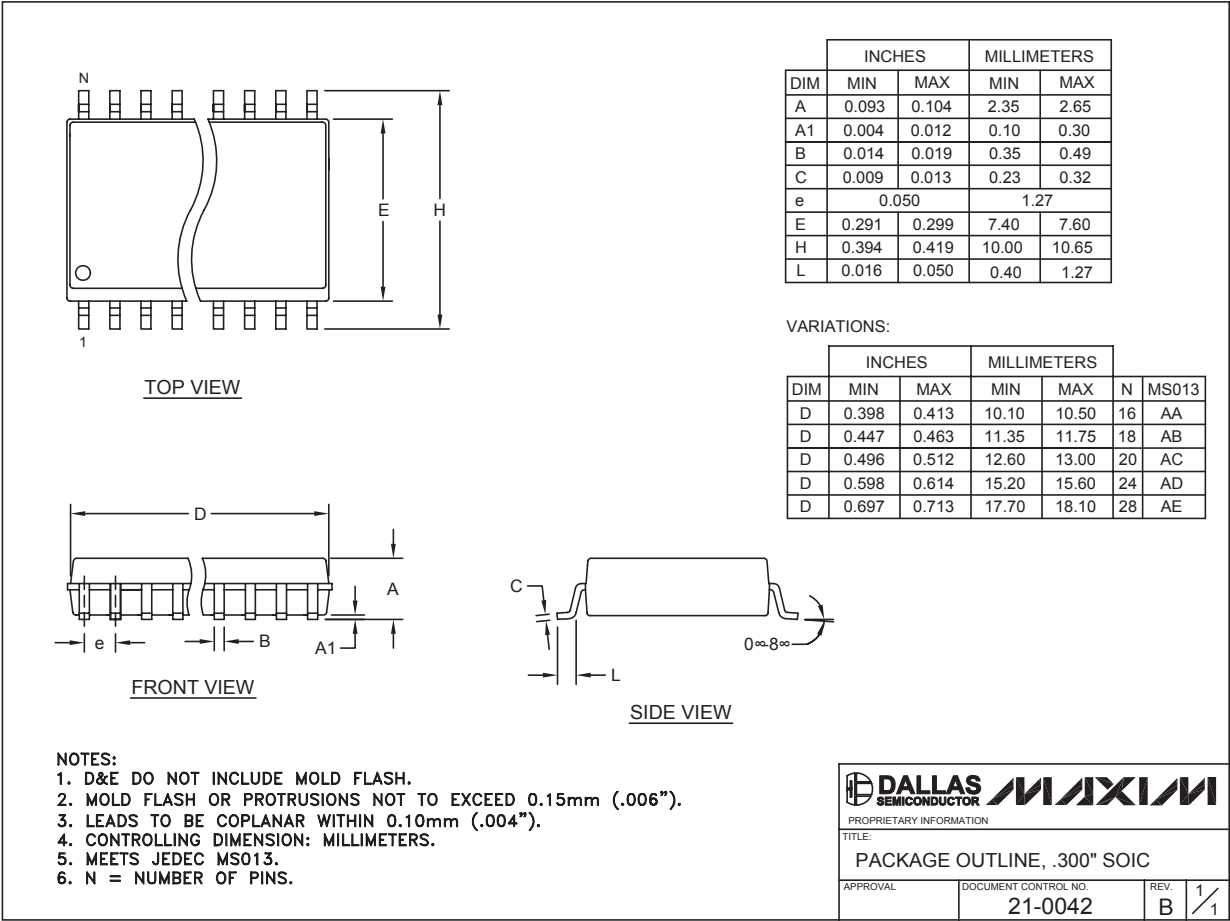
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21-0043 D  
DOCUMENT CONTROL NUMBER REV

# TTL Compatible CMOS Analog Switches

## Package Information (continued)

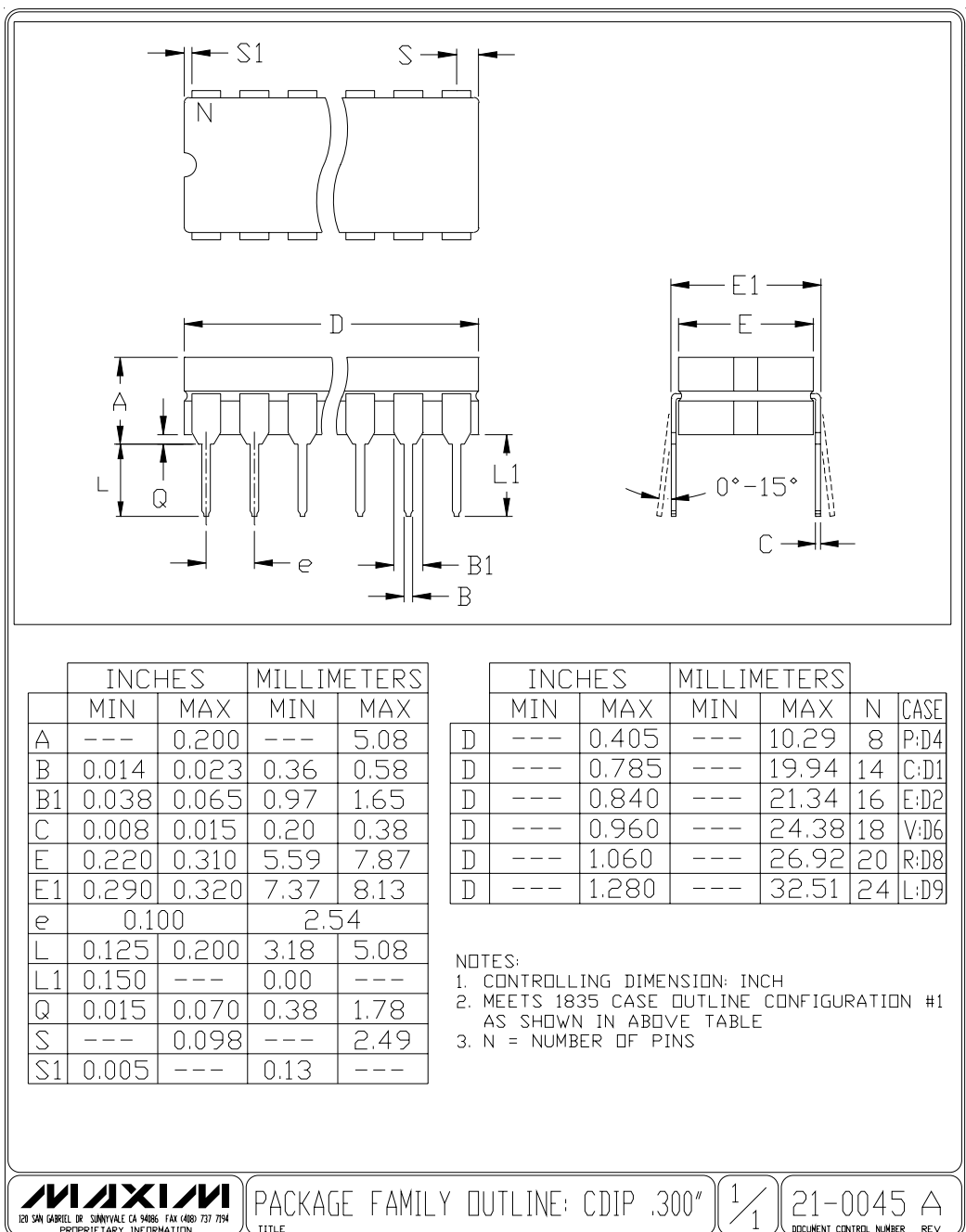
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)



# TTL Compatible CMOS Analog Switches

## Package Information (continued)

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# TTL Compatible CMOS Analog Switches

## Revision History

| REVISION NUMBER | REVISION DATE | DESCRIPTION                                     | PAGES CHANGED |
|-----------------|---------------|-------------------------------------------------|---------------|
| 0               | 2/88          | Initial release                                 | —             |
| 1               | 6/99          | Errors in the test limits and pin configuration | —             |
| 2               | 9/04          | Fixed Truth Table                               | —             |
| 3               | 11/07         | Correction to pin configuration                 | 1             |

DG300(A)/DG301(A)/DG302(A)/DG303(A)

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