

#### Current Transfer Ratio

Part Number	Classification (Note 1)	(Ic	sfer Ratio (%) / I <sub>F</sub> ) = 5 V, Ta = 25°C	Marking of Classification
		min	max	
	Blank	50	600	Blank, YE, GR, BL, GB
	Rank Y	50	50 150 YE	
TLP280	Rank GR	100	300	GR
	Rank BL	200	600	BL
	Rank GB	100	600	GB, GR, BL
TLP280-4	Blank	50	600	Blank, GB
1LF 200-4	Rank GB	100	600	GB

Note: For the supply status of TLP280 rank Y and BL products, please contact with our sales representative. Note 1: When ordering product, please specify both the part number and the classification, e.g. TLP280(GB).

Note: Application type name for certification test, please use standard product type name, i.e.

TLP280(GB): TLP280, TLP280-4(GB): TLP280-4.

## Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Ra TLP280	ting TLP280-4	Unit
Forward current		I <sub>F(RMS)</sub>	±	50	mA
Forward current derating (Ta ≥25°C)		ΔI <sub>F</sub> /°C	-0.7	-0.5	mA/°C
Pulse forward current (100 μs pulse, 100 pps)		hE6	// ±	:1	Α
LED	Diode power dissipation	PD	100	70	mW
	Diode power dissipation derating (Ta ≥25°C)	ΔP <sub>D</sub> /°C	-1	-0.7	mW/°C
	Junction temperature	Tj	<u> </u>	25	°C
	Collector-emitter voltage	VCEO	80		V
	Emitter-collector voltage	V <sub>E</sub> CO 4	7		V
Collector current		Ic (	5	mA	
Collector power dissipation (1 circuit)		Pc V	150	100	mW
٥	Collector power dissipation derating (Ta ≥ 25°C) (1 circuit)	ΔPc/°C	-1.5	-1.0	mW/°C
	Junction temperature	Ţį	125		°C
Stor	age temperature range	T <sub>stg</sub>	-55 to 125		°C
Operating temperature range		T <sub>opr</sub>	-55 to 100		°C
Lead soldering temperature (10 s)		T <sub>sol</sub>	260		°C
Total package power dissipation (1 circuit)		PT	200	170	mW
Total package power dissipation derating (Ta ≥ 25°C) (1 circuit)		ΔP <sub>T</sub> /°C	-2.0	-1.7	mW/°C
Isolation voltage (AC, 60 s, R.H.≤ 60%) (Note 1)		BVS	2500		Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc)

Note 1: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.



## **Electrical Characteristics (Ta = 25°C)**

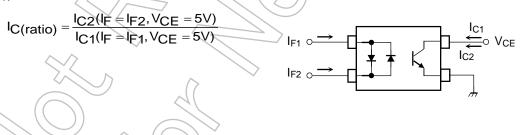
Characteristic		Symbol	Test Condition	Min	Тур	Max	Unit					
Ω	Forward voltage	VF	IF = ±10 mA	1.0	1.15	1.3	V					
"	Capacitance Capacitance		V = 0 V, f = 1 MHz	_	60	_	pF					
	Collector-emitter breakdown voltage V <sub>(E</sub>		I <sub>C</sub> = 0.5 mA	80	_		V					
	Emitter-collector breakdown voltage V <sub>(BR)</sub>		IE = 0.1 mA	7	-	_	V					
ō	5	ICEO	VCE = 48 V	(-)	0.01	0.1	^					
Detector	Callantan dank armunt (Nata 4)		Ambient light below (100 &x)		) <sub>2</sub>	10	μА					
۵	Collector dark current (Note 1)		VCE = 48 V, Ta = 85°C	/ <del>()</del>	2	50	^					
									Ambient light below (100 &x)		4	50
Capacitance (collector to emitter) CCE		V = 0 V, f = 1 MHz	> —	10	_	pF						

Note 1: Because of the construction, leak current might be increased by ambient light. Please use photocoupler with less ambient light.

# Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	۱ (	Min	Typ.	Max	Unit
Current transfer ratio	10/1-	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	0	50)	_	600	- %
Current transfer fatto	IC/IF		Rank GB	100		600	
Setunded OTP	lo/le( )	$IF = \pm 1 \text{ mA}, VCE = 0.4 \text{ V}$		) —	60	_	%
Saturated CTR	IC/IF(sat)		Rank GB	30	_	_	
		Ic = 2.4 mA, IF = ±8 mA	))	1	_	0.4	
Collector-emitter saturation voltage	VCE(sat)	IC = 0.2 mA, IF = ±1 mA		1	0.2	-	V
oata allon rollago			Rank GB		_	0.4	
Off-state collector current	IC(off)	VF = ± 0.7 V, VCE = 48 \	/	_	_	10	μΑ
CTR symmetry	IC(ratio)	IC (IF = -5 mA) / IC (IF =	5 mA) (Note 1)	0.33	_	3	_

Note 1:



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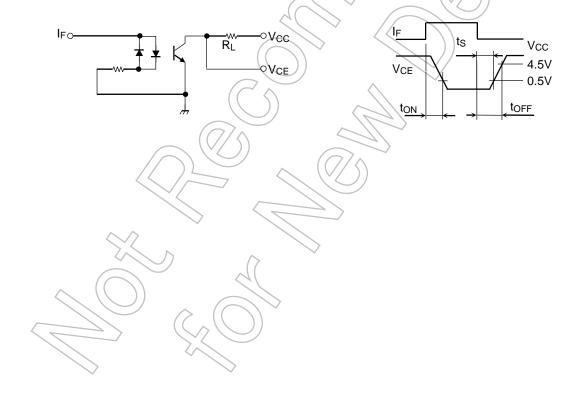
# **Isolation Characteristics (Ta = 25°C)**

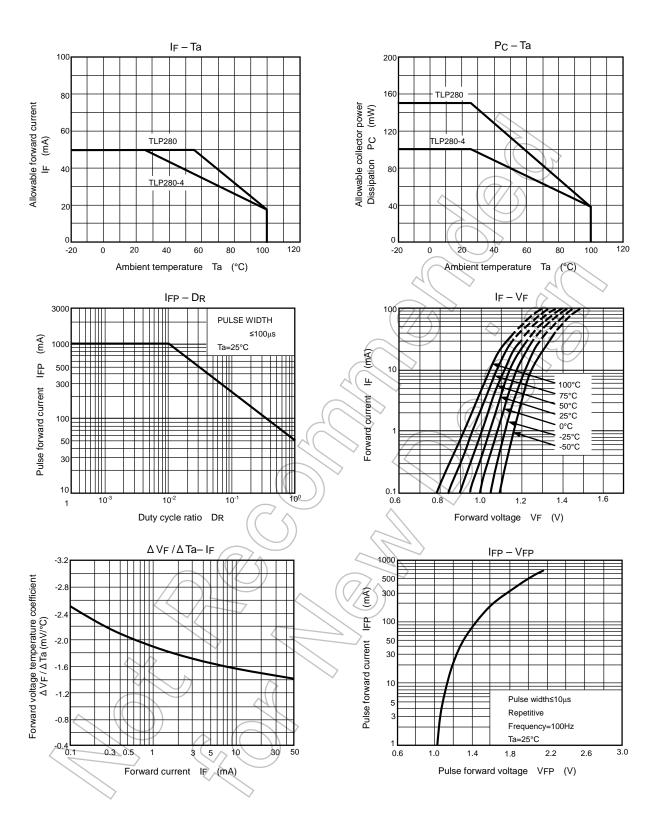
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	Vs = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H.≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	2500	_	_	V
		AC, 1 s, in oil		5000	_	V <sub>rms</sub>
		DC, 60 s, in oil	(-)	5000	_	V <sub>dc</sub>

# **Switching Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		_	2	_	— — µs
Fall time	tf	$V_{CC} = 10 \text{ V, I}_{C} = 2 \text{ mA}$ $R_L = 100 \Omega$	_	3	4	
Turn-on time	ton	R <sub>L</sub> = 100 Ω	- /	3		
Turn-off time	t <sub>off</sub>		-((	)3	_	
Turn-on time	toN		4	(2)	/ —	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = \pm 16 \text{ mA}$		25		μS
Turn-off time	toff	3(1)	<del>/</del> <del>)</del> )	40	_	

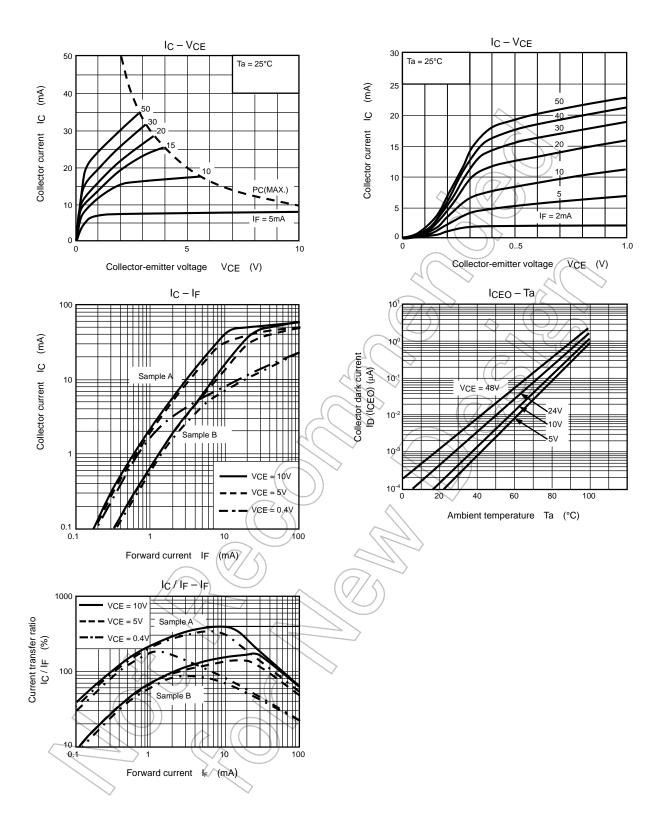
Fig. 1: Switching time test circuit



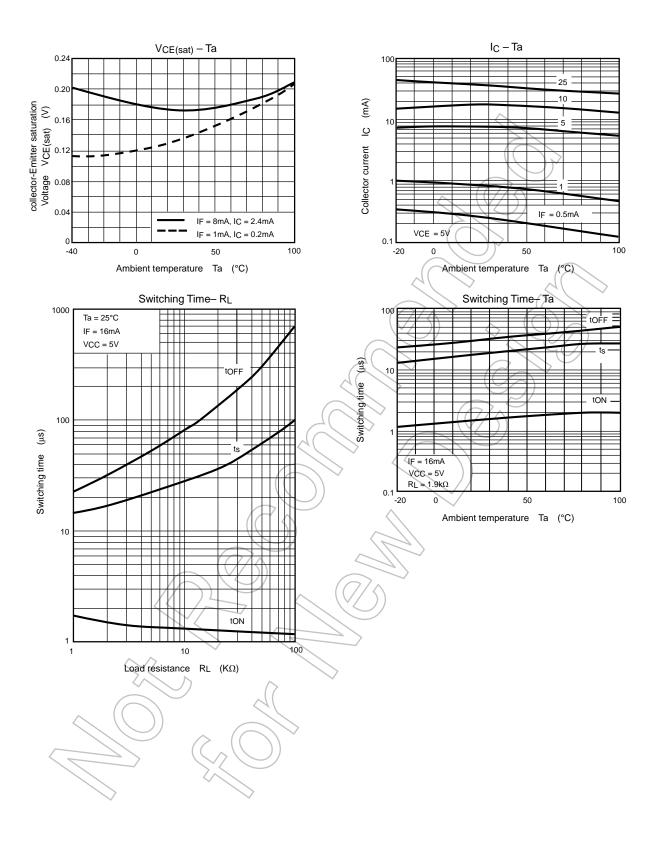


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<sup>\*</sup>The above graphs show typical characteristic.



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