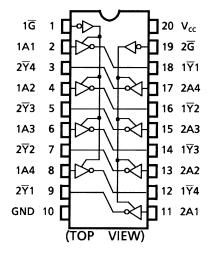
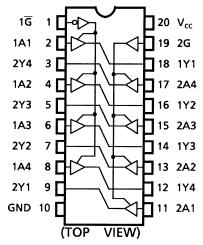
Pin Assignment

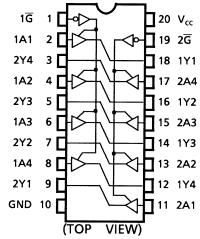
TC74HC240A



TC74HC241A

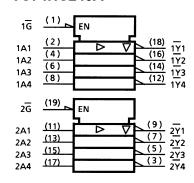


TC74HC244A

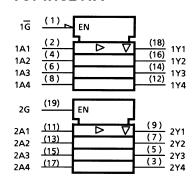


IEC Logic Symbol

TC74HC240A

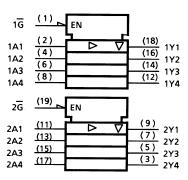


TC74HC241A



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TC74HC244A



Truth Table

	Inputs	Outputs			
G	G∆	An	Yn	\overline{Y}_n $^{\Delta\Delta}$	
L	Н	L	L	Н	
L	Н	Н	Н	L	
Н	L	Х	Z	Z	

Δ: For TC74HC241A only

 $\Delta\Delta$: For TC74HC240A only

X: Don't care

Z: High impedance



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±35	mA
DC V _{CC} /ground current	Icc	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to $65^{\circ}C$. From Ta = 65 to $85^{\circ}C$ a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	>
Operating temperature	T _{opr}	−40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.



Electrical Characteristics

DC Characteristics

Characteristics Symbo		Test Condition $V_{CC}\left(V\right)$		Ta = 25°C			Ta = -40 to 85°C		Unit	
				V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_	_	1.50	_	
High-level input voltage	V_{IH}			4.5	3.15	_	_	3.15	_	V
3.				6.0	4.20	—	_	4.20	_	
			_		_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}				_	_	1.35	_	1.35	V
3				6.0		_	1.80	_	1.80	
				2.0	1.9	2.0	_	1.9	_	V
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V _{ОН}			6.0	5.9	6.0	_	5.9	_	
J			$I_{OH} = -6 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
		V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage	V_{OL}			6.0		0.0	0.1	_	0.1	V
			I _{OL} = 6 mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 7.8 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or GND}$		6.0	_	_	±0.5	_	±5.0	μА
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		6.0		_	4.0	_	40.0	μА



AC Characteristics (input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
			CL (pF)	V _{CC} (V)	Min	Тур.	Max	Min	Max	
	t			2.0	_	25	60	_	75	
Output transition time	t _{TLH}	_	50	4.5	_	7	12	_	15	ns
	t _{THL}			6.0	—	6	10	_	13	
				2.0	_	36	90	_	115	
			50	4.5	_	12	18	_	23	
Propagation delay	t_{pLH}			6.0	—	10	15	_	20	ns
time	t_{pHL}	_		2.0	_	51	130	_	165	113
			150	4.5	_	17	26	_	33	
				6.0	—	14	22	_	28	
	^t pZL ^t pZH	$R_L = 1 \text{ k}\Omega$	50	2.0	_	48	125	_	155	- ns
				4.5	_	16	25	_	31	
Output enable time				6.0	_	14	21	_	26	
Output enable time			150	2.0	_	63	165	_	205	
				4.5	_	21	33	_	41	
				6.0	_	18	28	_	35	
	t _{pLZ} t _{pHZ}	$R_L = 1 \text{ k}\Omega$	50	2.0	_	32	125	_	155	ns
Output disable time				4.5	_	15	25	_	31	
				6.0	_	14	21	_	26	
Input capacitance	C _{IN}	_			_	5	10	_	10	pF
Output capacitance	C _{OUT}	_	_		_	10	_	_	_	pF
Power dissipation	C _{PD}	TC74HC240A			_	31	_	_	_	pF
capacitance	(Note)	TC74HC241A/244A			_	33	_	_	_	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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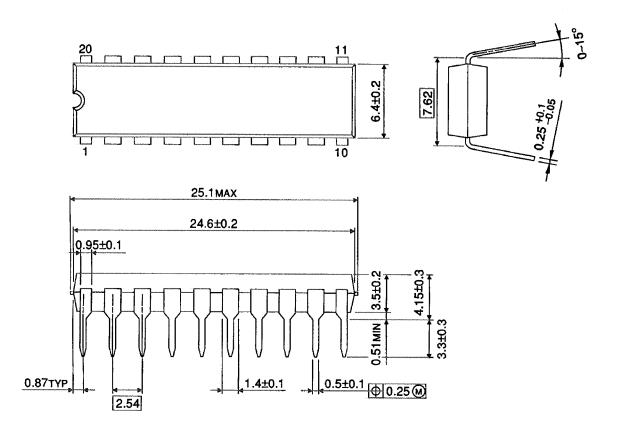
Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$ (per bit)



Package Dimensions

DIP20-P-300-2.54A Unit: mm



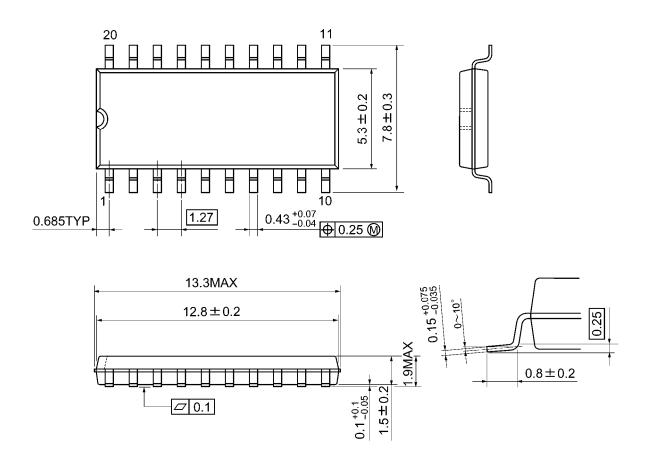
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Weight: 1.30 g (typ.)



Package Dimensions

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

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