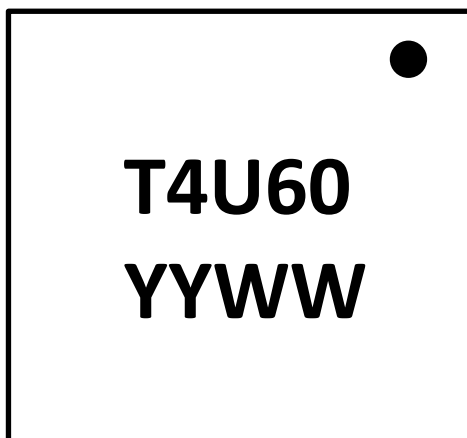


Marking Information



T4U60 = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 18 for 2018)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
 For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	60	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _{RM}		
Average Rectified Output Current	I _O	4	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	25	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	110	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	R _{θJC}	10	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R _{θJA}	70	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R _{θJC}	4	°C/W
Total Power Dissipation (Note 5)	P _{TOT}	1.4	W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V _F	—	0.38	0.52	V	I _F = 2A, T _J = +25°C
			0.46			I _F = 4A, T _J = +25°C
			0.33			I _F = 2A, T _J = +125°C
			0.45			I _F = 4A, T _J = +125°C
Leakage Current (Note 7)	I _R	—	30	150	μA	V _R = 60V, T _J = +25°C
			6	—	mA	V _R = 60V, T _J = +125°C
Total Capacitance	C _T	—	180	—	pF	V _R = 5V, f = 1MHz

Notes: 5. Device mounted on FR-4 substrate, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.
 6. Device mounted on FR-4 substrate, 2 oz. Copper, 1 sq. inch Cu pad.
 7. Short duration pulse test used to minimize self-heating effect.

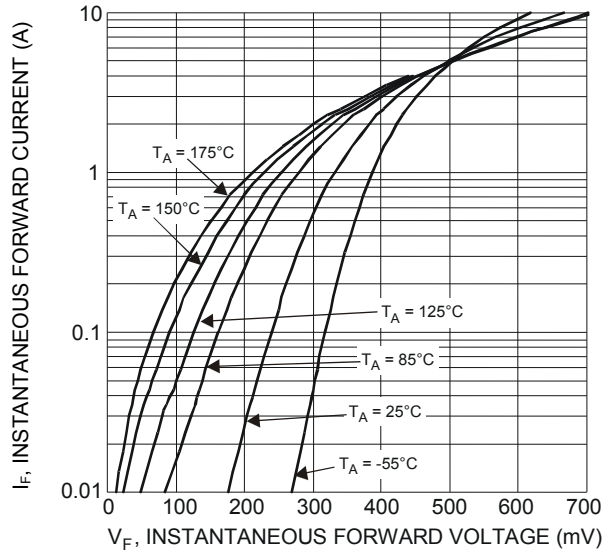


Figure 1 Typical Forward Characteristics

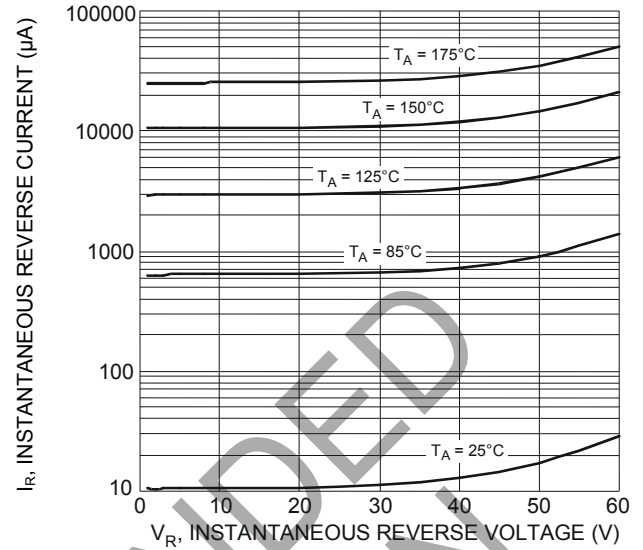


Figure 2 Typical Reverse Characteristics

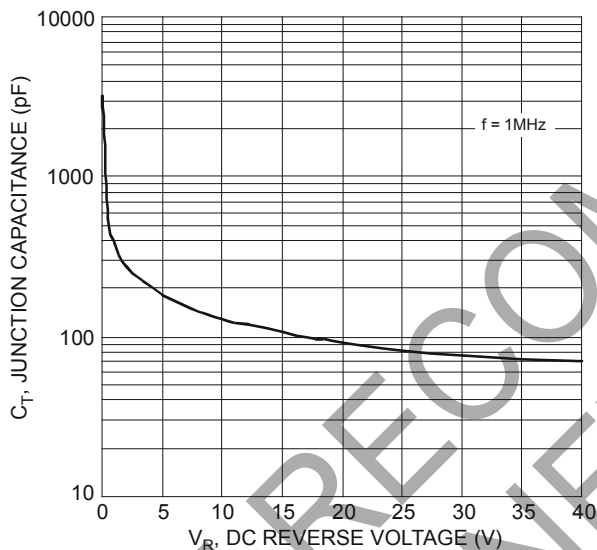


Figure 3 Typical Junction Capacitance

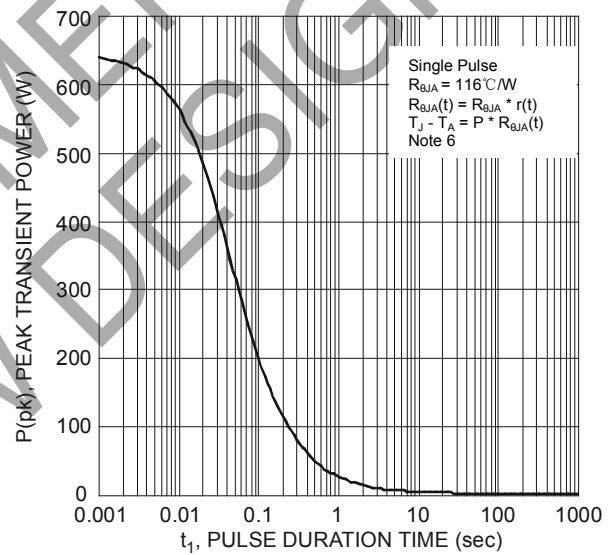
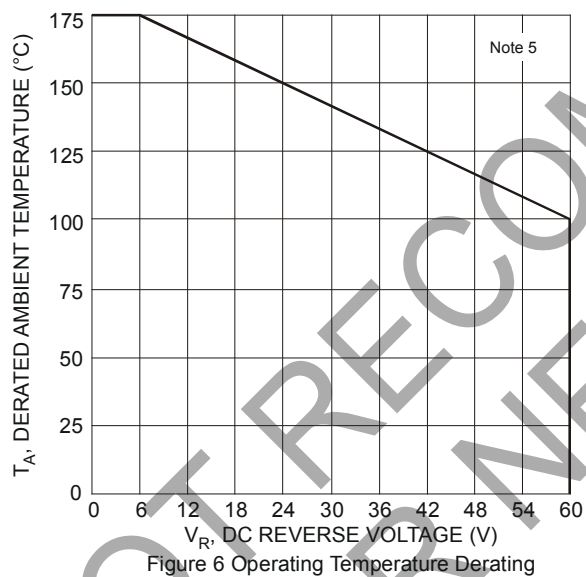
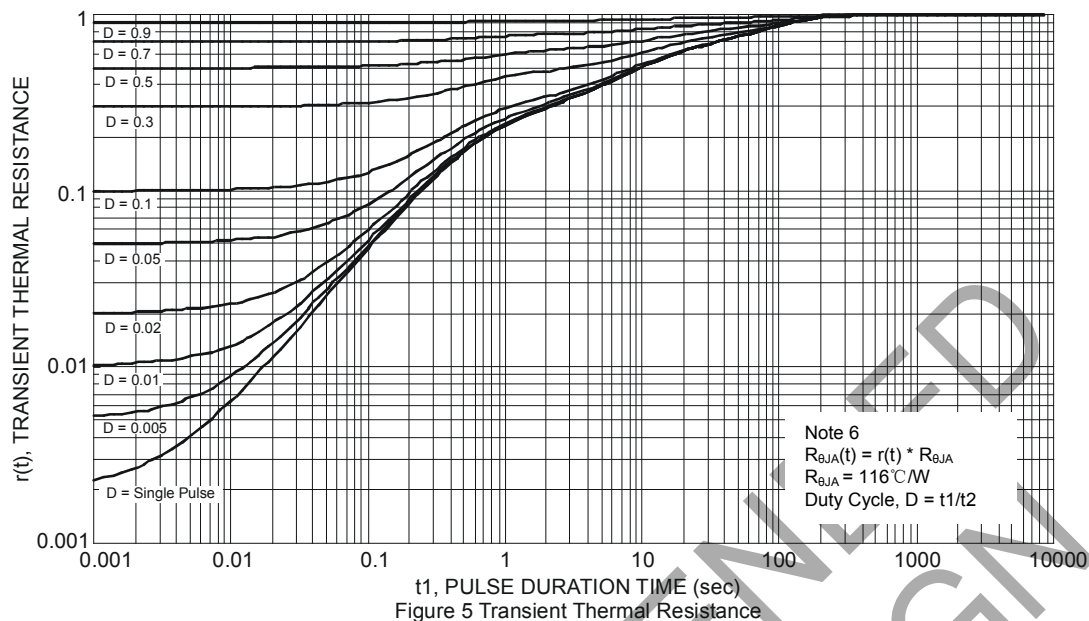


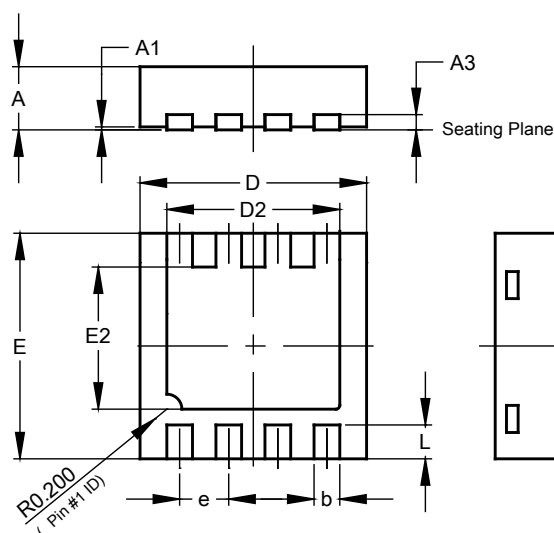
Figure 4 Single Pulse Maximum Power Dissipation



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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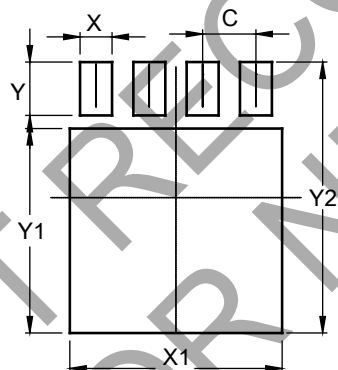


U-DFN3030-8			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0	0.05	0.02
A3	-	-	0.15
b	0.29	0.39	0.34
D	2.90	3.10	3.00
D2	2.19	2.39	2.29
e	-	-	0.65
E	2.90	3.10	3.00
E2	1.64	1.84	1.74
L	0.30	0.60	0.45
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN3030-8



Dimensions	Value (in mm)
C	0.650
X	0.390
X1	2.590
Y	0.650
Y1	2.490
Y2	3.300

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