

Maximum Ratings (Per Leg) @TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage		V _{RRM} V _{RWM}	300	V
DC Blocking Voltage		V_{RM}		
Average Rectified Output Current	er Leg Total	lo	20 40	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Lo		I _{FSM}	200	A

Thermal Characteristics (Per Leg)

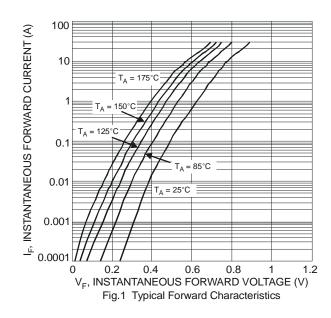
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance			
Thermal Resistance Junction to Case (Note 4)	R _θ JC	2	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-65 to +175	°C

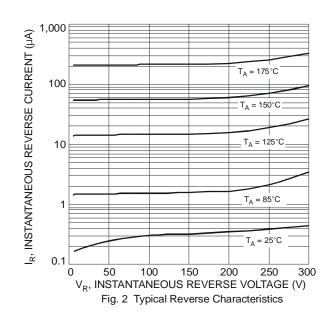
Electrical Characteristics (Per Leg) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (per leg)	VF	-	0.87	0.92 0.81	V	I _F = 20A, T _J = 25°C I _F = 20A, T _J = 125°C
Leakage Current (Note 5)	I _R	-	-	100 50		$V_R = 300V, T_J = 25^{\circ}C$ $V_R = 300V, T_J = 125^{\circ}C$
Reverse Recovery Time	t _{rr}	-	32	50	ns	$I_F = 0.5A$, $I_R = 1A$, $I_{RR} = 0.25A$
		-	26	35		$I_F = 1A$, $V_R = 30V$, di/dt = 100A/ μ s, $T_J = 25$ °C

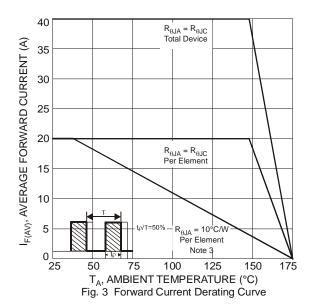
Notes:

- 4. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf
- 5. Short duration pulse test used to minimize self-heating effect.

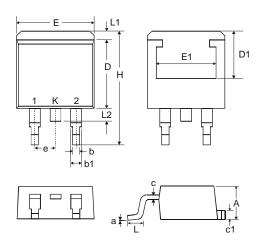






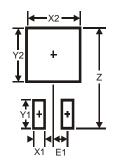


Package Outline Dimensions



D ² PAK				
Dim	Min	Max		
Α	4.07	4.82		
b	0.51	0.99		
b1	1.15	1.77		
С	0.356	0.58		
c1	1.143	1.65		
D	8.39	9.65		
D1	6.55	_		
Е	9.66	10.66		
E1	6.23	_		
е	2.54 Typ			
Η	14.61	15.87		
L	1.78	2.79		
L1	_	1.67		
L2	_	1.77		
а	0°	8°		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	16.9
X1	1.1
X2	10.8
Y1	3.5
Y2	7.01
E1	2.5



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