ABSOLUTE MAXIMUM RATINGS

V_{CC} to GND ....-0.3V to +30V
RS+, RS- to GND...-0.3V to +30V
OUT to GND ....-0.3V to (V_{CC} + 0.3V)
Output Short-Circuit to GND ....Continuous
Differential Input Voltage (V_{RS+} - V_{RS-}) ....±5V
Current Into Any Pin...±20mA
Continuous Power Dissipation (T_A = +70°C)
5-Pin SC70 (derate 2.27mW/°C above +70°C)...........200mW
6-Pin SOT23 (derate 8.7mW/°C above +70°C)...........696mW

Operating Temperature Range ....-40°C to +125°C
Junction Temperature ....+150°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+300°C
Soldering Temperature ....+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{RS+} = +2V to +28V, V_{SENSE} = (V_{RS+} - V_{RS-}) = 0, V_{CC} = +3V to +28V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Operating Voltage Range	V_{CC}	(Note 2)		3		28	V
Common-Mode Input Range	V_{CMR}	(Note 3)		2		28	V
Common-Mode Rejection	CMR	V_{SENSE} = 100mV, V_{CC} = 12V			90		dB
Supply Current	I_{CC}	V_{CC} = 28V			0.5	1.2	mA
Leakage Current	I_{RS+}/I_{RS-}	V_{CC} = 0V, V_{RS+} = 28V			0.05	1	µA
Input Bias Current	I_{RS+}				20	60	µA
	I_{RS-}				40	120	
Full-Scale Sense Voltage	V_{SENSE}	V_{SENSE} = (V_{RS+} - V_{RS-})			150		mV
Total OUT Voltage Error (Note 4)		V_{SENSE} = 100mV, V_{CC} = 12V, V_{RS+} = 2V			±1.0		%
		V_{SENSE} = 100mV, V_{CC} = 12V, V_{RS+} = 12V, T_A = +25°C			±1.0	±5.0	
		V_{SENSE} = 100mV, V_{CC} = 12V, V_{RS+} = 12V, T_A = T_{MIN} to T_{MAX}				±7.0	
		V_{SENSE} = 100mV, V_{CC} = 28V, V_{RS+} = 28V, T_A = +25°C			±1.0	±5.0	
		V_{SENSE} = 100mV, V_{CC} = 28V, V_{RS+} = 28V, T_A = T_{MIN} to T_{MAX}				±8.5	
		V_{SENSE} = 6.25mV (Note 5); V_{CC} = 12V, V_{RS+} = 12V			±7.5		
Extrapolated Input Offset Voltage	V_{OS}	V_{CC} = V_{RS+} = 12V, V_{SENSE} > 10mV			1.0		mV
OUT High Voltage	(V_{CC} - V_{OH})	V_{SENSE} = 150mV	MAX4073T, V_{CC} = 3V		0.8	1.2	V
			MAX4073F, V_{CC} = 7.5V				
			MAX4073H, V_{CC} = 15V				

ELECTRICAL CHARACTERISTICS (continued)

(V_{RS+} = +2V to +28V, V_{SENSE} = (V_{RS+} - V_{RS-}) = 0, V_{CC} = +3V to +28V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

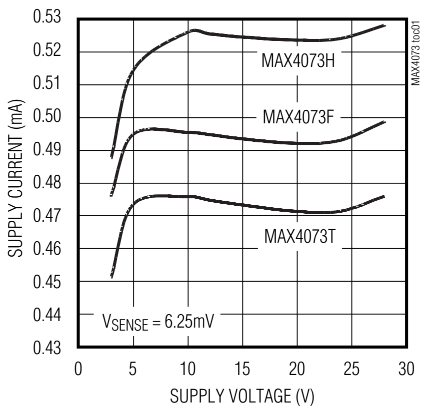
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Bandwidth	BW	V_{CC} = 12V, V_{RS+} = 12V, C_{LOAD} = 5pF	MAX4073T, V_{SENSE} = 100mV		1.8		MHz
			MAX4073F, V_{SENSE} = 100mV		1.7		
			MAX4073H, V_{SENSE} = 100mV		1.6		
			MAX4073T/F/H V_{SENSE} = 6.25mV (Note 5)		600		kHz
Gain	A_V	MAX4073T			20		V/V
		MAX4073F			50		
		MAX4073H			100		
Gain Accuracy	ΔA_V	V_{CC} = 12V, V_{RS+} = 12V, V_{SENSE} = 10mV to 150mV, MAX4073T/F	T_A = +25°C		±1.0	±4.5	%
			T_A = T_{MIN} to T_{MAX}			±6.5	
		V_{CC} = 12V, V_{RS+} = 12V, V_{SENSE} = 10mV to 100mV, MAX4073H	T_A = +25°C		±1.0	±4.5	
			T_A = T_{MIN} to T_{MAX}			±6.5	
OUT Settling Time to 1% of Final Value		V_{CC} = 12V V_{RS+} = 12V C_{LOAD} = 5pF	V_{SENSE} = 6.25mV to 100mV		400		ns
			V_{SENSE} = 100mV to 6.25mV		800		
Output Resistance	R_{OUT}				12		kΩ
Power-Supply Rejection Ratio	PSRR	V_{CC} = 3V to 28V	V_{SENSE} = 60mV, MAX4073T	70	78		dB
			V_{SENSE} = 24mV, MAX4073F	70	85		
			V_{SENSE} = 12mV, MAX4073H	70	90		
Power-Up Time (Note 6)		C_{LOAD} = 5pF, V_{SENSE} = 100mV			5		µs
Saturation Recovery Time (Note 7)		V_{CC} = 12V, V_{RS+} = 12V, C_{LOAD} = 5pF			5		µs

Note 1: All devices are 100% production tested at T_A = +25°C. All temperature limits are guaranteed by design.
Note 2: Inferred from PSRR test.
Note 3: Inferred from OUT Voltage Error test.
Note 4: Total OUT Voltage Error is the sum of the gain and offset errors.
Note 5: 6.25mV = 1/16 of 100mV full-scale sense voltage.
Note 6: Output settles to within 1% of final value.
Note 7: The device will not experience phase reversal when overdriven.

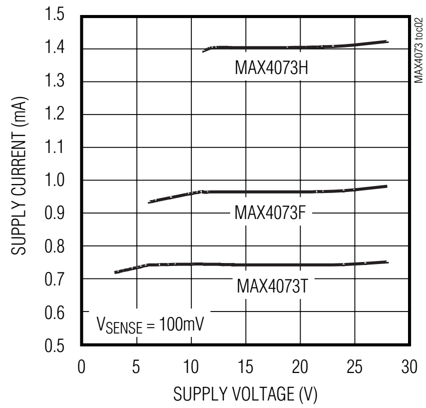
Typical Operating Characteristics

(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = 100mV, C_L = 5pF, T_A = +25°C, unless otherwise noted.)

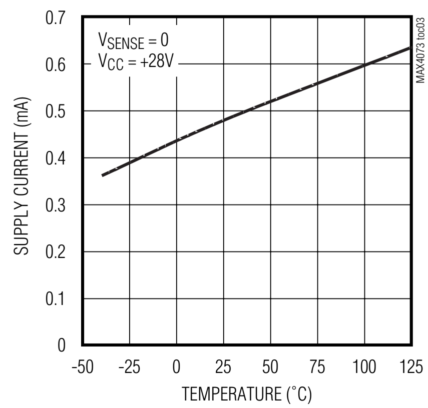
SUPPLY CURRENT vs. SUPPLY VOLTAGE



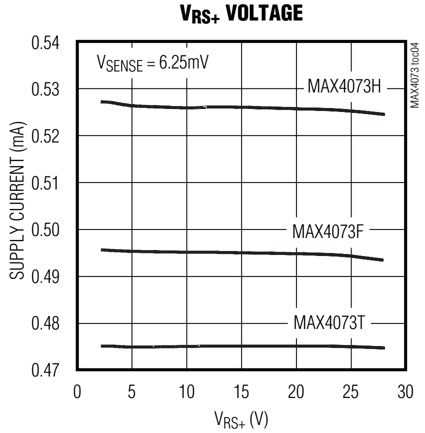
SUPPLY CURRENT vs. SUPPLY VOLTAGE



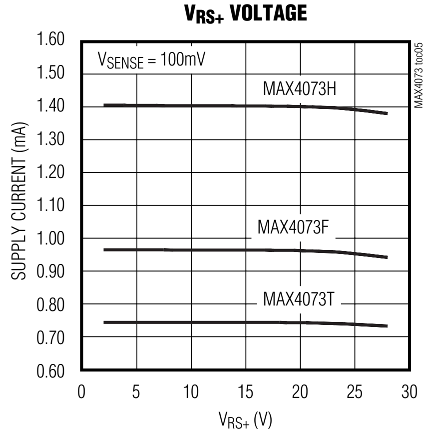
SUPPLY CURRENT vs. TEMPERATURE



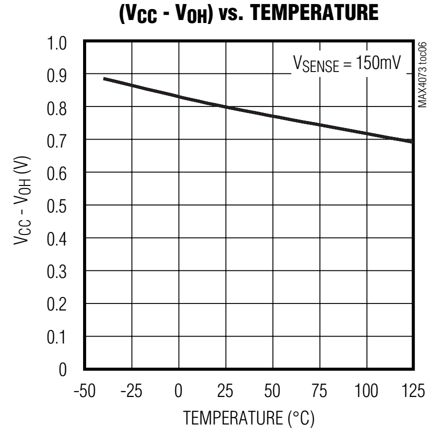
SUPPLY CURRENT vs. V_{RS+} VOLTAGE



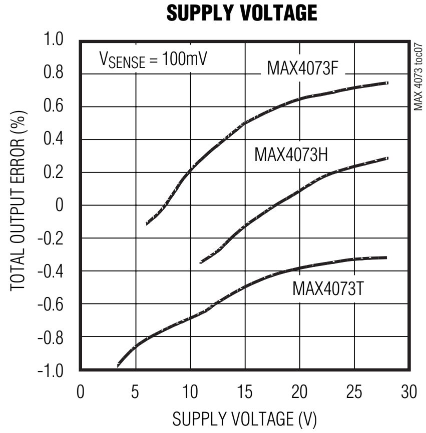
SUPPLY CURRENT vs. V_{RS+} VOLTAGE



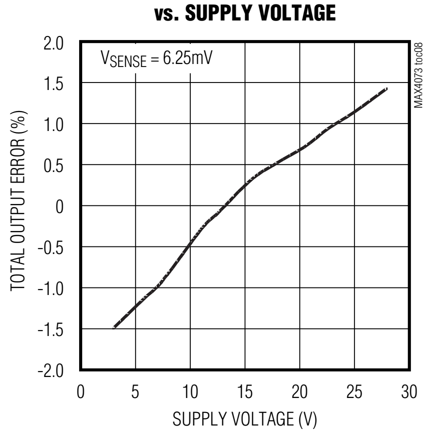
OUTPUT HIGH VOLTAGE (V_{CC} - V_{OH}) vs. TEMPERATURE



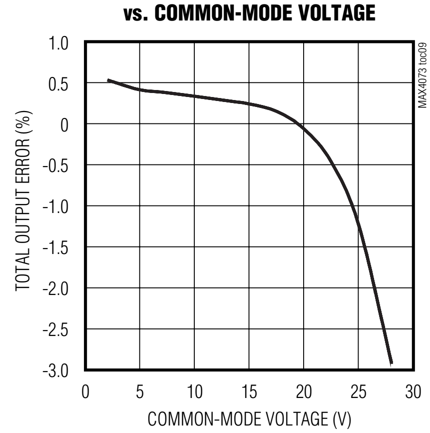
TOTAL OUTPUT ERROR vs. SUPPLY VOLTAGE



TOTAL OUTPUT ERROR vs. SUPPLY VOLTAGE



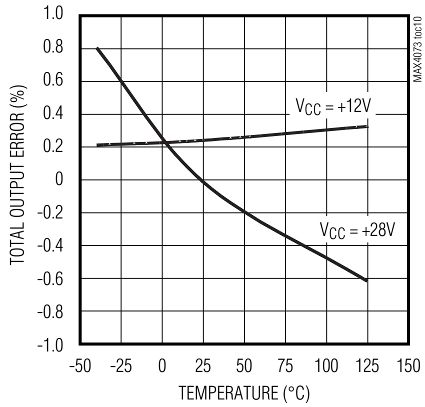
TOTAL OUTPUT ERROR vs. COMMON-MODE VOLTAGE



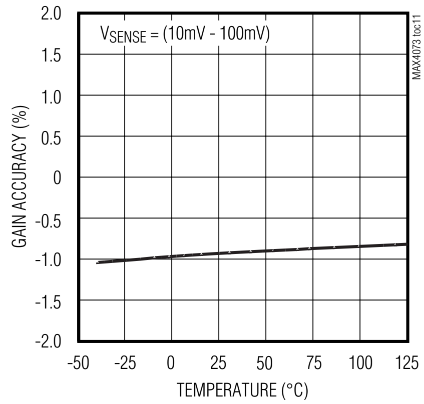
Typical Operating Characteristics (continued)

(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = 100mV, C_L = 5pF, T_A = +25°C, unless otherwise noted.)

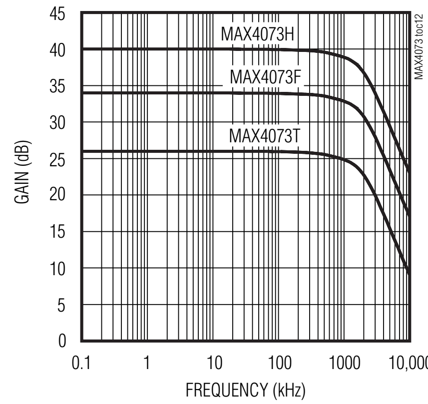
TOTAL OUTPUT ERROR vs. TEMPERATURE



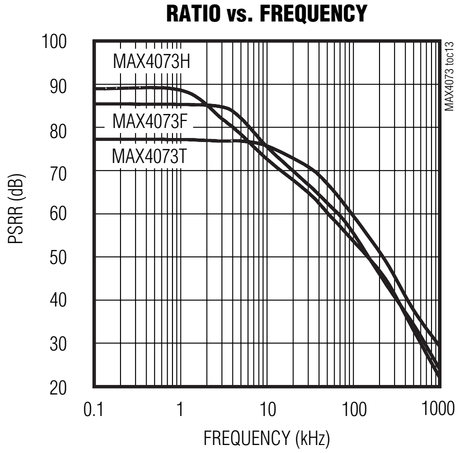
GAIN ACCURACY vs. TEMPERATURE



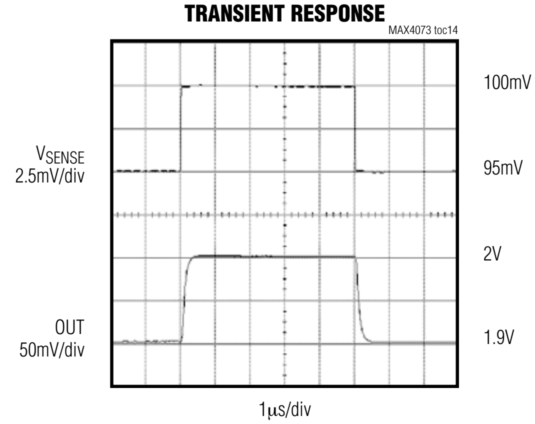
SMALL-SIGNAL GAIN vs. FREQUENCY



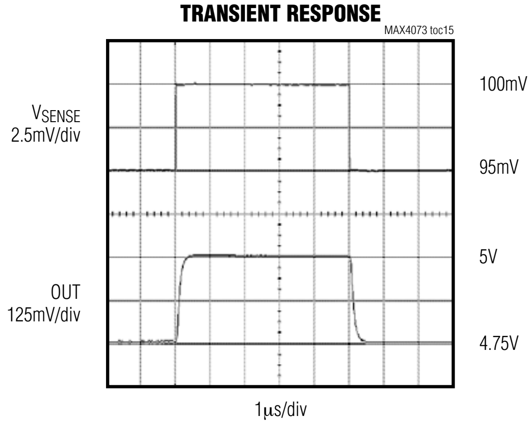
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



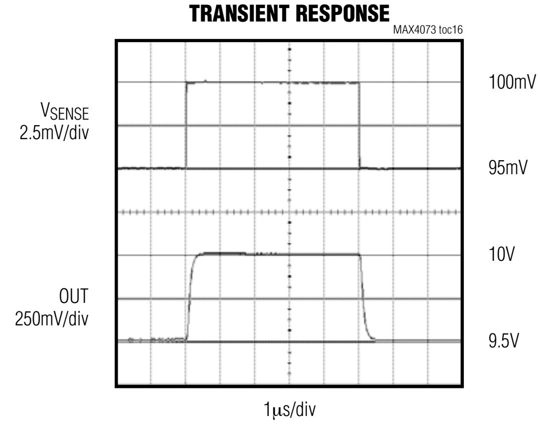
MAX4073T SMALL-SIGNAL TRANSIENT RESPONSE



MAX4073F SMALL-SIGNAL TRANSIENT RESPONSE



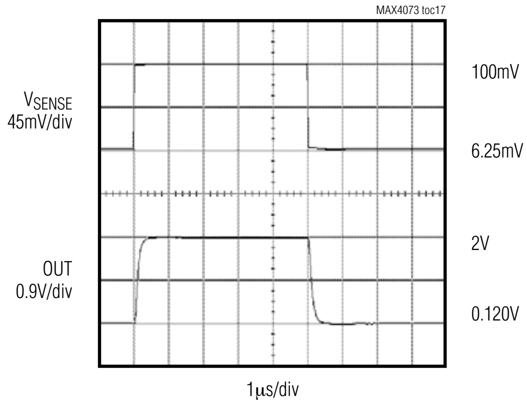
MAX4073H SMALL-SIGNAL TRANSIENT RESPONSE



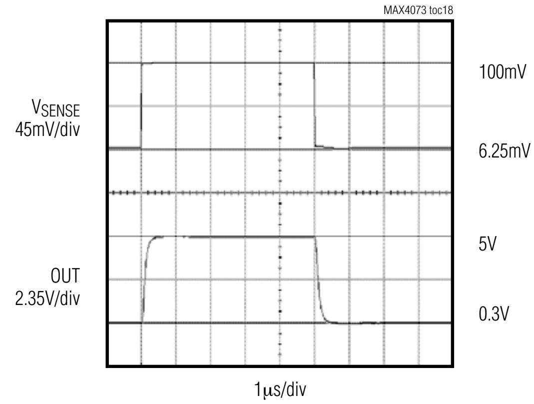
Typical Operating Characteristics (continued)

(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = 100mV, C_L = 5pF, T_A = +25°C, unless otherwise noted.)

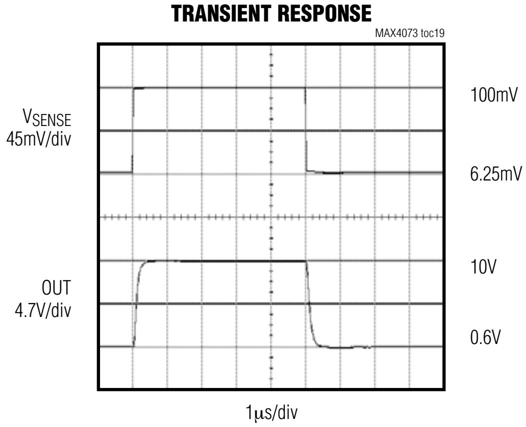
MAX4073T LARGE-SIGNAL TRANSIENT RESPONSE



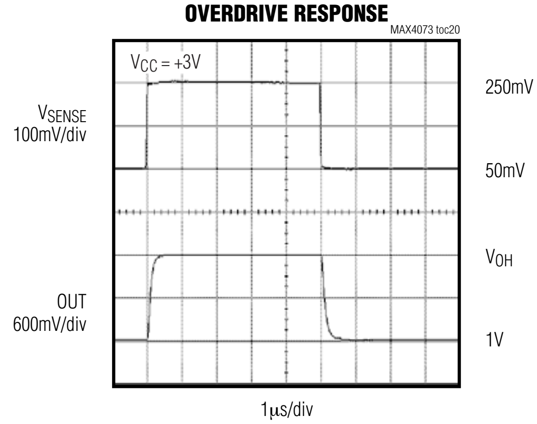
MAX4073F LARGE-SIGNAL TRANSIENT RESPONSE



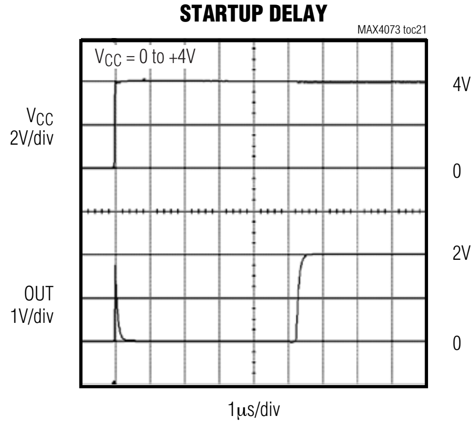
MAX4073H LARGE-SIGNAL TRANSIENT RESPONSE



MAX4073T OVERDRIVE RESPONSE



MAX4073T STARTUP DELAY



Pin Description

PIN		NAME	FUNCTION
SOT23	SC70		
1, 2	2	GND	Ground
3	3	V_{CC}	Supply Voltage Input. Bypass to GND with a 0.1µF capacitor.
4	4	RS+	Power-Side Connection to the External Sense Resistor
5	5	RS-	Load-Side Connection to the External Sense Resistor
6	1	OUT	Voltage Output. V_{OUT} is proportional to V_{SENSE}. Output impedance is approximately 12kΩ.

Detailed Description

The MAX4073 high-side current-sense amplifier features a +2V to +28V input common-mode range that is independent of supply voltage. This feature allows the monitoring of current out of a battery as low as +2V and also enables high-side current sensing at voltages greater than the supply voltage (V_{CC}).

The MAX4073 operates as follows: current from the source flows through R_{SENSE} to the load (Figure 1). Since the internal-sense amplifier's inverting input has high impedance, negligible current flows through RG2 (neglecting the input bias current). Therefore, the sense amplifier's inverting-input voltage equals V_{SOURCE} - (I_{LOAD})(R_{SENSE}). The amplifier's open-loop gain forces its noninverting input to the same voltage as the inverting input. Therefore, the drop across RG1 equals (I_{LOAD})(R_{SENSE}). Since I_{RG1} flows through RG1, I_{RG1} = (I_{LOAD})(R_{SENSE}) / RG1. The internal current mirror multiplies I_{RG1} by a current gain factor, β, to give $I_{RGD} = \beta \times I_{RG1}$. Solving $I_{RGD} = \beta \times (I_{LOAD})(R_{SENSE})/RG1$. Assuming infinite output impedance, $V_{OUT} = (I_{RGD})(RGD)$. Substituting in for I_{RGD} and rearranging, $V_{OUT} = \beta \times (RGD/RG1)(R_{SENSE} \times I_{LOAD})$. The parts gain equals β × RGD/RG1. Therefore, V_{OUT} = (GAIN) (R_{SENSE}) (I_{LOAD}), where GAIN = 20V/V for MAX4073T, GAIN = 50V/V for MAX4073F, and GAIN = 100V/V for MAX4073H.

Set the full-scale output range by selecting R_{SENSE} and the appropriate gain version of the MAX4073.

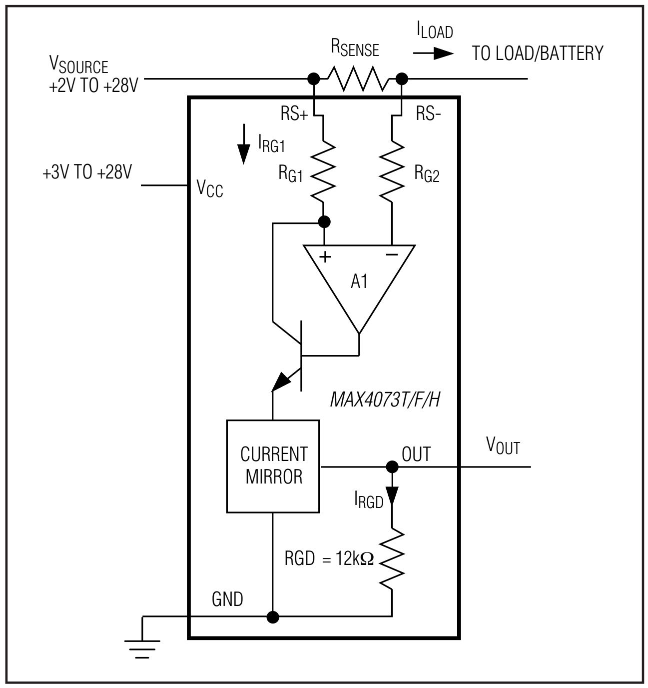


Figure 1. Functional Diagram

Applications Information

Recommended Component Values

The MAX4073 senses a wide variety of currents with different sense resistor values. Table 1 lists common resistor values for typical operation of the MAX4073.

Choosing R_{SENSE}

To measure lower currents more accurately, use a large value for R_{SENSE}. The larger value develops a higher-sense voltage that reduces offset voltage errors of the internal op amp. Typical sense voltages range between 10mV and 150mV.

In applications monitoring very high currents, R_{SENSE} must be able to dissipate the I^2R losses. If the resistor's rated power dissipation is exceeded, its value may drift or it may fail altogether, causing a differential voltage across the terminals in excess of the absolute maximum ratings (±5V).

If I_{SENSE} has a large high-frequency component, minimize the inductance of R_{SENSE}. Wire-wound resistors have the highest inductance, metal-film resistors are somewhat better, and low-inductance metal-film resistors are best suited for these applications.

For V_{SENSE} = 100mV, full-scale output voltage can be 2V, 5V, or 10V depending on the gain. For proper operation, ensure V_{CC} exceeds the full-scale output voltage by 1.2V (see Output High Voltage (V_{CC} - V_{OH}) vs. Temperature in the *Typical Operating Characteristics*).

Using a PCB Trace as R_{SENSE}

If the cost of R_{SENSE} is an issue and accuracy is not critical, use the alternative solution shown in Figure 2. This solution uses copper PC board traces to create a sense resistor. The resistivity of a 0.1-inch-wide trace of 2-ounce copper is approximately 30mΩ/ft. The resistance-temperature coefficient of copper is fairly high (approximately 0.4%/°C), so systems that experience a wide temperature variance must compensate for this effect. In addition, do not exceed the maximum power dissipation of the copper trace.

For example, the MAX4073T (with a maximum load current of 10A and an R_{SENSE} of 5mΩ) creates a full-scale V_{SENSE} of 50mV that yields a maximum V_{OUT} of 1V. R_{SENSE} in this case requires about 2 inches of 0.1 inch-wide copper trace.

Output Impedance

The output of the MAX4073 is a current source driving a 12kΩ resistance. Resistive loading added to OUT reduces the output gain of the MAX4073. To minimize output errors for most applications, connect OUT to a high-impedance input stage. When output buffering is required, choose an op amp with a common-mode input range and an output voltage swing that includes ground when operating with a single supply. The op amp's supply voltage range should be at least as high as any voltage the system may encounter.

The percent error introduced by output loading is determined with the following formula:

$$\%_{ERROR} = 100\left(\frac{R_{LOAD}}{12k\Omega + R_{LOAD}} - 1\right)$$

where R_{LOAD} is the external load applied to OUT.

Current Source Circuit

Figure 3 shows a block diagram using the MAX4073 with a switching regulator to make a current source.

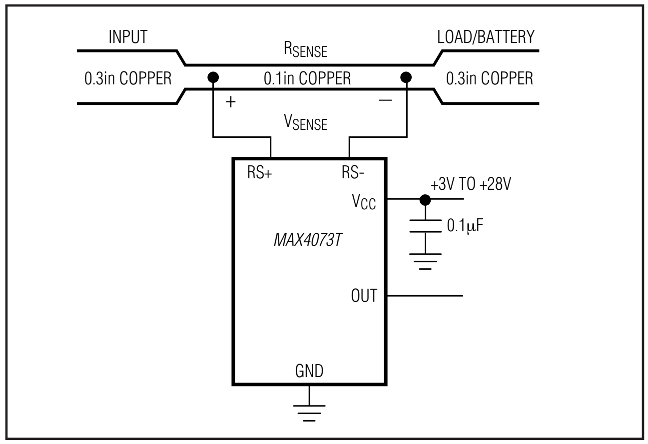


Figure 2. MAX4073T Connections Showing Use of PC Board

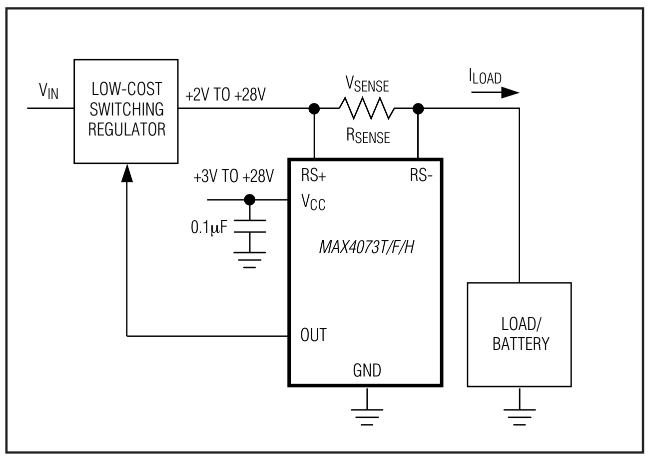
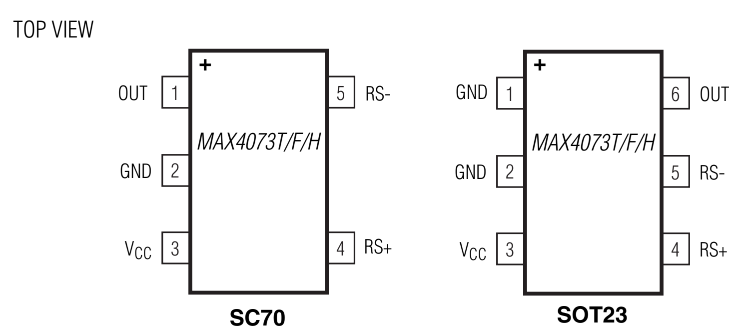


Figure 3. Current Source

Table 1. Recommended Component Values

FULL-SCALE LOAD CURRENT I_{LOAD} (A)	CURRENT-SENSE RESISTOR R_{SENSE} (mΩ)	GAIN	FULL-SCALE OUTPUT VOLTAGE (FULL-SCALE V_{SENSE} = 100mV) V_{OUT} (V)
0.1	1000	20	2.0
		50	5.0
		100	10.0
1	100	20	2.0
		50	5.0
		100	10.0
5	20	20	2.0
		50	5.0
		100	10.0
10	10	20	2.0
		50	5.0
		100	10.0

Pin Configurations



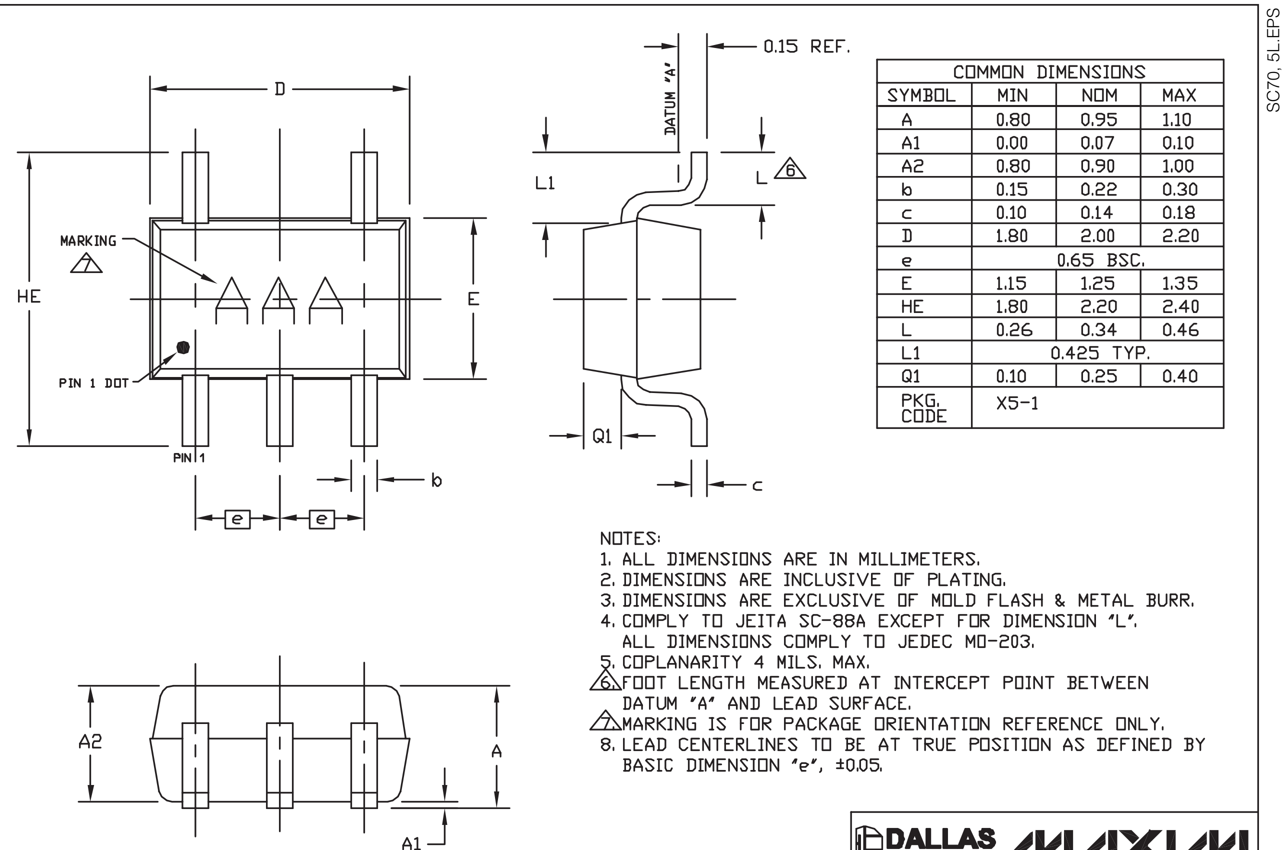
Chip Information

PROCESS: Bipolar

Package Information

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
5 SC70	X5+1	21-0076	90-0188
6 SOT23	U6+4	21-0058	90-0175



COMMON DIMENSIONS			
SYMBOL	MIN	NOM	MAX
A	0.80	0.95	1.10
A1	0.00	0.07	0.10
A2	0.80	0.90	1.00
b	0.15	0.22	0.30
c	0.10	0.14	0.18
D	1.80	2.00	2.20
e	0.65 BSC.		
E	1.15	1.25	1.35
HE	1.80	2.20	2.40
L	0.26	0.34	0.46
L1	0.425 TYP.		
Q1	0.10	0.25	0.40
PKG. CODE	X5-1		

NOTES:
1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONS ARE INCLUSIVE OF PLATING.
3. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. COMPLY TO JEITA SC-88A EXCEPT FOR DIMENSION "L". ALL DIMENSIONS COMPLY TO JEDEC MO-203.
5. COPLANARITY 4 MILS. MAX.
6. FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM "A" AND LEAD SURFACE.
7. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.
8. LEAD CENTERLINES TO BE AT TRUE POSITION AS DEFINED BY BASIC DIMENSION "e", ±0.05.

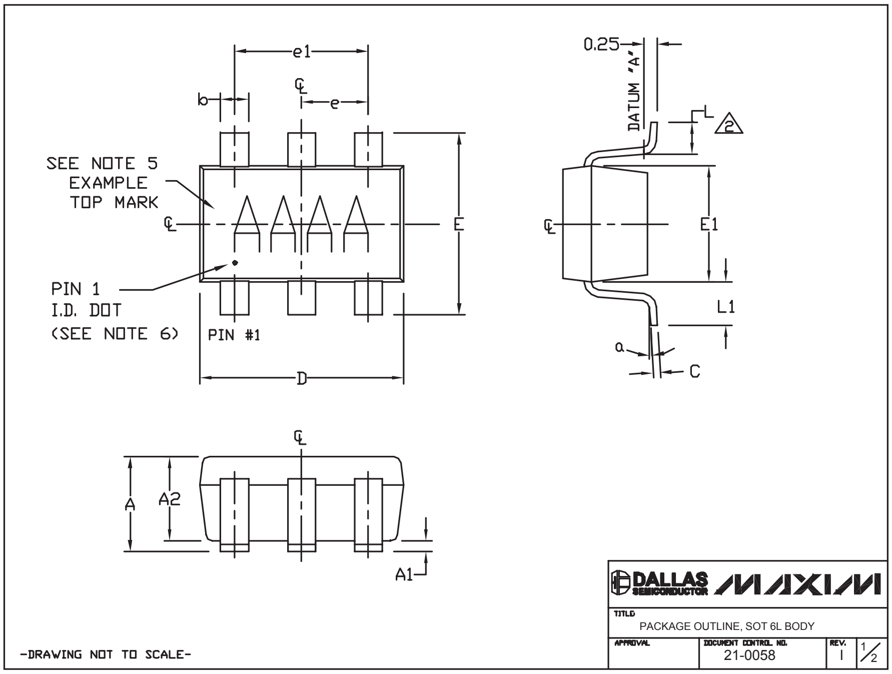
DALLAS SEMICONDUCTOR MAXIM

TITLE: PACKAGE OUTLINE, 5L SC70			
APPROVAL	DOCUMENT CONTROL NO. 21-0076	REV. E	1/1

-DRAWING NOT TO SCALE-

Package Information (continued)

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.



NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. (△) FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE.
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR. MOLD FLASH, PROTRUSION OR METAL BURR SHOULD NOT EXCEED 0.25mm.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. PIN 1 IS LOWER LEFT PIN WHEN READING TOP MARK FROM LEFT TO RIGHT. (SEE EXAMPLE TOP MARK)
6. PIN 1 I.D. DOT IS 0.3mm ø MIN. LOCATED ABOVE PIN 1.
7. MEETS JEDEC MO178, VARIATION AB.
8. SOLDER THICKNESS MEASURED AT FLAT SECTION OF LEAD BETWEEN 0.08mm AND 0.15mm FROM LEADTIP.
9. LEAD TO BE COPLANAR WITHIN 0.1mm.
10. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
11. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

SYMBOL	MIN	NOMINAL	MAX
A	0.90	1.25	1.45
A1	0.00	0.05	0.15
A2	0.90	1.10	1.30
b	0.35	0.40	0.50
C	0.08	0.15	0.20
D	2.80	2.90	3.00
E	2.60	2.80	3.00
E1	1.50	1.625	1.75
L	0.35	0.45	0.60
L1		0.60 REF.	
e1		1.90 BSC.	
e		0.95 BSC.	
α	0°	2.5°	10°

PKG CODES:
U6-1, U6-2, U6-4, U6C-8, U6SN-1, U6CN-2, U6S-3, U6F-5, U6F-6, U6FH-5, U6FH-6

DALLAS SEMICONDUCTOR MAXIM

TITLE: PACKAGE OUTLINE, SOT 6L BODY

APPROVAL | DOCUMENT CONTROL NO. 21-0058 | REV. I | 2/2

-DRAWING NOT TO SCALE-

MAX4073T/F/H

Low-Cost, SC70, Voltage-Output, High-Side Current-Sense Amplifier

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	5/01	Initial release	—
2	8/12	Added lead-free notation to Ordering Information.	1

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