ABSOLUTE MAXIMUM RATINGS

IN to GND	0.3V to +42V
OUT to GND	0.3V to +42V
SENS to IN	0.3V to +0.3V
SC, OL, SHDN, OLT, AOUT to GND	0.3V to +6.0V
Current into Any Pin Except OUT and SENS	±20mA
Current into SENS and OUT	±340mA
Continuous Power Dissipation ($T_A = +70^{\circ}C$)	

16-Pin QSOP (derate 18.2mW/°C above +70°C)....1454.5mW

Junction-to-Case Thermal Resistance (θ_{JC}) (Note 1).....25°C/W Junction-to-Ambient Thermal Resistance (θ_{JA}) (Note 1).....55°C/W Operating Temperature Range-40°C to +105°C Junction Temperature Range-40°C to +150°C Storage Temperature Range-65°C to +150°C Lead Temperature (soldering, 10s)....+300°C

MIXIM

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to <u>www.maxim-ic.com/thermal-tutorial</u>.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = +9V \text{ to } +18V, T_A = T_J = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
GENERAL						
Operating Input Voltage Range	VIN		5		18	V
Quiescent Supply Current	Icc	V _{SHDN} < 0.4V		0.6	1.2	mA
Shutdown Supply Current	I _{SD}	V _{SHDN} > 1.7V, T _A = +25°C			5	μA
(VIN - VSENS) to VAOUT Gain	Av	Vaout/(Vin - Vsens)		13		V/V
AOUT Maximum Voltage		(V _{IN} - V _{SENS}) > 300mV		4.3		V
AOUT Zero-Current Output Voltage		$(V_{IN} - V_{SENS}) = 0$	340	400	460	mV
		$I_{LOAD} = I_{SC}$, (VIN - V _{SENS}) = 100mV	1.5	1.7	1.9	
AOUT Voltage		$I_{LOAD} = 2 \times I_{SC}, (V_{IN} - V_{SENS}) = 200 \text{mV}$	2.7	3	3.3	V
AOUT Output Impedance	Z _{AOUT}			5		kΩ
Switch Dropout Voltage	VD	Measured between SENS and OUT while sourcing 100mA			0.6	V
Thermal Shutdown Threshold	TSHDN	Temperature rising	+150	+164		°C
Thermal Shutdown Hysteresis	T _{HYST}			15		°C
Reference Output Voltage	VREF		2.7	3	3.3	V
Reference Output Impedance	Z _{REF}			5		kΩ
THRESHOLDS		·				
Open-Load Current Threshold	I _{OL}	$R_{SENSE} = 1\Omega (V_{OLT} = 0.66V \text{ for the} MAX16913 only)$	10	20	30	mA
Nominal Open-Load Threshold Setting Range	Volr	(MAX16913A only)	10		50	mV
Short-Circuit Voltage Threshold	V _{SC}	$R_{SENSE} = 1\Omega$	87	100	110	mV
Voltage between IN and SENS	V _{LIM}	At current limit, V _{IN} = 14V	173	200	225	mV
Overvoltage Shutdown Threshold	Vovlo	V _{IN} rising	18	21	24	V
Overcurrent Blanking Time	t BLANK		100	200		ms
Retry Time	t RETRY		1500	3000		ms

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Typical Operating Characteristics

ELECTRICAL CHARACTERISTICS (continued)

(V_{IN} = +9V to +18V, $T_A = T_J = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
LOGIC LEVELS						
SC, OL Output-Voltage Low	V _{OL}	Sink current = 1mA			0.4	V
SC, OL Output-Leakage Current	ILEAK	$V_{\overline{SC}} = V_{\overline{OL}} = 5V$		0.01		μA
SHDN Input Low Voltage	VIL				0.4	V
SHDN Input High Voltage	VIH		1.7			V
TIMING						
Startup Response Time	tst	SHDN falling edge to 90% of V_{IN} , no load		100		μs

 $(V_{IN} = 14V, R_{SENSE} = 1\Omega, T_A = +25^{\circ}C, unless otherwise noted.)$



___Typical Operating Characteristics (continued)

 $(V_{IN} = 14V, R_{SENSE} = 1\Omega, T_A = +25^{\circ}C$, unless otherwise noted.)



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_Pin Description

PIN	NAME	FUNCTION
1, 8, 9, 16	GND	Ground
2	IN	Input Voltage. Bypass IN to GND with a low-ESR ceramic capacitor with a minimum value of 0.1μ F.
3, 4	SENS	Input to Current-Sense Amplifier. Connect the sense resistor between SENS and IN.
5, 6	OUT	Switch Output
7	SHDN	Active-High Control Input. Drive SHDN low to turn on the device.
10	N.C.	No Connection. Not internally connected.
11	OLT	Open-Load Threshold Setting Input. A resistive divider between REF, OLT, and GND sets the open-load current threshold (MAX16913A). For the MAX16913, connect OLT to GND.
12	REF	+3V Nominal Reference Output. Use a resistive divider between REF, OLT, and ground to set the open-load current threshold (MAX16913A). The output impedance of this voltage regulator is $5k\Omega$.
13	AOUT	Current-Monitor Voltage Output. AOUT can be used to measure the load current by means of an external ADC. The output impedance is $5k\Omega$.
14	ŌĹ	Open-Drain Open-Load Indicator Output. \overline{OL} goes low when the load current is lower than the open-load current threshold, or when there is a short-to-battery fault. Connect \overline{OL} to a 10k Ω pullup resistor. See Table 1.
15	SC	Open-Drain Short-Circuit Indicator Output. \overline{SC} goes low when the load current is greater than the short-circuit current threshold or when there is a short-to-battery fault. Connect \overline{SC} to a 10k Ω pullup resistor. See Table 1.

MAX16913 Functional Diagram



MAX16913A Functional Diagram



Detailed Description

The MAX16913/MAX16913A high-voltage, high-side, current-sense switches feature internal current limiting to prevent system damage due to fault conditions. The MAX16913/MAX16913A input voltage range extends from 5V to 18V, making them ideal for providing phantom power to remote radio-frequency low-noise amplifiers (LNAs) in automotive applications. High-side current monitoring does not interfere with the ground path of the load being measured, making these devices particularly useful in a wide range of high-voltage battery-powered systems.

The MAX16913/MAX16913A monitor the load current by means of a high-side current-sense amplifier and provide an analog output voltage proportional to that current at AOUT.

The devices combine a high-side current-sense amplifier, an internal switch, a +3V bandgap reference, and two open-drain fault indicator outputs. These features enable the design of remote power circuits with short-circuit, short-to-battery, and thermal protection. For the MAX16913A, the open-load threshold is externally adjustable using a resistive divider.

A fault-blanking feature enables the circuit to ignore momentary faults, such as those caused by initial charging of a capacitive load at turn-on, preventing false alarms to the system.

The MAX16913/MAX16913A provide an active-high control input (SHDN) to put the devices in low-power shutdown mode.

Current-Sense Amplifier

The integrated current-sense amplifier is a differential amplifier that amplifies the voltage between V_{IN} and V_{SENS}. A sense resistor (0.65 Ω min, 4.7 Ω max), R_{SENSE}, is connected across V_{IN} and V_{SENS}. When the load current passes through the sense resistor, a voltage drop develops across it. The current-sense amplifier amplifies this voltage.

The current-sense amplifier features an internally fixed gain of 13V/V (typ). The following equations show the relationship between the current-sense amplifier output voltage (AOUT) and load current:

$$I_{LOAD}(A) = \frac{(V_{IN} - V_{SENS})(V)}{R_{SENSE}(\Omega)}$$
$$V_{AOUT}(V) = [A_V(V/V) \times (V_{IN} - V_{SENS})(V)] + 0.4V$$

AOUT is the output of an internal buffer with a $5k\Omega$ output impedance.

The AOUT voltage is clamped to typically 4.3V in order to avoid damage to external circuitry.

Load Protection

The MAX16913/MAX16913A monitor the load current through an external sense resistor and perform the following actions:

- If the monitored current is lower than the open-load current, the device signals open load (see the *Open Load* section).
- If the monitored current is higher than the short-circuit current (I_{SC}), the device enters the short-circuit mode (see the *Short Circuit* section).

The devices also perform a short-to-battery detection every time the internal switch turns on (see the *Short-to-Battery Detection* section). In addition, thermal shutdown protects the MAX16913/MAX16913A from overheating (see the *Thermal Shutdown* section). Two open-drain outputs (OL and SC) indicate the device status (see Table 1).

Open Load

If the load current drops below the open-load current threshold, the OL output asserts low. An open-load condition does not turn off the internal switch. The MAX16913 provides an internally fixed open-load threshold, while the MAX16913A features an adjustable open-load threshold.

For the MAX16913, when (V_{IN} - V_{SENS}) drops below 20mV (typ), the device signals open load.

For the MAX16913A, using a resistive divider between REF, the open-load threshold adjustment pin (OLT), and GND sets the open-load threshold (see the *Open-Load Threshold Selection* section).

Short Circuit

If the load current reaches the short-circuit current threshold (ISC), the t_{BLANK} timer begins counting. During this period, the load current is limited to a value of twice the short-circuit current threshold ($2 \times I_{SC}$). If the short-circuit condition is present for t_{BLANK}, SC asserts low and the internal switch turns off. The timer resets if the short-circuit condition disappears before the blanking time (t_{BLANK}) has elapsed. If the switch is turned off at the end of t_{BLANK}, a retry timer (t_{RETRY}) starts immediately after the blanking time has elapsed. During that time, the switch stays off. At the end of t_{RETRY}, the switch turns on again. If the fault still exists, the cycle repeats. If the fault has been removed, the switch stays on. During this cycle, SC stays low. During retry when the switch is off, the current through the





Figure 1. Short-Circuit Timing Diagram

switch is zero (see Figure 1). If the load current is greater than I_{SC}, but does not reach the 2 x I_{SC} threshold, the t_{BLANK} timer still begins counting (see Figure 2). The resistor across IN and SENS sets I_{SC} to a typical value of 100mV/R_{SENSE} (Ω). Blanking time and retry time have fixed values of 100ms (min) and 1500ms (min), respectively.

Short-to-Battery Detection

It may happen that OUT is shorted to the battery due to a fault in the system. The MAX16913/MAX16913A detect this failure by comparing the OUT voltage and the IN voltage before the switch turns on. Every time the switch turns on, such as at the end of the retry time, or once the thermal shutdown condition disappears, the short-to-battery detection is performed. At this



point, if the device detects any failure, the switch stays off, and both SC and OL assert low (see Table 1).

Thermal Shutdown

Thermal shutdown circuitry protects the devices from overheating. The switch turns off immediately when the junction temperature exceeds $+150^{\circ}$ C (min) (see Table 1). The switch turns on again after the device temperature drops by approximately 15°C (typ).

Shutdown (SHDN)

The MAX16913/MAX16913A feature an active-high control input (SHDN) to put the devices in a low-power shutdown mode. The devices turn off and consume only 5µA (max) of shutdown current when SHDN is driven high.





Figure 2. Overcurrent Timing Diagram

Undervoltage and Overvoltage Lockout

The MAX16913/MAX16913A include undervoltage circuitry (UVLO) to prevent erroneous switch operation when the input voltage goes below approximately 4.25V during startup and brownout conditions. Input voltages of less than 4.25V inhibit operation of the

SC	ŌL	VAOUT	DEVICE STATUS
0	0	0.4V	OUT Short-to-Battery
0	1	Cycling While Autoretrying	Short-Circuit Condition
1	0	0.4V	Open-Load Condition
1	1	(V _{IN} - V _{SENS}) x 13 + 0.4V	Normal Operation

Table 1. Status Truth Table

device by turning off the internal charge pump and the switch. These devices also feature an overvoltage lockout (OVLO) threshold of +21V (typ). When V_{IN} is greater than V_{OVLO}, the device immediately turns off the switch and the internal charge pump.

Internal Reference

The MAX16913/MAX16913A feature a +3V bandgap reference output, stable over supply voltage and temperature. For the MAX16913A, the reference output is connected to the resistive divider which sets the open-load threshold. The output impedance of the internal reference is $5k\Omega$ (typ).



Applications Information

Choosing the Sense Resistor

Ideally, the maximum load current develops the fullscale sense voltage across the current-sense resistor. The current-sense amplifier output voltage is given by:

 $V_{AOUT}(V) = [(V_{IN} - V_{SENS})(V) \times A_V(V/V)] + 0.4(V)$

where V_{AOUT} is the output voltage of the current-sense amplifier, and A_V is the gain of the current-sense amplifier of 13V/V (typ). Calculate the maximum value for R_{SENSE} so that the differential voltage across IN and SENS does not exceed the minimum full-scale sense voltage (87mV):

$$R_{SENSE}(\Omega) = \frac{V_{DIFF(MIN)}(V)}{I_{LOAD(FULL-SCALE)}(A)}$$

where $V_{\text{DIFF}(\text{MIN})} = V_{\text{IN}} - V_{\text{SENS}} = 87 \text{mV}$ minimum at maximum load current.

Use resistors specified for current-sensing applications with a minimum resistance value of 0.65Ω , and the maximum resistance value of 4.7Ω . Keep inductance low if ISENSE has a large high-frequency component. Wire-wound resistors have the highest inductance, while metal film is somewhat better. Low-inductance metal-film resistors are also available. Instead of being spiral wrapped around a core, as in metal-film or wire-wound resistors, they are a straight band of metal and are available in values under 1Ω . Because of the high current that flows through RSENSE, avoid parasitic trace resistance from causing errors in the sense voltage.

Open-Load Threshold Selection

For the MAX16913A, a resistive divider between REF, OLT, and GND sets the open-load threshold. See Figure 3.

Use the following formula to set the desired open-load threshold:

$R_2(k\Omega)$	$(R_{SENSE}(\Omega) \times I_{OL}(A) \times A_V(V/V)) + 0.4V$
$\overline{(R_1+R_2)(k\Omega)}$	V _{REF} (V)

where I_{OL} is the desired open-load current threshold; A_V is the current-sense amplifier gain (13V/V typ), and V_{REF} is the reference voltage (+3V typ). The sum of R₁ and R₂ should be large enough so that the output impedance of the internal reference (5k Ω) is negligible compared to the sum of R₁ and R₂, and has a minimum effect on the accuracy of the adjusted open-load threshold.



Figure 3. Open-Load Threshold Selection

For example, to set the open-load threshold at 10mA, using a 1Ω sense resistor, use the following method to calculate the value of R₁ and R₂:

$R_2(k\Omega)$	$= \frac{(1(\Omega) \times 0.01(A) \times 13(V/V)) + 0.4V}{0.01(A) \times 13(V/V)} = 0.177$
$\frac{1}{(R_1 + R_2)(k\Omega)} =$	3(V)

Choose $R_1 = 470k\Omega$ and calculate R_2 as $101k\Omega$.

Input Capacitor

Connect a low-leakage ceramic capacitor from IN to GND to limit the input voltage drop during momentary output short-circuit conditions, and to protect the device against transients due to inductance in the IN line. For example, use at least a 0.1μ F ceramic capacitor if the input inductance (including any stray inductance) is estimated to be 20 μ H. Larger capacitor values reduce the voltage undershoot at the input.

Output Capacitor

In an analogous fashion to the input capacitor, an output capacitor protects the device against transients due to any series inductance in the output. Under no conditions should the OUT pin voltage go below -0.3V as specified in the *Absolute Maximum Ratings*. If a capacitor alone is not sufficient to avoid large negative transients on OUT, then a Schottky diode should be used to clamp transients which go below ground. With a 100µH output series inductor, a 220µF output capacitor is needed to eliminate potential problems. With larger inductor values or smaller capacitors, a Schottky clamp diode will be necessary.

Layout and Thermal Dissipation

To optimize the switch response time to output shortcircuit condition, it is very important to keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors as close as possible to the device (no more than 5mm).





IN and OUT must be connected with wide short traces to the power bus. During normal operation, the power dissipation is small and the package temperature change is minimal. If the output is continuously shorted to ground at the maximum supply voltage, the devices are protected because the total power dissipated during the short is scaled down by the duty cycle imposed by the protection:

 $P_{(MAX)} = \frac{V_{IN(MAX)} \times I_{OUT(MAX)} \times t_{BLANK}}{t_{RETRY} + t_{BLANK}}$

Pin Configuration TOP VIEW GND 16 GND 15 SC IN 2 14 OL SENS 3 MAXIM SENS 4 MAX16913 13 AOUT MAX16913A 12 REF OUT 5 11 OLT OUT 6 SHDN 7 10 N.C. GND 8 9 GND QSOP

Chip Information

PROCESS: BICMOS

Package Information

For the latest package outline information, go to **www.maxim-ic.com/packages**.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
16 QSOP	E16-8F <u>21-0112</u>	

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