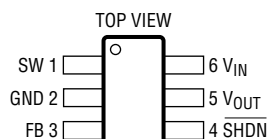


ABSOLUTE MAXIMUM RATINGS

(Note 1)

V_{IN} Voltage	–0.3V to 6V
SW Voltage	–0.3V to 6V
SHDN, FB Voltage	–0.3V to 6V
V_{OUT}	–0.3V to 6V
Operating Temperature Range (Note 2) ..	–40°C to 85°C
Storage Temperature Range	–65°C to 125°C
Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION



S6 PACKAGE
6-LEAD PLASTIC TSOT-23

$T_{JMAX} = 125^{\circ}\text{C}$, $\theta_{JA} = 165^{\circ}\text{C/W}$, $\theta_{JC} = 102^{\circ}\text{C/W}$

ORDER PART NUMBER

S6 PART NUMBER

LTC3426ES6

LTAJT

Order Options Tape and Reel: Add #TR

Lead Free: Add #PBF Lead Free Tape and Reel: Add #TRPBF

Lead Free Part Marking: <http://www.linear.com/leadfree/>

Consult LTC Marketing for parts specified with wider operating temperature ranges.

CONVERTER CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}\text{C}$. $V_{IN} = 1.8\text{V}$, $V_{OUT} = 3.3\text{V}$, unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	$\overline{\text{SHDN}} = V_{IN}$	1.6			V
Output Voltage Adjust Range		2.25		5	V
Feedback Voltage		● 1.173	1.22	1.247	V
Feedback Input Current	$V_{FB} = 1.23\text{V}$			0.1	μA
Quiescent Current (Shutdown)	$\overline{\text{VSHDN}} = 0\text{V}$, Not Including Switch Leakage			1	μA
Quiescent Current	$\overline{\text{SHDN}} = V_{IN}$, Not Switching		600	1000	μA
Switch Leakage	$V_{SW} = 5\text{V}$		0.2	10	μA
Switch On Resistance	$V_{OUT} = 3.3\text{V}$ $V_{OUT} = 5\text{V}$		0.11 0.10		Ω Ω
Current Limit		● 2	2.3		A
Maximum Duty Cycle	$V_{FB} = 1.15\text{V}$	80	85		%
Switching Frequency		● 0.85	1.2	1.5	MHz
SHDN Input High		1			V
SHDN Input Low				0.4	V
SHDN Input Current	$\overline{\text{SHDN}} = 5.5\text{V}$			1	μA

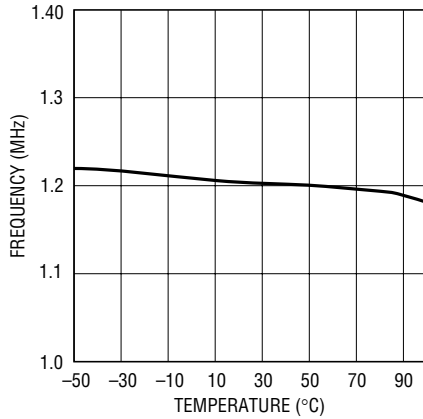
Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The LTC3426 is guaranteed to meet performance specifications from 0°C to 85°C. Specifications over the –40°C to 85°C operating temperature are assured by design, characterization and correlation with statistical process controls.

Note 3: This IC includes overtemperature protection that is intended to protect the device during momentary overload conditions. Junction temperature will exceed 125°C when overtemperature protection is active. Continuous operation above the specified maximum operating junction temperature may impair device reliability.

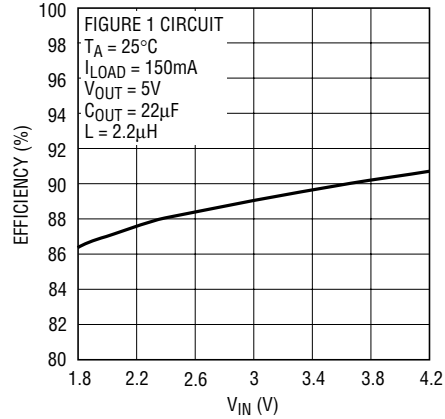
TYPICAL PERFORMANCE CHARACTERISTICS

Oscillator Frequency vs Temperature



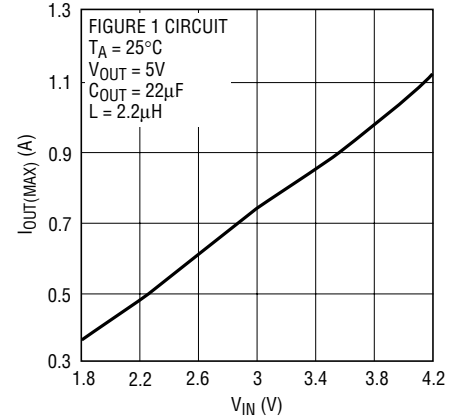
3426 G01

Efficiency vs V_{IN}



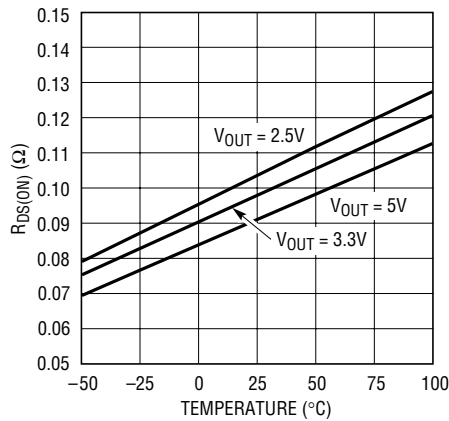
LT1108 • TPC12

$I_{OUT(MAX)}$ vs V_{IN}



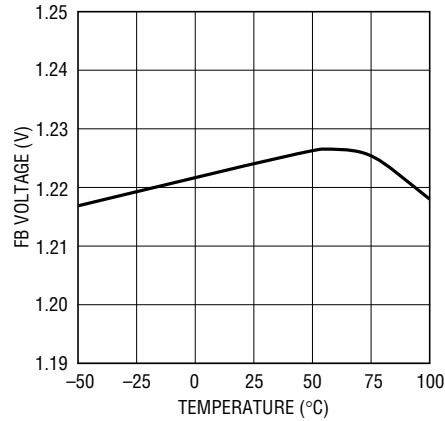
3426 G03

$R_{DS(ON)}$ vs Temperature



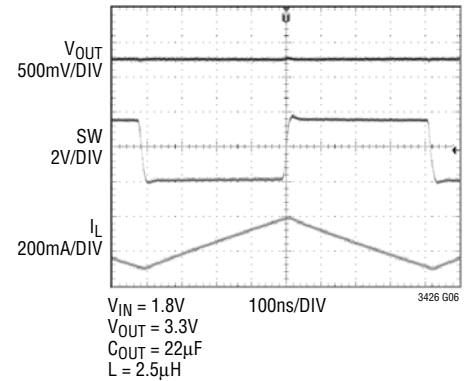
3426 G04

FB Pin Voltage



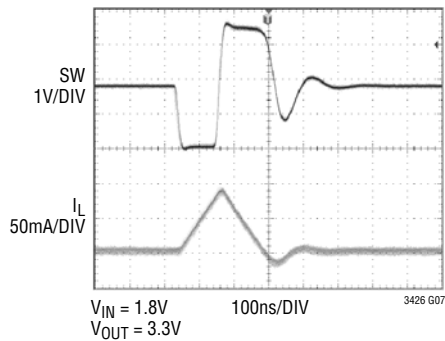
3426 G05

Switching Waveforms



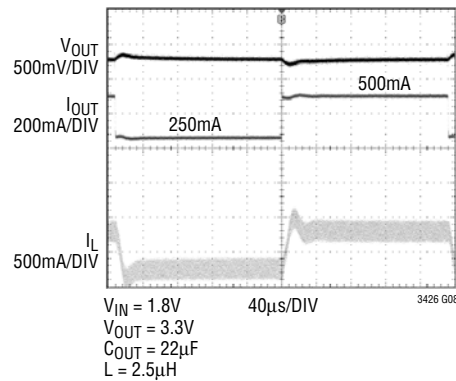
3426 G06

SW Pin Antiringing Operation



3426 G07

V_{OUT} Transient Response



3426 G08

PIN FUNCTIONS

SW (Pin 1): Switch Pin. Connect inductor between SW and V_{IN} . A Schottky diode is connected between SW and V_{OUT} . Keep these PCB trace lengths as short and wide as possible to reduce EMI and voltage overshoot. If the inductor current falls to zero, an internal 100Ω antiringing switch is connected from SW to V_{IN} to minimize EMI.

GND (Pin 2): Signal and Power Ground. Provide a short direct PCB path between GND and the (–) side of the output capacitor(s).

FB (Pin 3): Feedback Input to the g_m Error Amplifier. Connect resistor divider tap to this pin. The output voltage can be adjusted from 2.5V to 5V by:

$$V_{OUT} = 1.22 \cdot \left(1 + \frac{R1}{R2} \right)$$

$\overline{\text{SHDN}}$ (Pin 4): Logic Controlled Shutdown Input.

$\overline{\text{SHDN}}$ = High: Normal free running operation

$\overline{\text{SHDN}}$ = Low: Shutdown, quiescent current $< 1\mu\text{A}$

Typically, $\overline{\text{SHDN}}$ should be connected to V_{IN} through a 1M pull-up resistor.

V_{OUT} (Pin 5): Output Voltage Sense Input. The NMOS switch gate drive is derived from the greater of V_{OUT} and V_{IN} .

V_{IN} (Pin 6): Input Supply. Must be locally bypassed.

BLOCK DIAGRAM

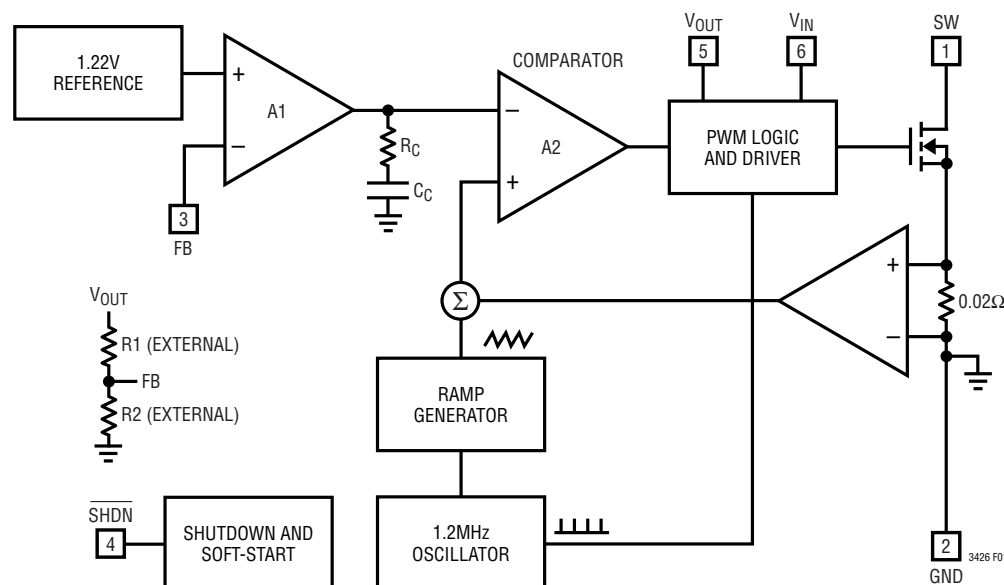


Figure 1

OPERATION

The LTC3426 is a monolithic 1.2MHz boost converter housed in a 6-lead SOT-23 package. The device features fixed frequency, current mode PWM control for excellent line and load regulation. The low $R_{DS(ON)}$ NMOS switch enables the device to maintain high efficiency over a wide range of load current. Operation of the feedback loop which sets the peak inductor current to keep the output in regulation can be best understood by referring to the Block Diagram in Figure 1. At the start of each clock cycle a latch in the PWM logic is set and the NMOS switch is turned on. The sum of a voltage proportional to the switch current and a slope compensating voltage ramp is fed to the positive input to the PWM comparator. When this voltage exceeds either a voltage proportional to the 2A current limit or the PWM control voltage, the latch in the PWM logic is reset and NMOS switch is turned off. The PWM

control voltage at the output of the error amplifier is the amplified and compensated difference between the feedback voltage on the FB pin and the internal reference voltage of 1.22V. If the control voltage increases, more current is delivered to the output. When the control voltage exceeds the I_{LIMIT} reference voltage, the peak current is limited to a minimum of 2A. The current limit helps protect the LTC3426 internal switch and external components connected to it. If the control voltage decreases, less current is delivered to the output. During load transients control voltage may decrease to the point where no switching occurs until the feedback voltage drops below the reference. The LTC3426 has an integrated soft-start feature which slowly ramps up the feedback control node from 0V. The soft-start is initiated when $\overline{SHDN}$ is pulled high.

APPLICATIONS INFORMATION

Setting the Output Voltage

The output voltage, V_{OUT} , is set by a resistive divider from V_{OUT} to ground. The divider tap is tied to the FB pin. V_{OUT} is set by the formula:

$$V_{OUT} = 1.22 \cdot \left(1 + \frac{R1}{R2} \right)$$

Inductor Selection

The LTC3426 can utilize small surface mount inductors due to its 1.2MHz switching frequency. A 1.5 μ H or 2.2 μ H inductor will be the best choice for most LTC3426 applications. Larger values of inductance will allow greater output current capability by reducing the inductor ripple current. Increasing the inductance above 3.3 μ H will increase component size while providing little improvement in output current capability. The inductor current ripple is typically set for 20% to 40% of the maximum inductor current (I_P). High frequency ferrite core inductor materials reduce frequency dependent power losses compared to cheaper powdered iron types, improving efficiency. The inductor should have low DCR (DC resistance)

to reduce the I^2R power losses, and must be able to handle the peak inductor current without saturating.

Several inductor manufacturers are listed in Table 1.

Table 1. Inductor Manufacturers

TDK	www.tdk.com
Sumida	www.sumida.com
Murata	www.murata.com

Output and Input Capacitor Selection

Low ESR (equivalent series resistance) capacitors should be used to minimize the output voltage ripple. Multilayer ceramic capacitors are an excellent choice as they have extremely low ESR and are available in small footprints. A 15 μ F to 30 μ F output capacitor is sufficient for most applications. X5R and X7R dielectric materials are preferred for their ability to maintain capacitance over wide voltage and temperature ranges.

Low ESR input capacitors reduce input switching noise and reduce the peak current drawn from the input supply. It follows that ceramic capacitors are also a good choice for input decoupling and should be located as close as

APPLICATIONS INFORMATION

possible to the device. A 10μF input capacitor is sufficient for most applications. Table 2 shows a list of several ceramic capacitor manufacturers. Consult the manufacturers for detailed information in their entire selection of ceramic parts.

Table 2. Ceramic Capacitor Manufacturers

Taiyo Yuden	www.t-yuden.com
Murata	www.murata.com
TDK	www.component.tdk.com

Diode Selection

A Schottky diode is recommended for use with the LTC3426. Use of a low forward voltage diode such as the ON Semiconductor MBRA210LT3 is recommended. A Schottky diode rated at 2A is recommended for use with the LTC3426.

PCB Layout Guidelines

The high speed operation of the LTC3426 demands careful attention to board layout. You will not get advertised performance with careless layout. Figure 2 shows the recommended component placement. A large ground pin copper area will help to lower the chip temperature.

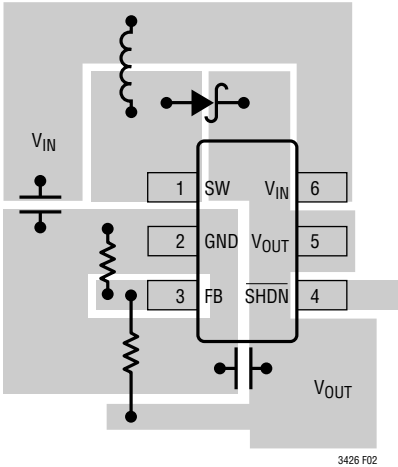
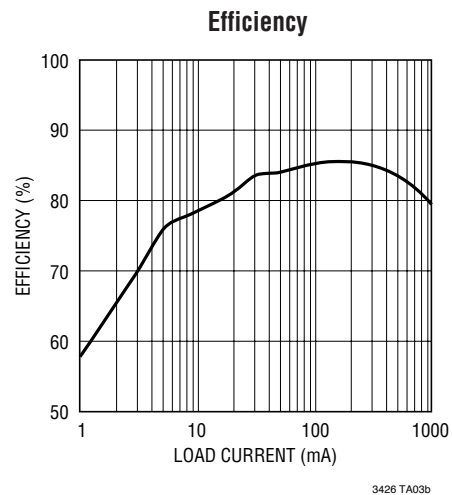
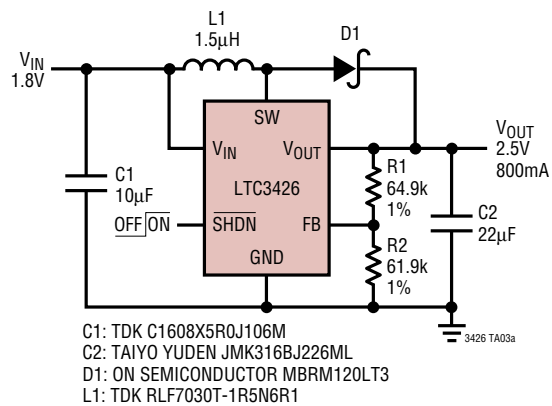
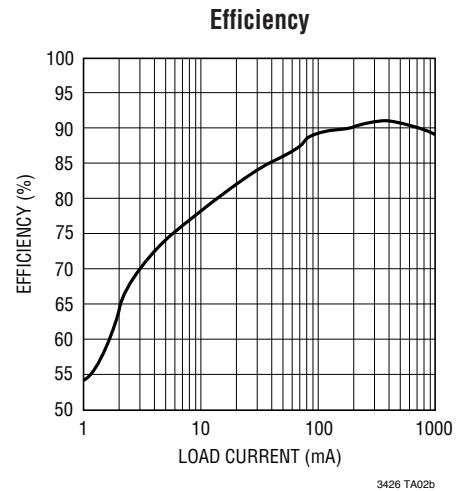
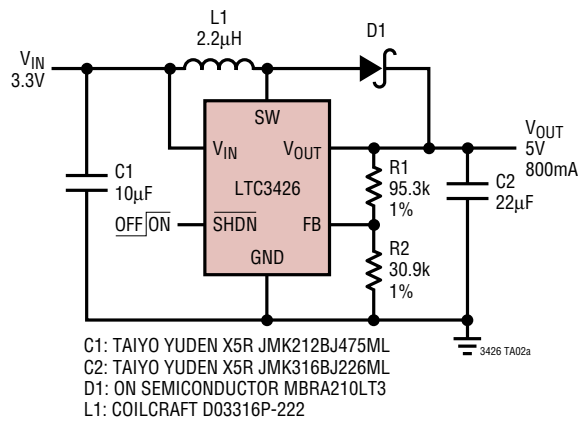
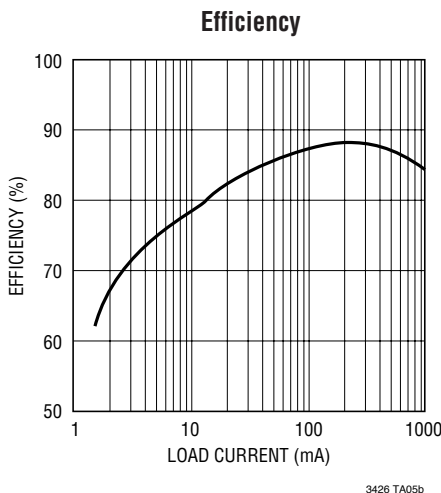
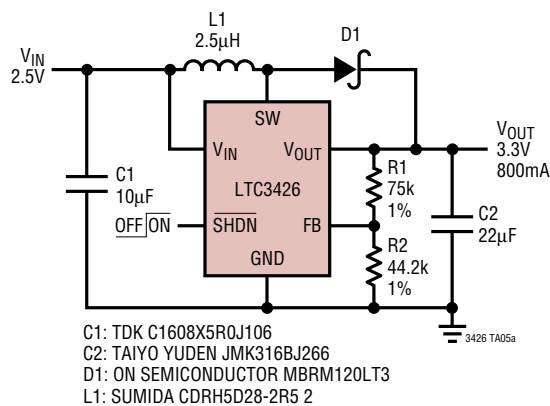
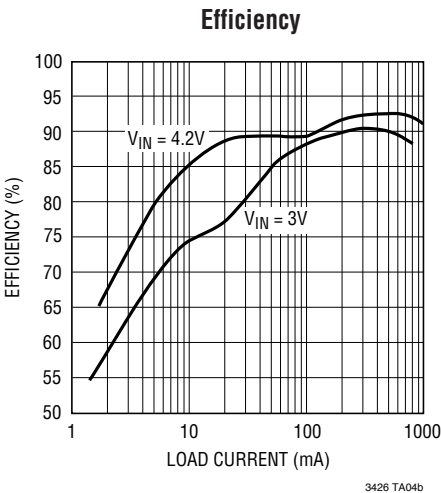
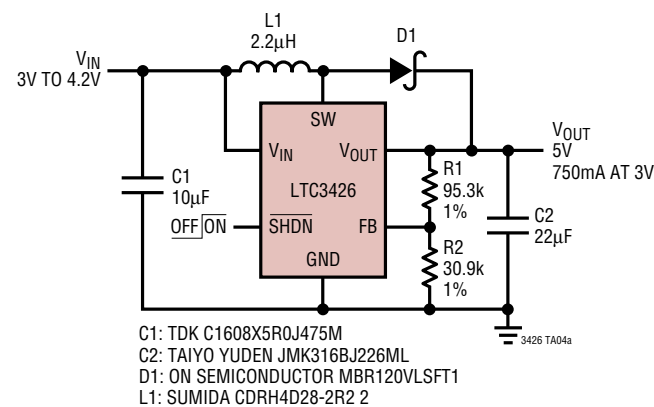


Figure 2. Recommended Component Placement for Single Layer Board

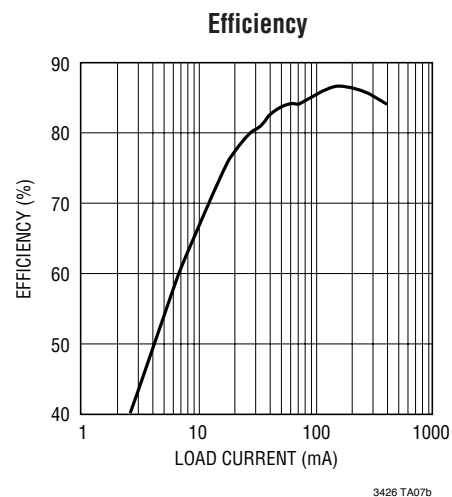
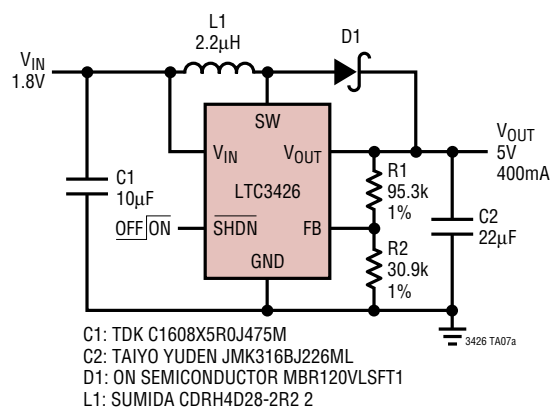
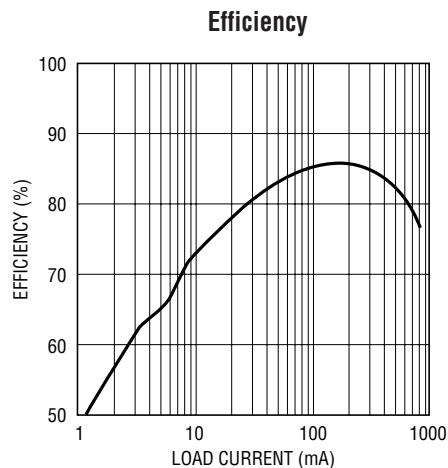
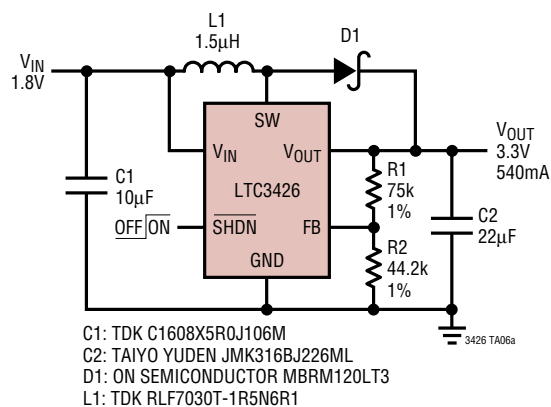
TYPICAL APPLICATIONS



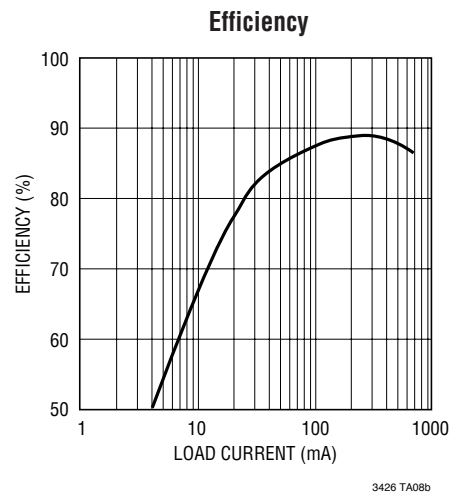
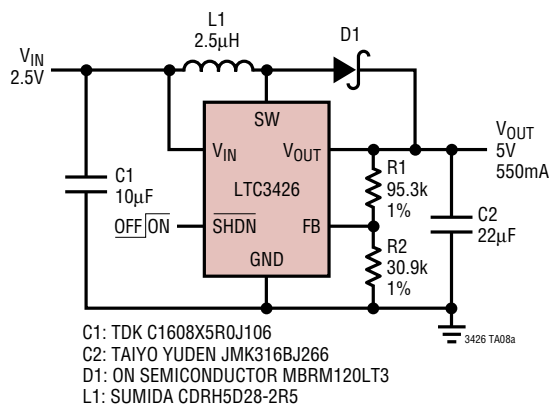
TYPICAL APPLICATIONS



TYPICAL APPLICATIONS

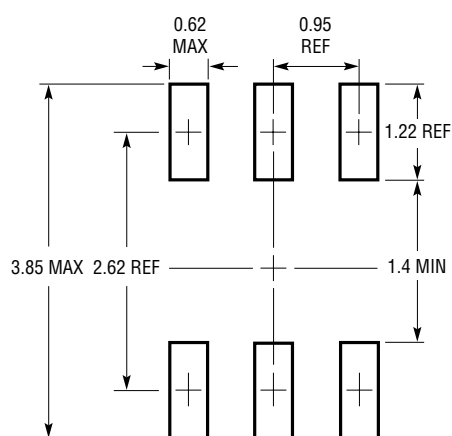


TYPICAL APPLICATIONS

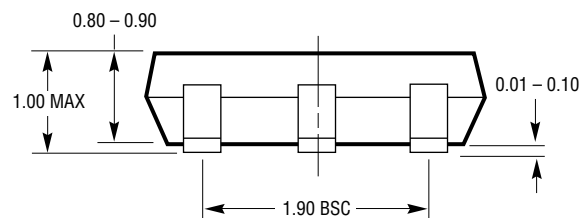
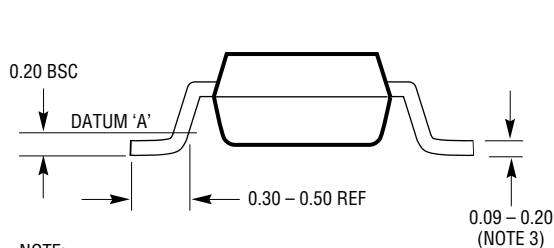
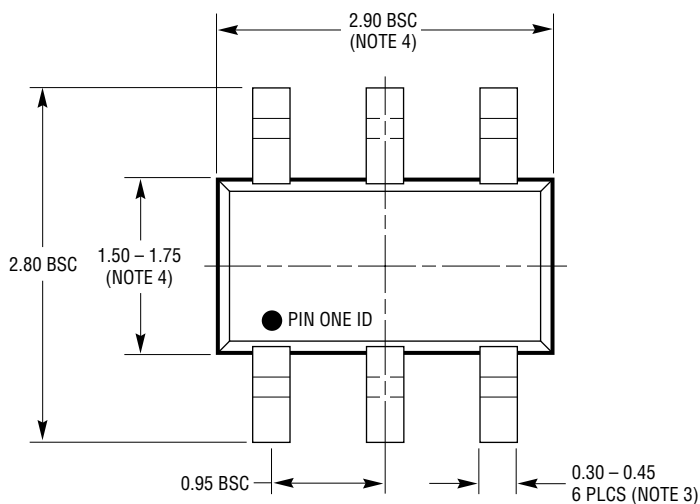


PACKAGE DESCRIPTION

S6 Package
6-Lead Plastic TSOT-23
 (Reference LTC DWG # 05-08-1636 Rev B)



RECOMMENDED SOLDER PAD LAYOUT
 PER IPC CALCULATOR



S6 TSOT-23 0302 REV B

NOTE:

1. DIMENSIONS ARE IN MILLIMETERS
2. DRAWING NOT TO SCALE
3. DIMENSIONS ARE INCLUSIVE OF PLATING
4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
5. MOLD FLASH SHALL NOT EXCEED 0.254mm
6. JEDEC PACKAGE REFERENCE IS MO-193

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1613	550mA (I_{SW}), 1.4MHz, High Efficiency Step-Up DC/DC Converter	90% Efficiency, V_{IN} : 0.9V to 10V, $V_{OUT(MAX)}$ = 34V, I_Q = 3mA, I_{SD} < 1 μ A, ThinSOT
LT1946/LT1946A	1.5A (I_{SW}), 1.2MHz/2.7MHz, High Efficiency Step-Up DC/DC Converter	High Efficiency, V_{IN} : 2.45V to 16V, $V_{OUT(MAX)}$ = 34V, I_Q = 3.2mA, I_{SD} < 1 μ A, MS8
LTC3400/LTC3400B	600mA (I_{SW}), 1.2MHz, Synchronous Step-Up DC/DC Converter	92% Efficiency, V_{IN} : 0.5V to 5V, $V_{OUT(MAX)}$ = 5V, I_Q = 19 μ A/300 μ A, I_{SD} < 1 μ A, ThinSOT
LTC3401/LTC3402	1A/2A (I_{SW}), 3MHz, Synchronous Step-Up DC/DC Converter	97% Efficiency, V_{IN} : 0.5V to 5V, $V_{OUT(MAX)}$ = 5.5V, I_Q = 38 μ A, I_{SD} < 1 μ A, MS10
LTC3421	3A (I_{SW}), 3MHz, Synchronous Step-Up DC/DC Converter with Output Disconnect	95% Efficiency, V_{IN} : 0.5V to 4.5V, $V_{OUT(MAX)}$ = 5.25V, I_Q = 12 μ A, I_{SD} < 1 μ A, QFN24
LTC3425	5A (I_{SW}), 8MHz, 4-Phase Synchronous Step-Up DC/DC Converter with Output Disconnect	95% Efficiency, V_{IN} : 0.5V to 4.5V, $V_{OUT(MAX)}$ = 5.25V, I_Q = 12 μ A, I_{SD} < 1 μ A, QFN32
LTC3429	600mA (I_{SW}), 550kHz, Synchronous Step-Up DC/DC Converter with Soft-Start/Output Disconnect	90% Efficiency, V_{IN} : 0.5V to 4.3V, $V_{OUT(MAX)}$ = 5V, I_Q = 20 μ A, I_{SD} < 1 μ A, ThinSOT
LTC3436	3A (I_{SW}), 1MHz, High Efficiency Step-Up DC/DC Converter	V_{IN} : 3V to 25V, $V_{OUT(MAX)}$ = 34V, I_Q = 0.9mA, I_{SD} < 6 μ A, TSSOP16E
LTC3459	75mA (I_{SW}), 10V Micropower Synchronous Boost Converter in ThinSOT	V_{IN} : 1.5V to 5.5V, $V_{OUT(MAX)}$ = 10V, I_Q = 10 μ A, I_{SD} < 1 μ A, ThinSOT
LTC3464	85mA (I_{SW}), High Efficiency Step-Up DC/DC Converter with Schottky and PNP Disconnect	V_{IN} : 2.3V to 10V, $V_{OUT(MAX)}$ = 34V, I_Q = 25 μ A, I_{SD} < 1 μ A, ThinSOT