

PROTECTION PRODUCTS
Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 10/1000μs)	P_{pk}	1500	Watts
Peak Pulse Current (tp = 10/1000μs)	I_{pp}	100	A
Peak Pulse Current (tp = 10/160μs)	I_{pp}	200	A
Thermal Resistance Junction to Case	θ_{JC}	30	°C/W
Thermal Resistance Junction to Ambient	θ_{JA}	95	°C/W
Lead Soldering Temperature	T_L	260 (10 sec.)	°C
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

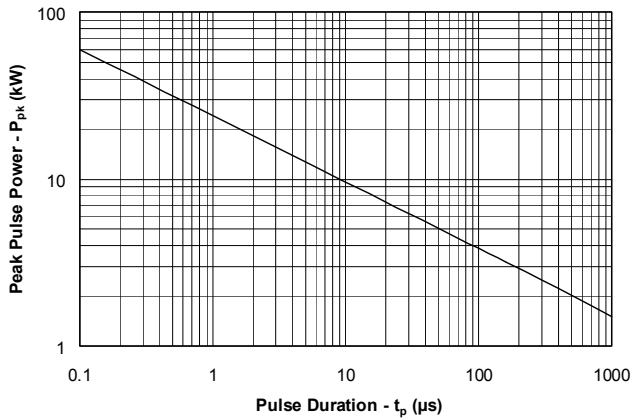
Electrical Characteristics

LC01-6						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				6	V
Reverse Breakdown Voltage	V_{BR}	$I_t = 1mA$	8			V
Reverse Leakage Current	I_R	$V_{RWM} = 6V, T=25^\circ C$			25	μA
Clamping Voltage	V_C	$I_{pp} = 100A$ $tp = 10/1000\mu s$			15	V
Clamping Voltage	V_C	$I_{pp} = 200A$ $tp = 10/160\mu s$			16	V
Temperature Coefficient of Breakdown Voltage	αT_{VBR}			3		mV/°C
Junction Capacitance	C_j	$V_R = 0V, f = 1MHz$			50	pF

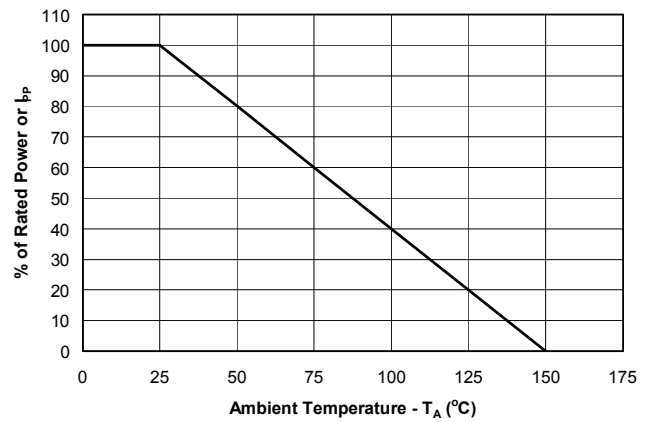
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Typical Characteristics

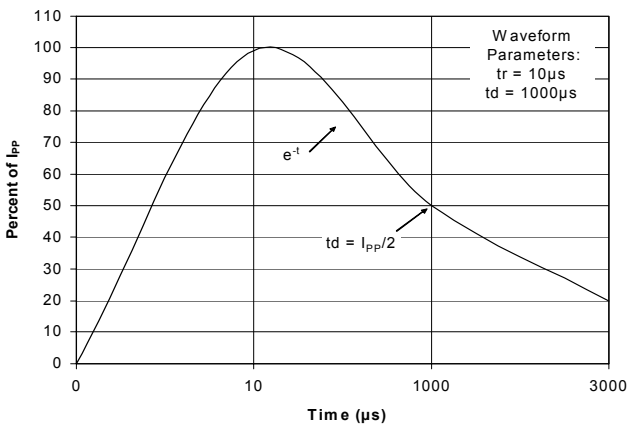
Non-Repetitive Peak Pulse Power vs. Pulse Time



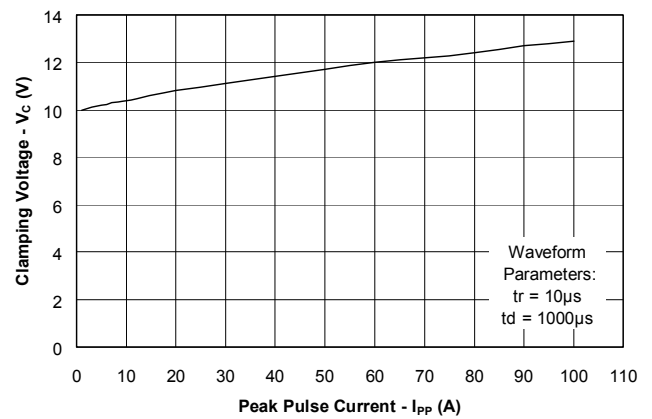
Power Derating Curve



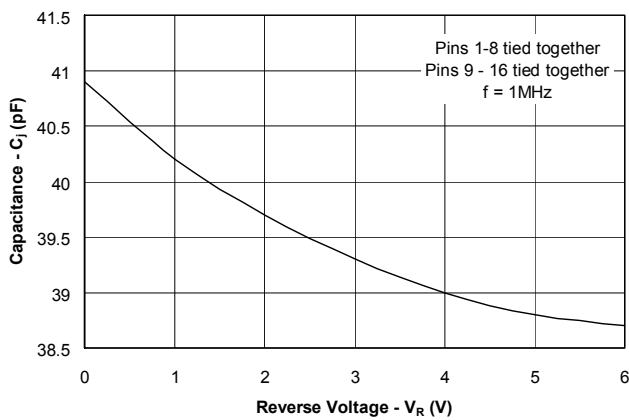
Pulse Waveform



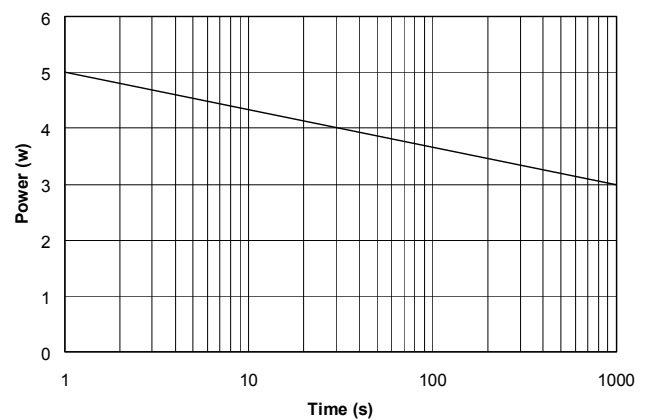
Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage



AC Power Dissipation



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Applications Information

Device Connection Options for Protection of High-Speed Data Lines

The LC01-6 is designed to protect high-speed data lines from transient over-voltages which result from lightning and ESD. The device is designed to protect one line pair in differential (Line-to-Line) mode. Pins 1-8 are connected to line 1 and pins 9-16 are connected to line 2 as shown. All pins should be connected for best results.

T1/E1 Linecard Protection

A typical long-haul T1/E1 linecard protection circuit is shown below. The LC01-6 is connected between Tip and Ring on the transmit and receive line pairs. It provides protection to metallic (line-to-line) lightning surges per Bellcore 1089 and FCC Part 68. Depending on the application, a high voltage crowbar device (not shown) such as a gas discharge tube (GDT) or TVS Thyristor may be used to suppress common mode (line-to-ground) surges. To complete the protection circuit, the SRDA05-4 (or SRDA3.3-4 for 3.3V supplies) is employed as the secondary IC side protection element. This device helps prevent the transceiver from latching up by providing fine clamping of transients that are coupled through the transformer. Positive temperature coefficient (PTC) resistors or fuses are also required for AC power cross protection. For further information, reference Semtech application note AN97-10.

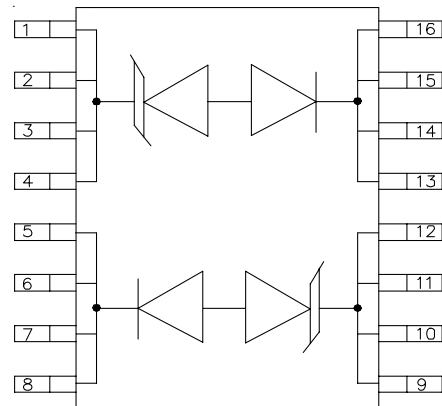


Figure 1 - Schematic and Pin Configuration

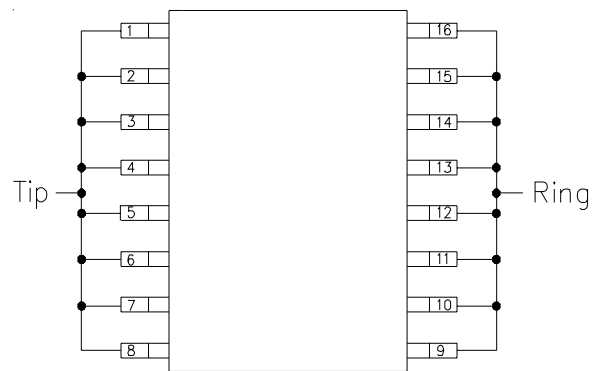
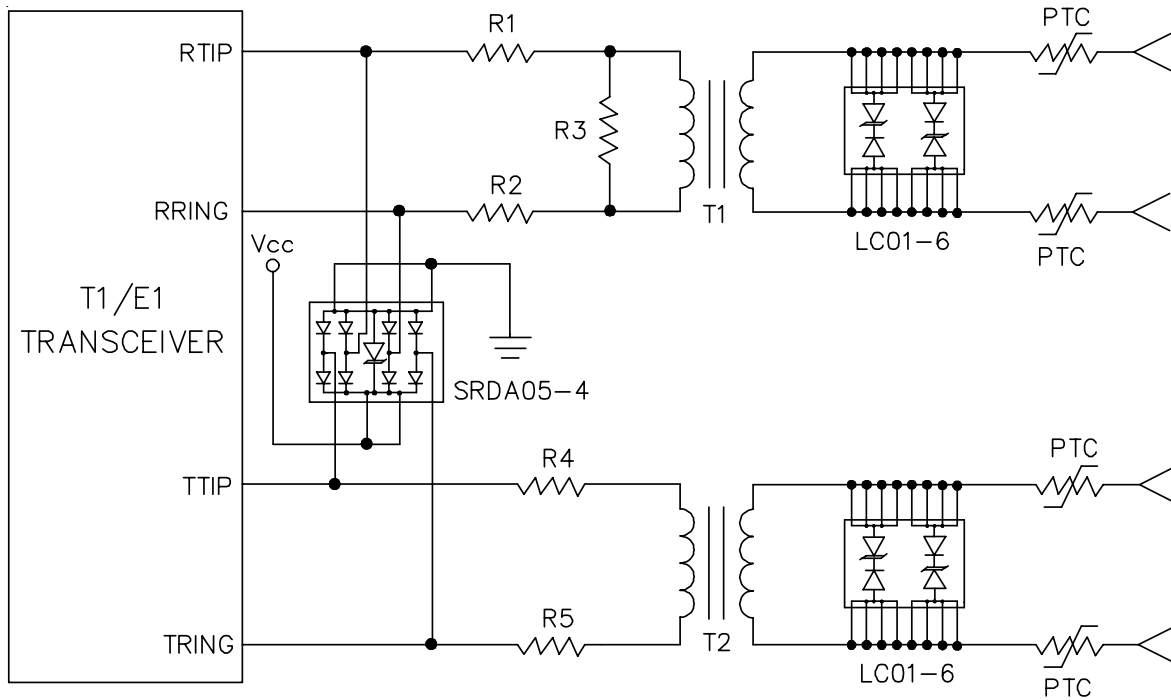


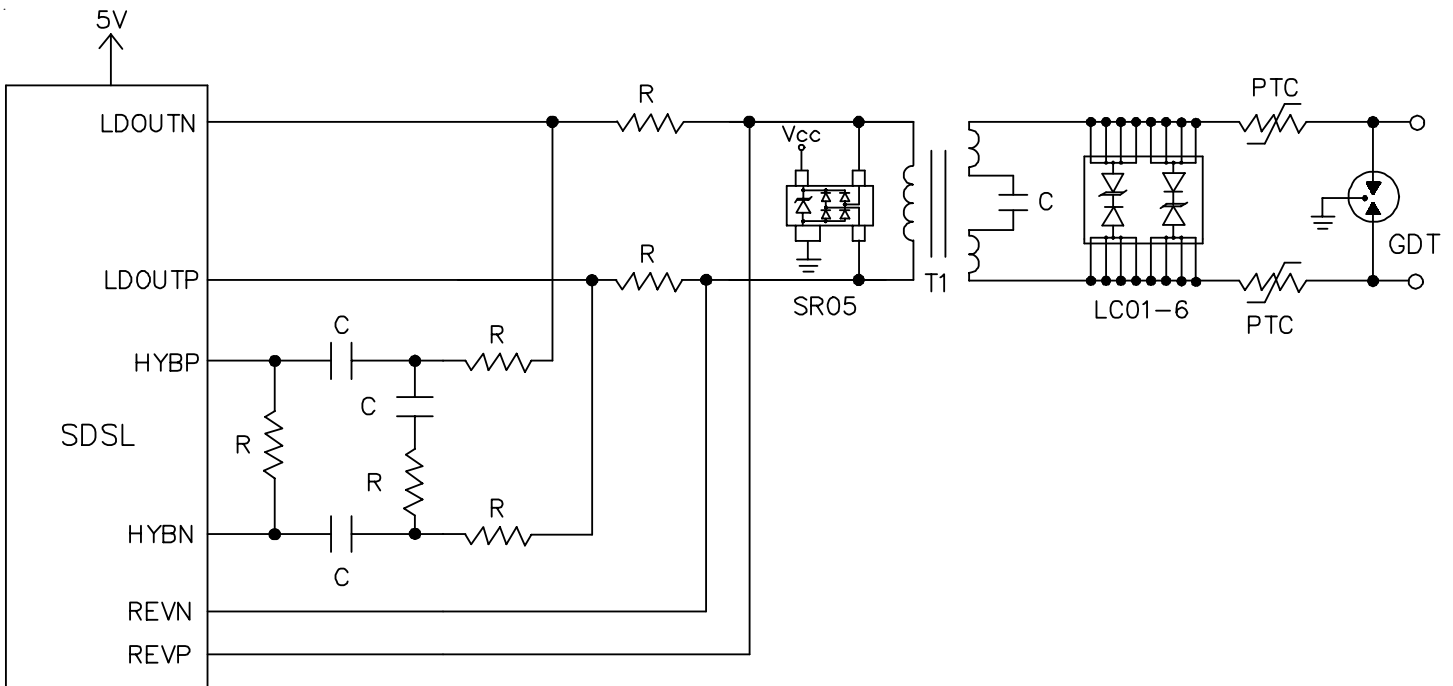
Figure 2 - Connection for Differential Protection (Line-to-Line)

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Typical Applications



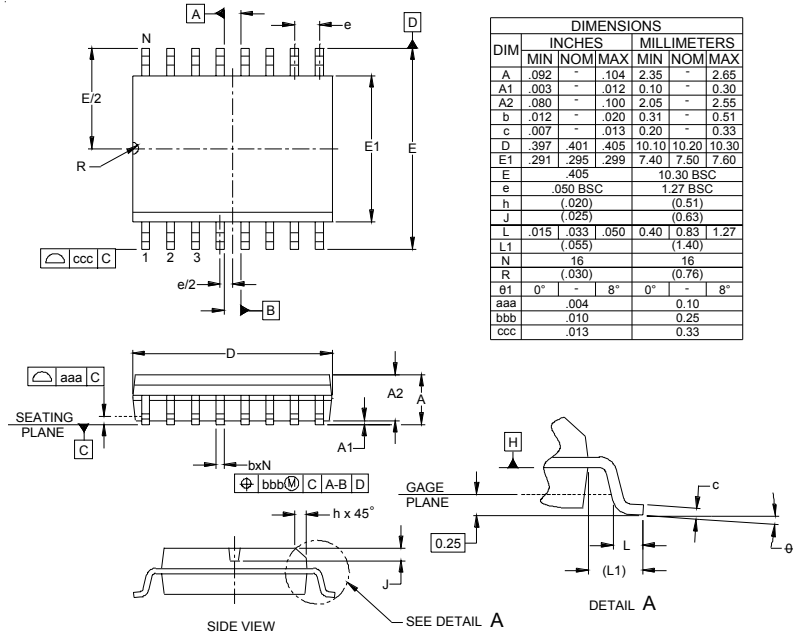
T1/E1 Line Card Protection (Long-Haul Applications)



SDSL Protection

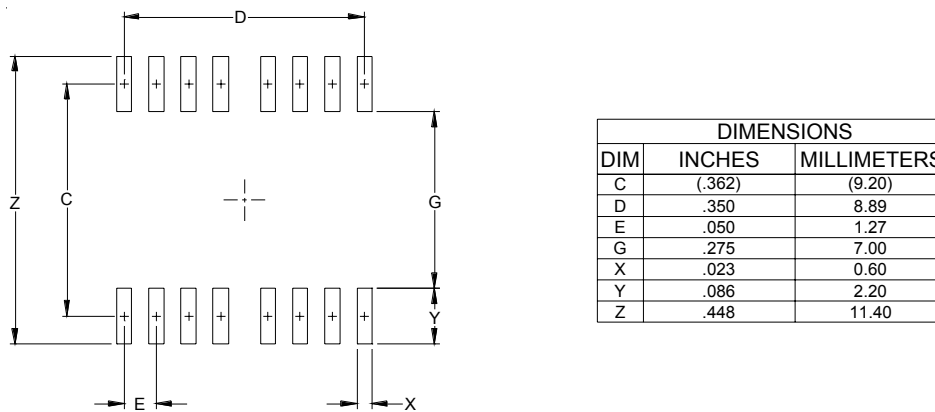
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Outline Drawing - S0-16W



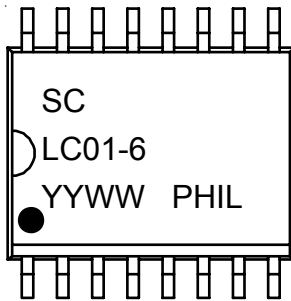
- NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
 2. DATUMS [A] AND [B] TO BE DETERMINED AT DATUM PLANE.
 3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
 4. REFERENCE JEDEC STD MS-013, VARIATION AA.

Land Pattern - S0-16W



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Marking



Note:
 (1) yyww = Date Code

Ordering Information

Part Number	Working Voltage	Qty/Pkg	Reel Size
LC01-6.TD	6V	1000/Reel	13 Inch
LC01-6.TDT ⁽¹⁾	6V	1000/Reel	13 Inch
LC01-6	6V	46/Tube	N/A
LC01-6.T ⁽¹⁾	6V	46/Tube	N/A

Note:
 (1) Lead-Free Product

Contact Information

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