

### Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction – case	$R_{thJC}$		0.80	K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		1.05	
Thermal resistance, junction – ambient	$R_{thJA}$		40	

### Electrical Characteristic, at $T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=0.2mA$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15V, I_C=30A$	-	1.5	2.05	
		$T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	1.9 -	-	
Diode forward voltage	$V_F$	$V_{GE}=0V, I_F=30A$	-	1.65	2.05	
		$T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	1.6 -	-	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=0.43mA,$ $V_{CE}=V_{GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600V,$ $V_{GE}=0V$	-	-	40	$\mu A$
		$T_j=25^{\circ}C$ $T_j=175^{\circ}C$	- -	- -	2000	
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0V, V_{GE}=20V$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE}=20V, I_C=30A$	-	16.7	-	S
Integrated gate resistor	$R_{Gint}$		-			$\Omega$

### Dynamic Characteristic

Input capacitance	$C_{iss}$	$V_{CE}=25V, V_{GE}=0V, f=1MHz$	-	1630	-	pF
Output capacitance	$C_{oss}$		-	108	-	
Reverse transfer capacitance	$C_{rss}$		-	50	-	
Gate charge	$Q_{Gate}$	$V_{CC}=480V, I_C=30A, V_{GE}=15V$	-	167	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13	-	nH
Short circuit collector current <sup>1)</sup>	$I_{C(SC)}$	$V_{GE}=15V, t_{SC}\leq 5\mu s, V_{CC}=400V, T_j=150^\circ\text{C}$	-	275	-	A

<sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

### Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_J=25^{\circ}\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=30\text{A}$ , $V_{GE}=0/15\text{V}$ , $r_G=10.6\Omega$ , $L_{\sigma}=136\text{nH}$ , $C_{\sigma}=39\text{pF}$ $L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	23	-	ns
Rise time	$t_r$		-	21	-	
Turn-off delay time	$t_{d(off)}$		-	254	-	
Fall time	$t_f$		-	46	-	
Turn-on energy	$E_{on}$		-	0.69	-	mJ
Turn-off energy	$E_{off}$		-	0.77	-	
Total switching energy	$E_{ts}$		-	1.46	-	

### Anti-Parallel Diode Characteristic

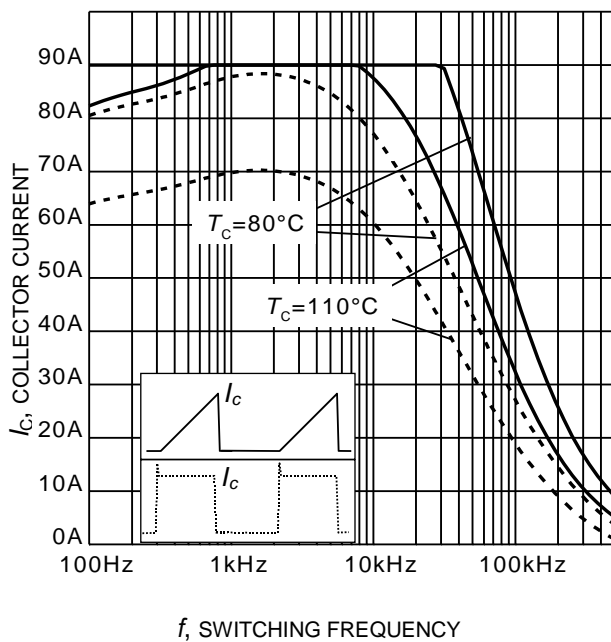
Diode reverse recovery time	$t_{rr}$	$T_j=25^\circ\text{C}$ , $V_R=400\text{V}$ , $I_F=30\text{A}$ , $di_F/dt=910\text{A}/\mu\text{s}$	-	143	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.92	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	16.3	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	603	-	$\text{A}/\mu\text{s}$

### Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

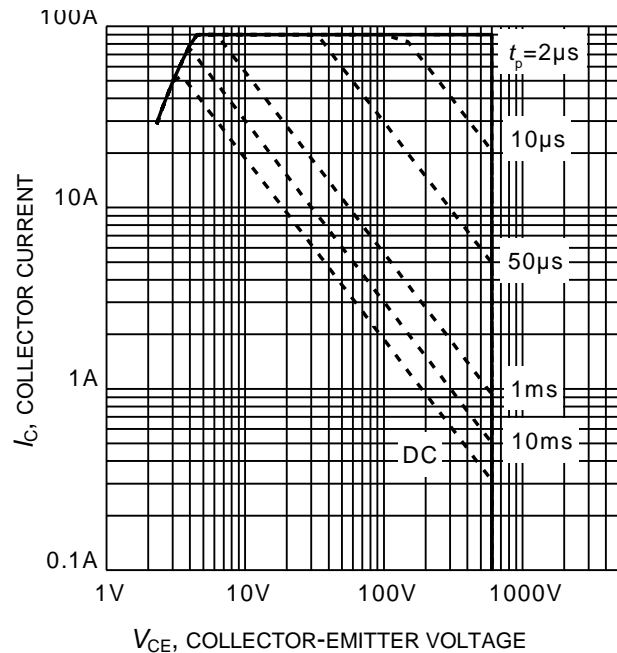
Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=175^{\circ}\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=30\text{A}$ , $V_{GE}=0/15\text{V}$ , $r_G=10.6\Omega$ , $L_{\sigma}=136\text{nH}$ , $C_{\sigma}=39\text{pF}$ $L_{\sigma}$ , $C_{\sigma}$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	24	-	ns
Rise time	$t_r$		-	26	-	
Turn-off delay time	$t_{d(off)}$		-	292	-	
Fall time	$t_f$		-	90	-	
Turn-on energy	$E_{on}$		-	1.0	-	mJ
Turn-off energy	$E_{off}$		-	1.1	-	
Total switching energy	$E_{ts}$		-	2.1	-	

### Anti-Parallel Diode Characteristic

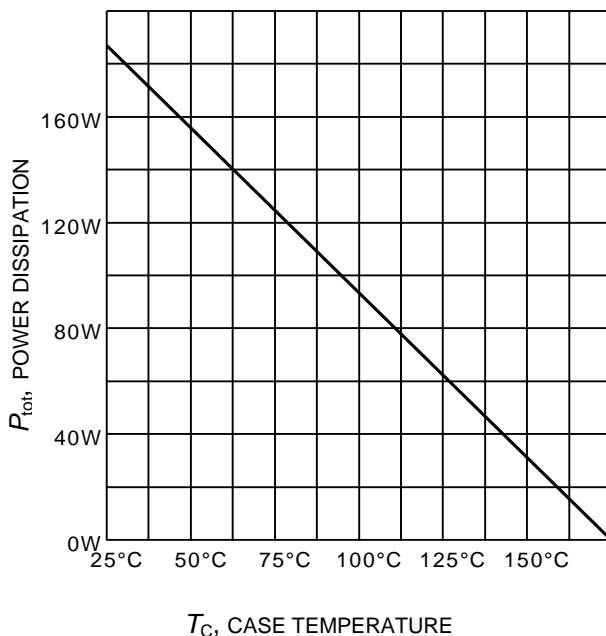
Diode reverse recovery time	$t_{rr}$	$T_j=175^\circ\text{C}$ $V_R=400\text{V}$ , $I_F=30\text{A}$ , $di_F/dt=910\text{A}/\mu\text{s}$	-	225	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	2.39	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	22.3	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	310	-	$\text{A}/\mu\text{s}$



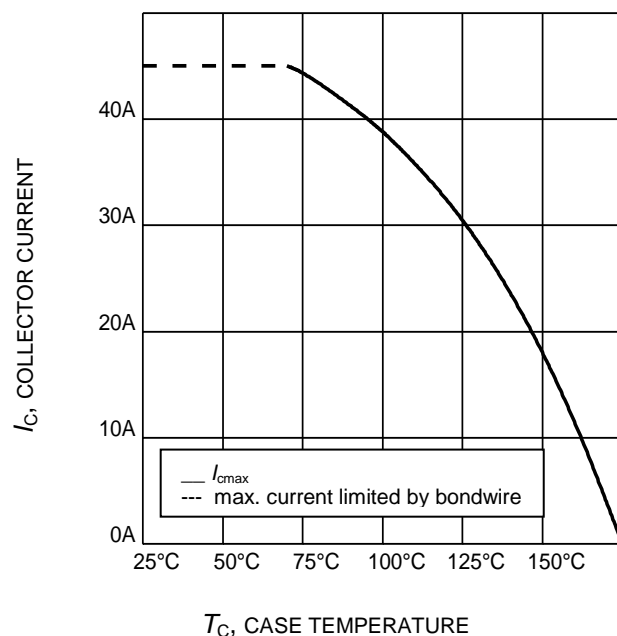
**Figure 1. Collector current as a function of switching frequency**  
 $(T_j \leq 175^\circ\text{C}, D = 0.5, V_{CE} = 400\text{V}, V_{GE} = 0/15\text{V}, r_G = 10\Omega)$



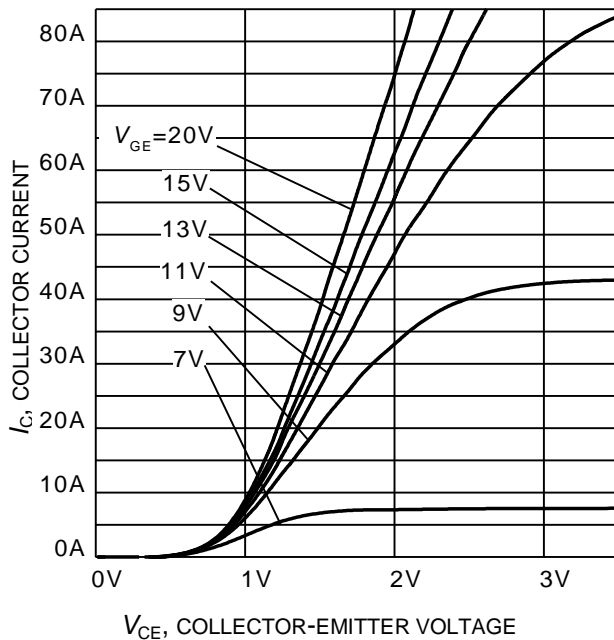
**Figure 2. Safe operating area**  
 $(D = 0, T_C = 25^\circ\text{C}, T_j \leq 175^\circ\text{C}; V_{GE} = 0/15\text{V})$



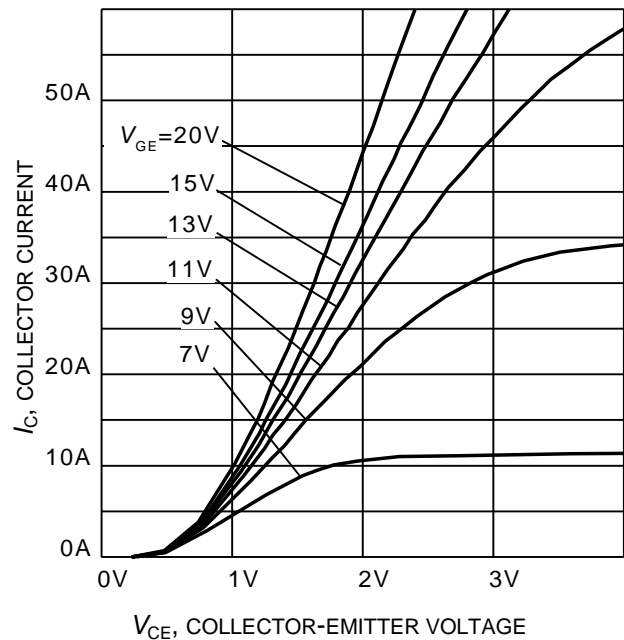
**Figure 3. Power dissipation as a function of case temperature**  
 $(T_j \leq 175^\circ\text{C})$



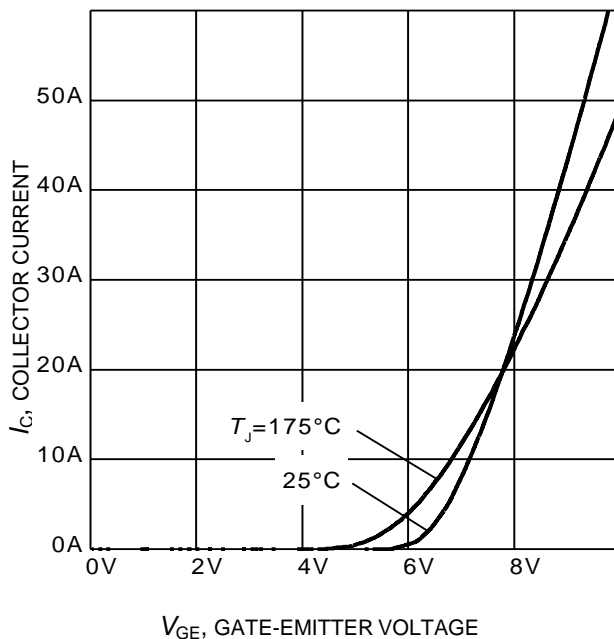
**Figure 4. Collector current as a function of case temperature**  
 $(V_{GE} \geq 15\text{V}, T_j \leq 175^\circ\text{C})$



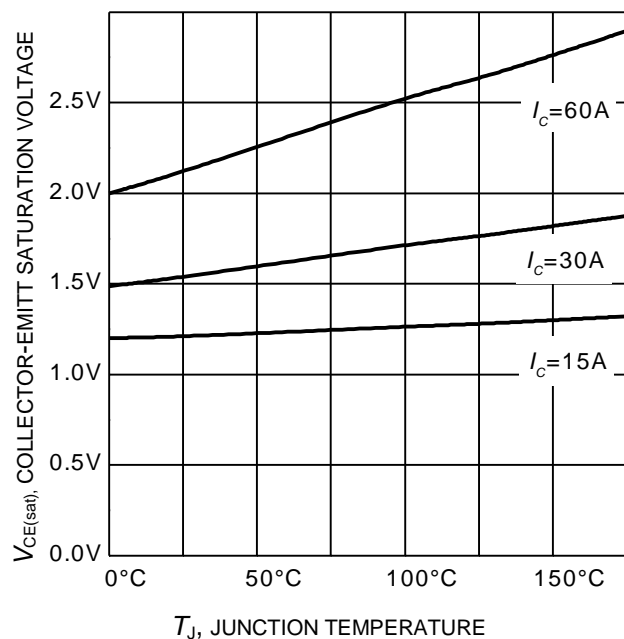
**Figure 5. Typical output characteristic**  
( $T_j = 25^\circ\text{C}$ )



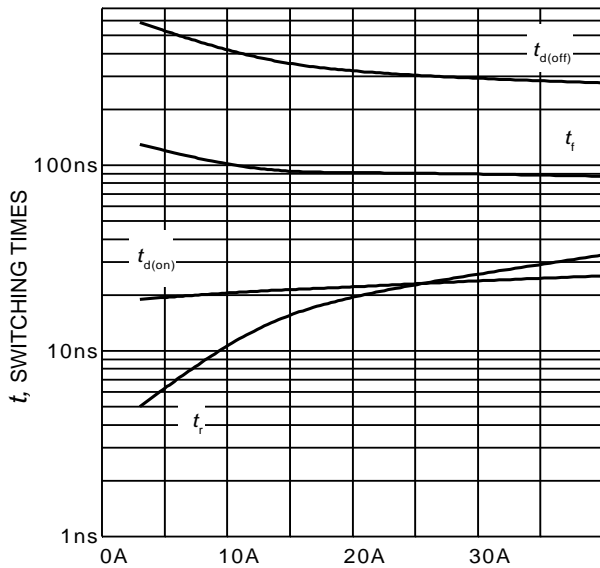
**Figure 6. Typical output characteristic**  
( $T_j = 175^\circ\text{C}$ )



**Figure 7. Typical transfer characteristic**  
( $V_{CE} = 10\text{V}$ )

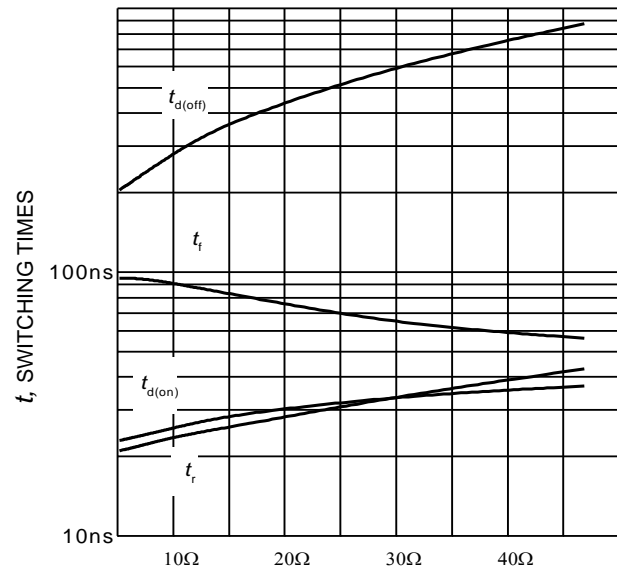


**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE} = 15\text{V}$ )



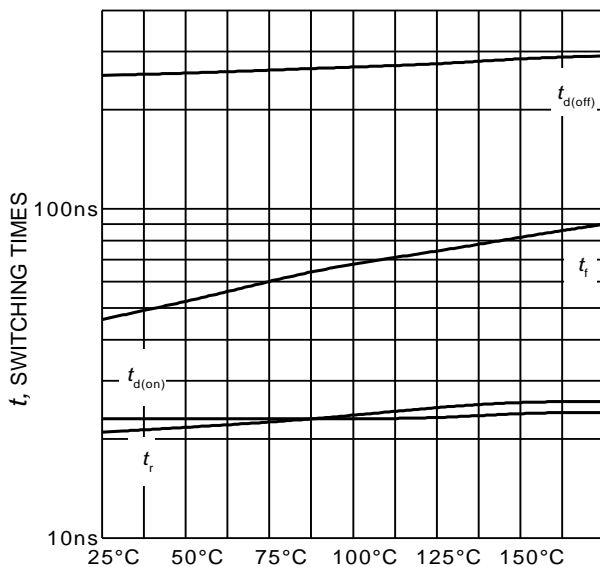
$I_C$ , COLLECTOR CURRENT

**Figure 9. Typical switching times as a function of collector current**  
(inductive load,  $T_J=175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $r_G = 10\Omega$ ,  
Dynamic test circuit in Figure E)



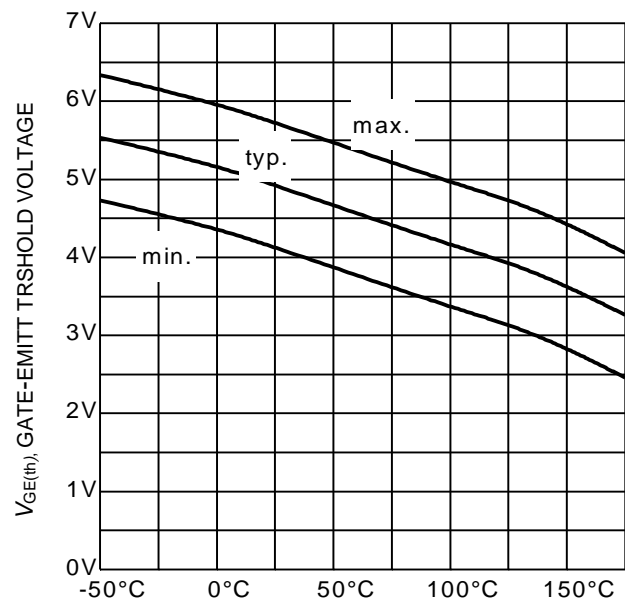
$R_G$ , GATE RESISTOR

**Figure 10. Typical switching times as a function of gate resistor**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 30\text{A}$ ,  
Dynamic test circuit in Figure E)



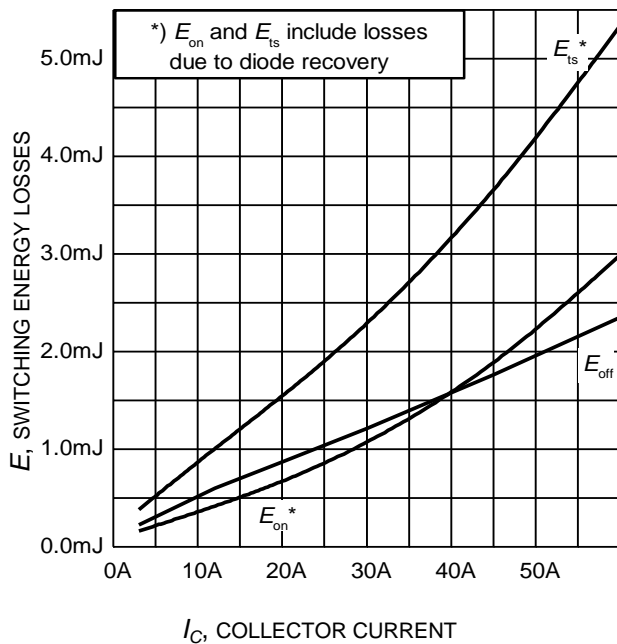
$T_J$ , JUNCTION TEMPERATURE

**Figure 11. Typical switching times as a function of junction temperature**  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 30\text{A}$ ,  $r_G=10\Omega$ ,  
Dynamic test circuit in Figure E)

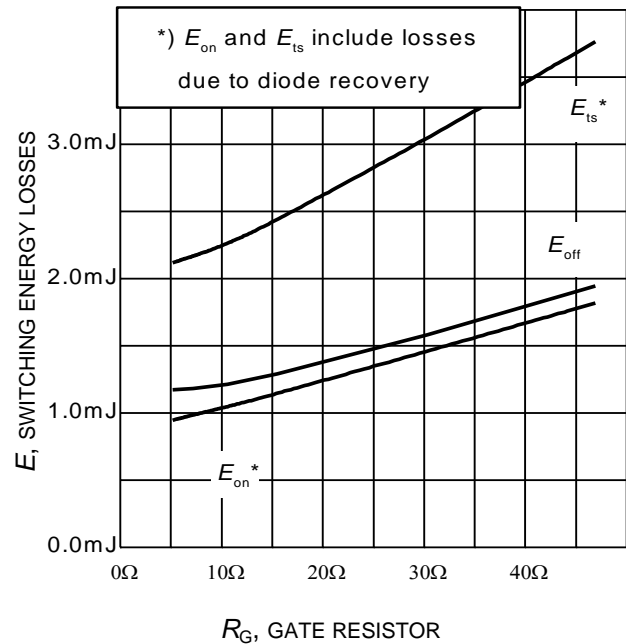


$T_J$ , JUNCTION TEMPERATURE

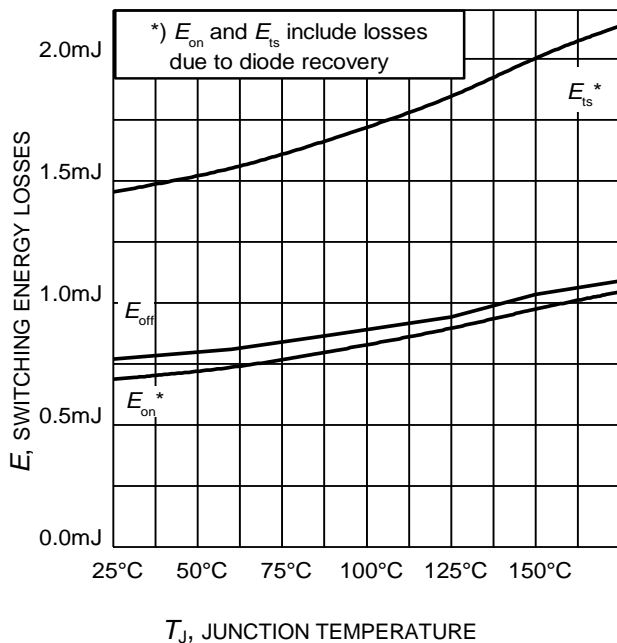
**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**  
( $I_C = 0.43\text{mA}$ )



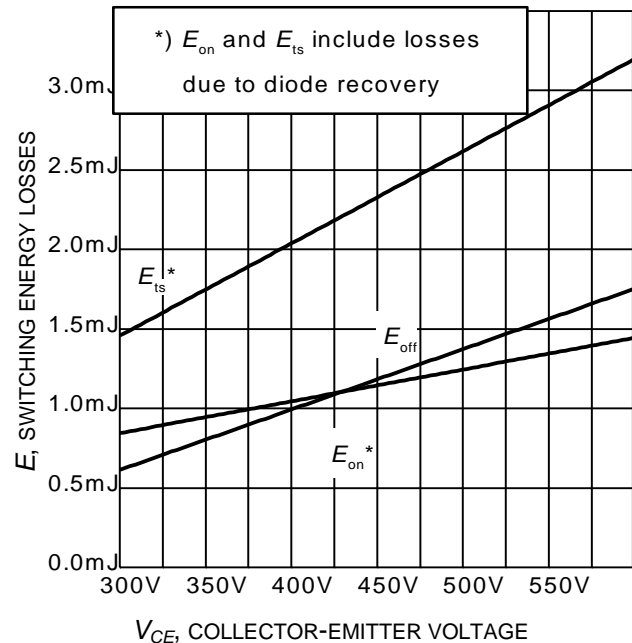
**Figure 13. Typical switching energy losses as a function of collector current**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $r_G = 10\Omega$ , Dynamic test circuit in Figure E)



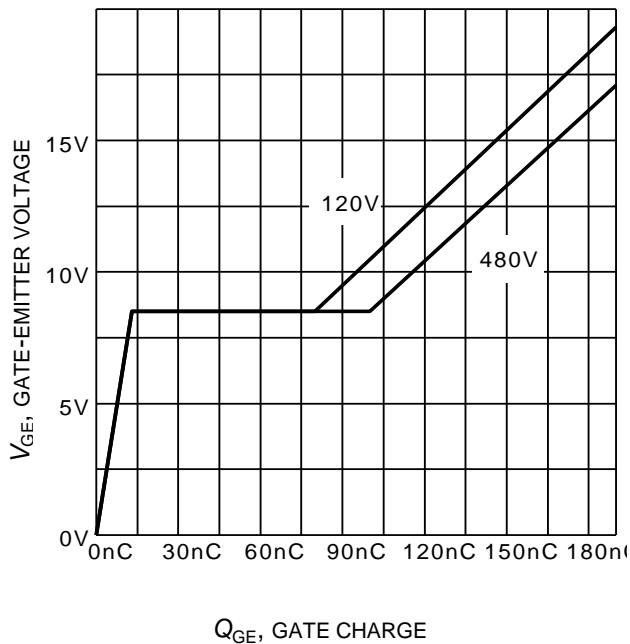
**Figure 14. Typical switching energy losses as a function of gate resistor**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 30\text{A}$ , Dynamic test circuit in Figure E)



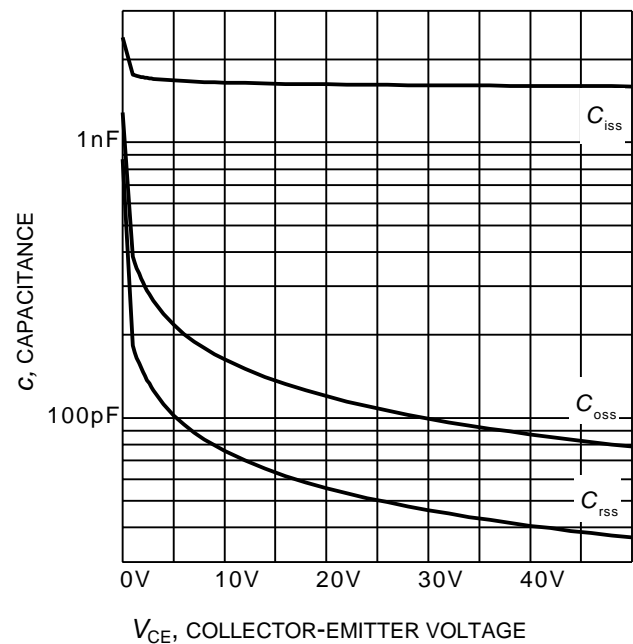
**Figure 15. Typical switching energy losses as a function of junction temperature**  
(inductive load,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 30\text{A}$ ,  $r_G = 10\Omega$ , Dynamic test circuit in Figure E)



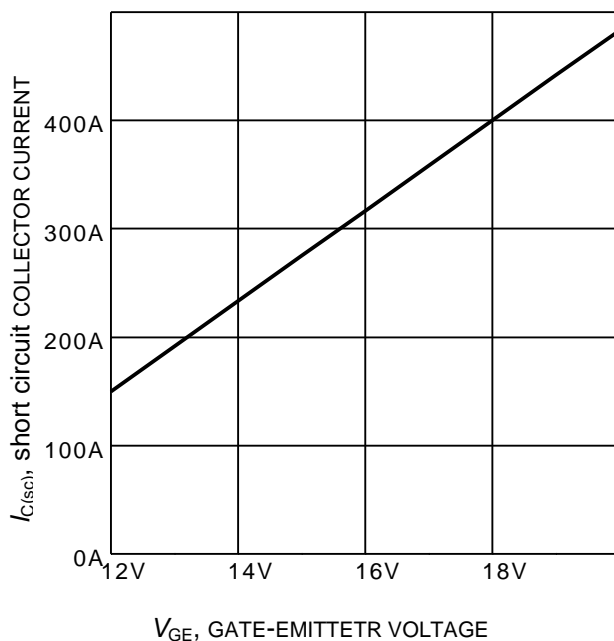
**Figure 16. Typical switching energy losses as a function of collector emitter voltage**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 30\text{A}$ ,  $r_G = 10\Omega$ , Dynamic test circuit in Figure E)



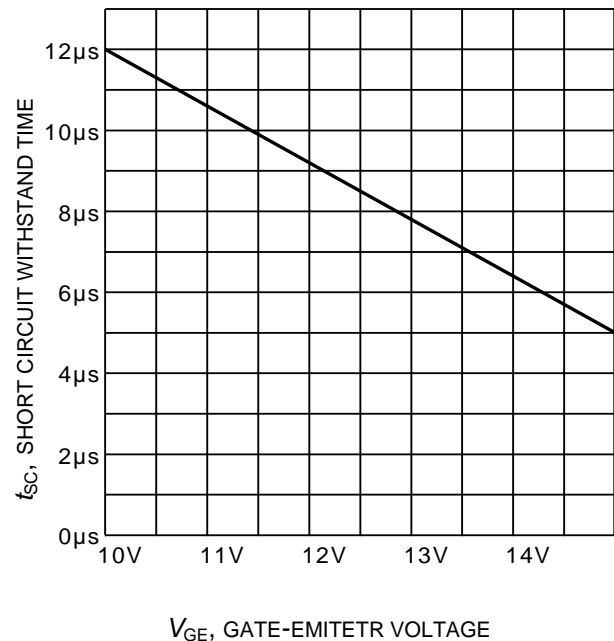
**Figure 17. Typical gate charge**  
( $I_C=30\text{ A}$ )



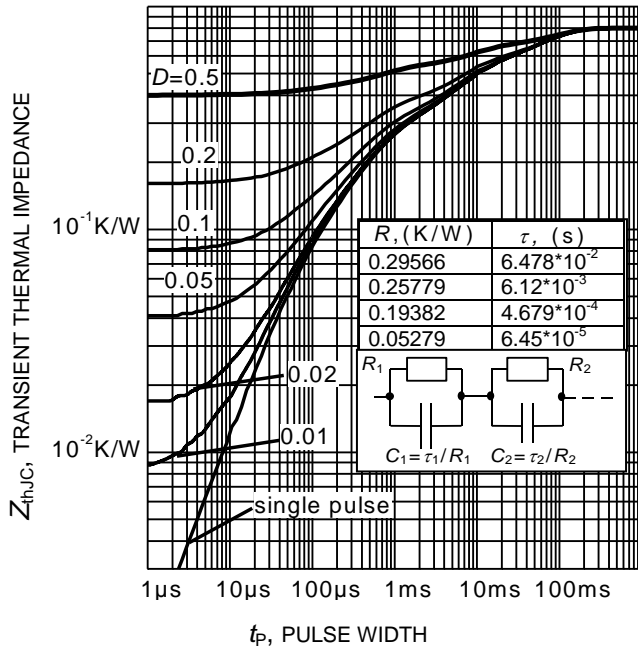
**Figure 18. Typical capacitance as a function of collector-emitter voltage**  
( $V_{GE}=0\text{V}$ ,  $f = 1\text{ MHz}$ )



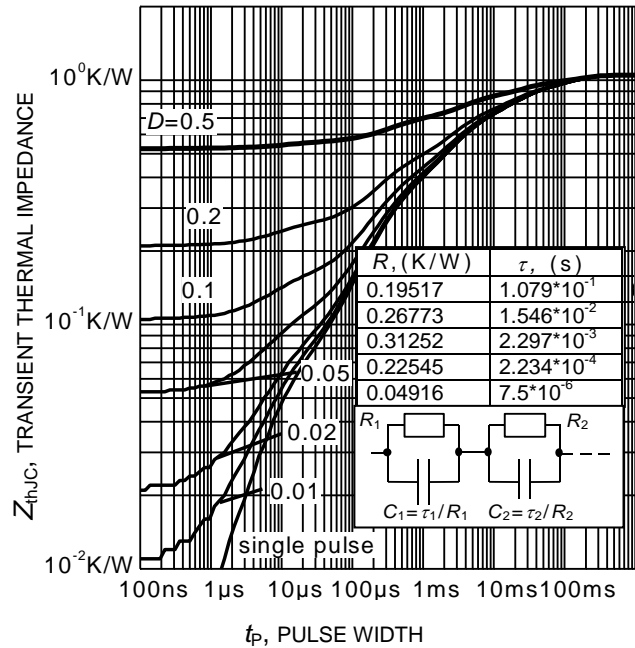
**Figure 19. Typical short circuit collector current as a function of gate-emitter voltage**  
( $V_{CE} \leq 400\text{V}$ ,  $T_j \leq 150^\circ\text{C}$ )



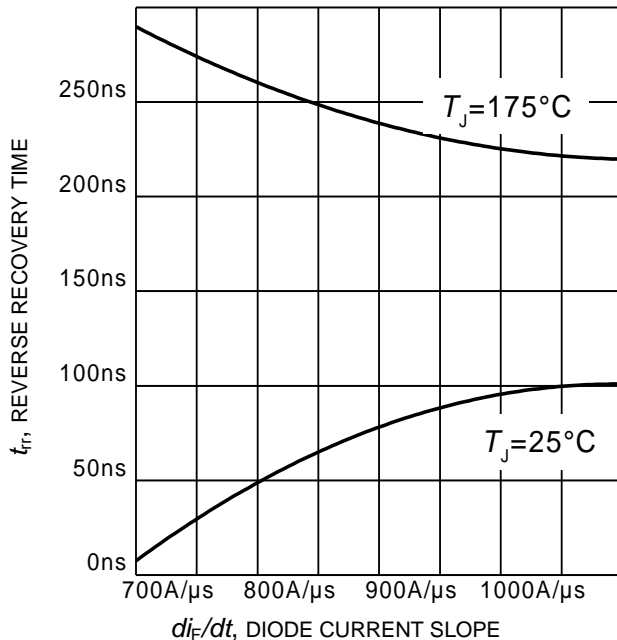
**Figure 20. Short circuit withstand time as a function of gate-emitter voltage**  
( $V_{CE}=400\text{V}$ , start at  $T_j=25^\circ\text{C}$ ,  $T_{jmax}<150^\circ\text{C}$ )



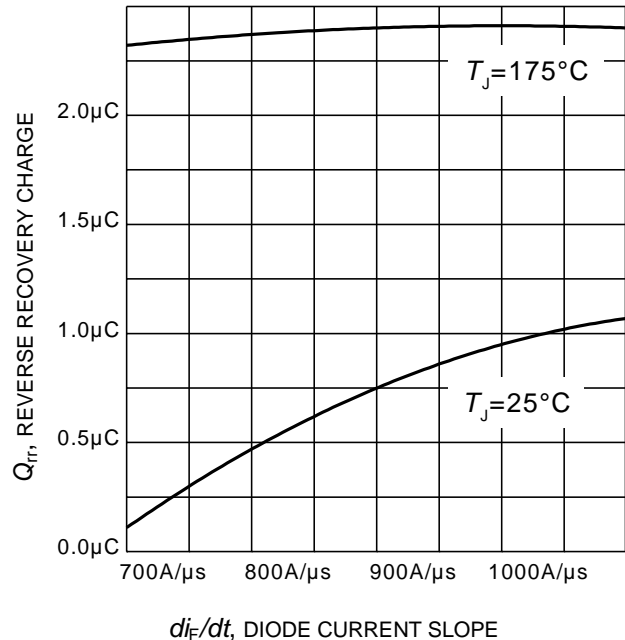
**Figure 21. IGBT transient thermal impedance**  
( $D = t_p / T$ )



**Figure 22. Diode transient thermal impedance as a function of pulse width**  
( $D = t_p / T$ )

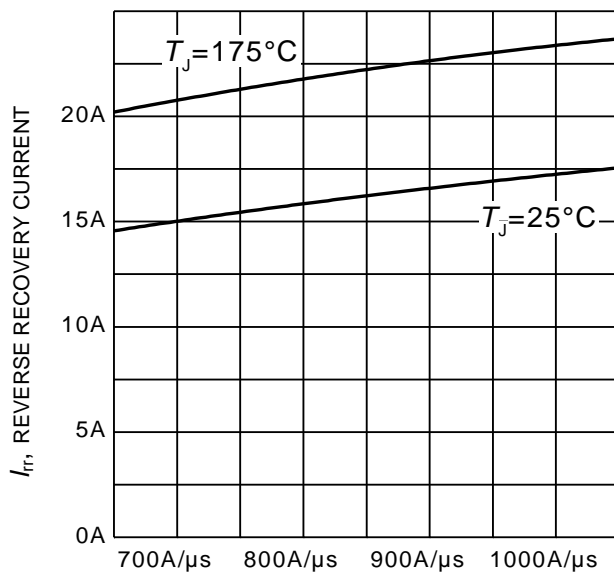


**Figure 23. Typical reverse recovery time as a function of diode current slope**  
( $V_R = 400V$ ,  $I_F = 30A$ ,  
Dynamic test circuit in Figure E)



**Figure 24. Typical reverse recovery charge as a function of diode current slope**  
( $V_R = 400V$ ,  $I_F = 30A$ ,  
Dynamic test circuit in Figure E)

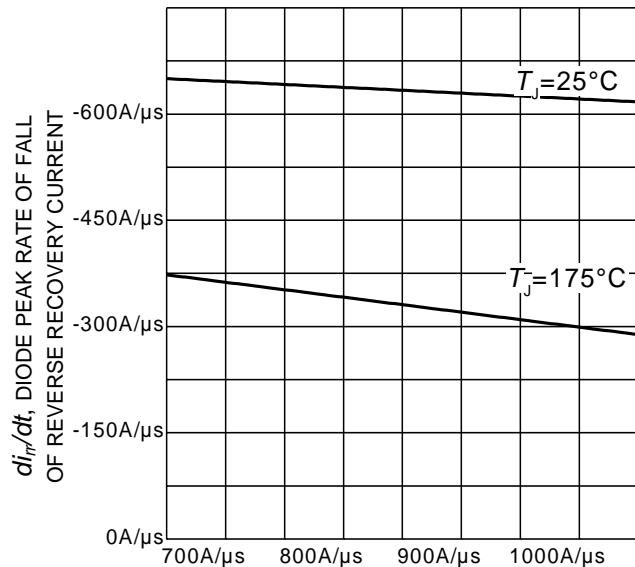




$di_F/dt$ , DIODE CURRENT SLOPE

**Figure 25. Typical reverse recovery current as a function of diode current slope**

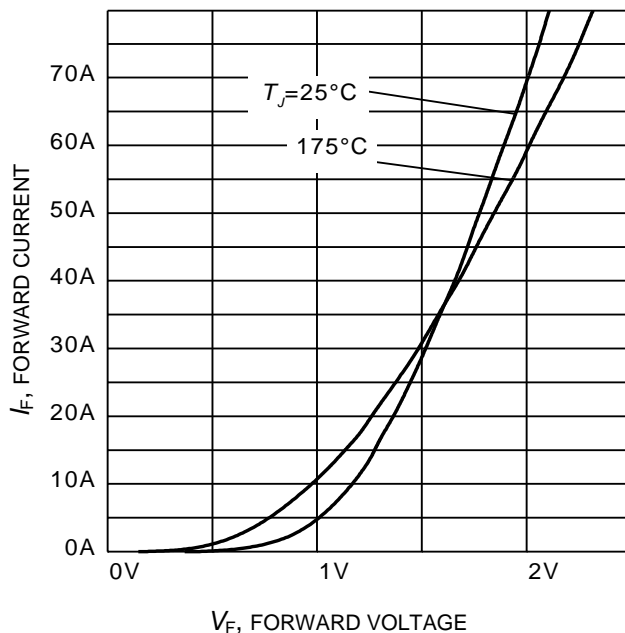
( $V_R = 400V$ ,  $I_F = 30A$ ,  
Dynamic test circuit in Figure E)



$di_F/dt$ , DIODE CURRENT SLOPE

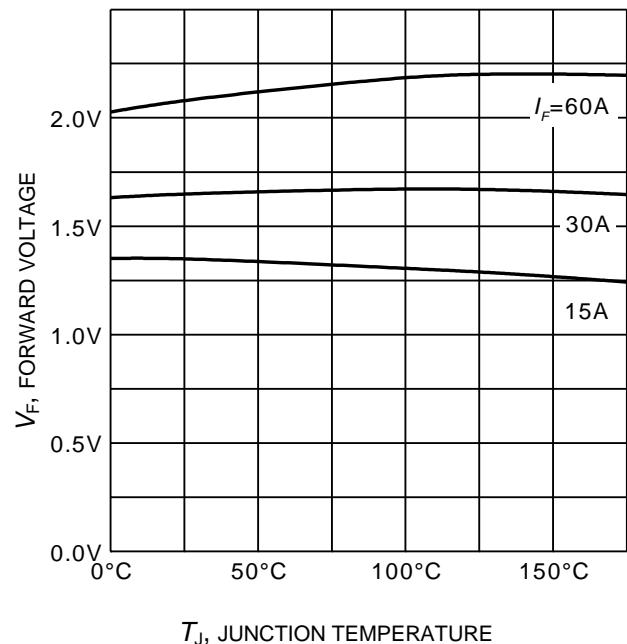
**Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**

( $V_R = 400V$ ,  $I_F = 30A$ ,  
Dynamic test circuit in Figure E)



$V_F$ , FORWARD VOLTAGE

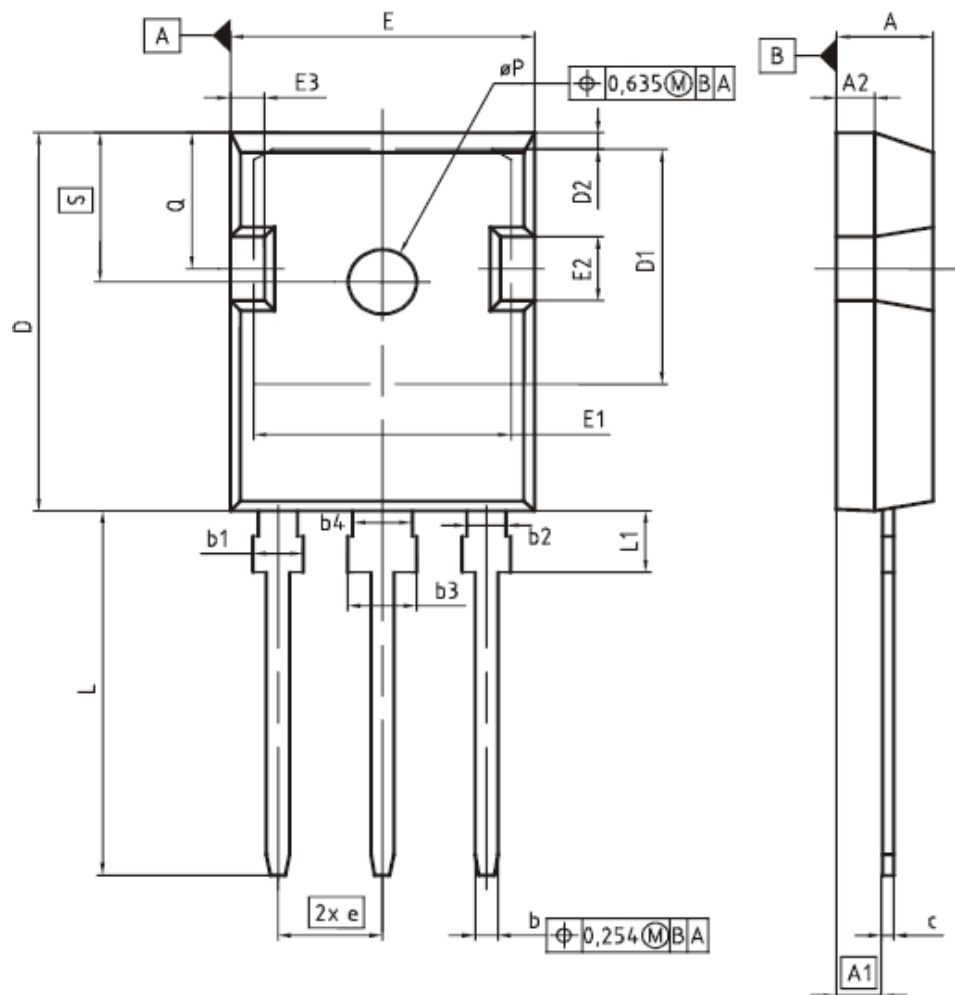
**Figure 27. Typical diode forward current as a function of forward voltage**



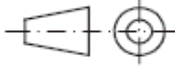
$T_j$ , JUNCTION TEMPERATURE

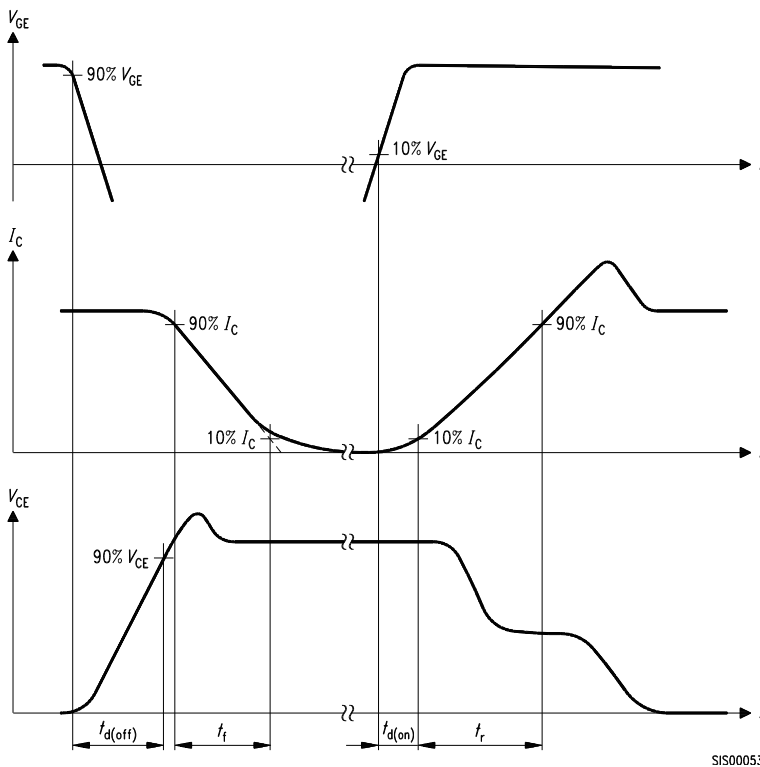
**Figure 28. Typical diode forward voltage as a function of junction temperature**

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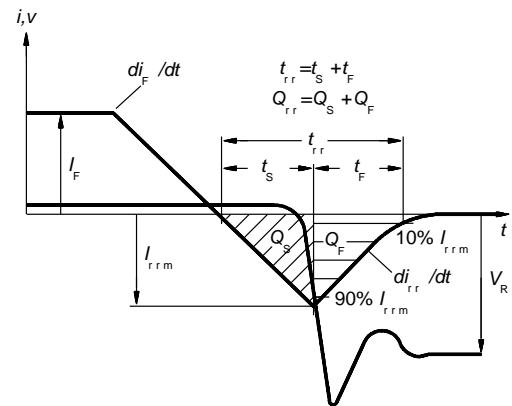


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4,83	5,21	0,190	0,205
A1	2,27	2,54	0,089	0,100
A2	1,85	2,16	0,073	0,085
b	1,07	1,33	0,042	0,052
b1	1,90	2,41	0,075	0,095
b2	1,90	2,16	0,075	0,085
b3	2,87	3,38	0,113	0,133
b4	2,87	3,13	0,113	0,123
c	0,55	0,88	0,022	0,027
D	20,80	21,10	0,819	0,831
D1	16,25	17,65	0,640	0,695
D2	0,95	1,35	0,037	0,053
E	15,70	16,13	0,618	0,635
E1	13,10	14,15	0,516	0,557
E2	3,68	5,10	0,145	0,201
E3	1,00	2,60	0,039	0,102
e	5,44 (BSC)		0,214 (BSC)	
N	3		3	
L	19,80	20,32	0,780	0,800
L1	4,10	4,47	0,161	0,176
øP	3,50	3,70	0,138	0,146
Q	5,49	6,00	0,216	0,236
S	6,04	6,30	0,238	0,248

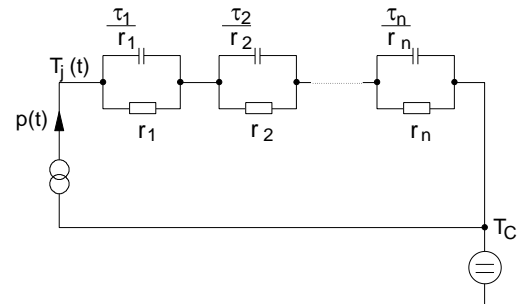
<b>DOCUMENT NO.</b> Z8B00003327
<b>SCALE</b> 0 5 5 7,5mm
<b>EUROPEAN PROJECTION</b> 
<b>ISSUE DATE</b> 09-07-2010
<b>REVISION</b> 05



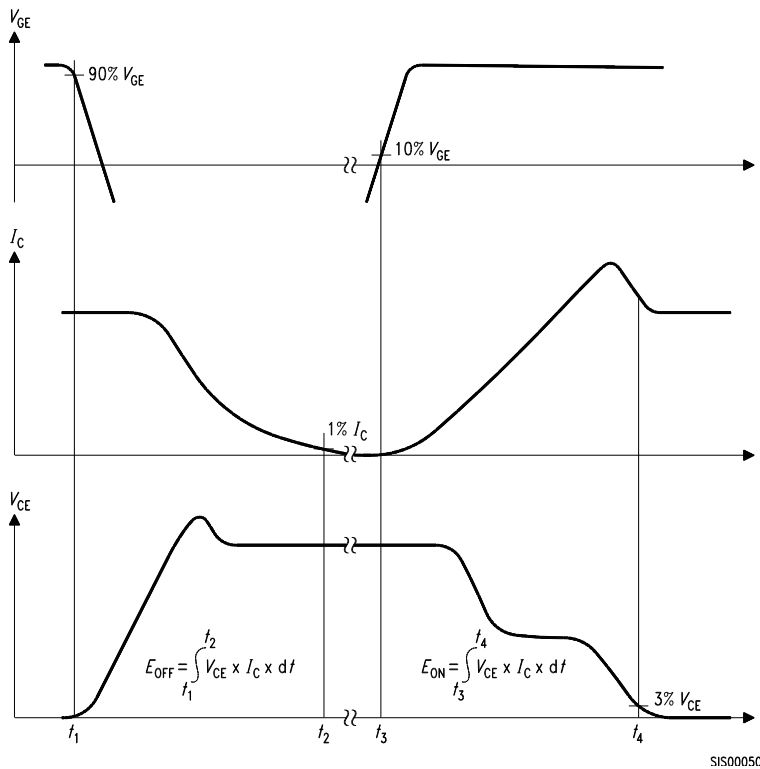
**Figure A. Definition of switching times**



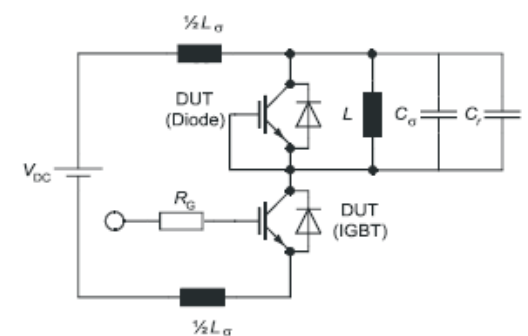
**Figure C. Definition of diodes switching characteristics**



**Figure D. Thermal equivalent circuit**



**Figure B. Definition of switching losses**



**Figure E. Dynamic test circuit**  
Parasitic inductance  $L_\sigma$ ,  
Parasitic capacitor  $C_\sigma$ ,  
Relief capacitor  $C_r$   
(only for ZVT switching)

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