Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Drain Current (Note 5) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-2.7 -2	A
Pulsed Drain Current (Note 6)			I_{DM}	-8	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	1.08	W
Thermal Resistance, Junction to Ambient @T_A = +25°C (Note 5)	$R_{\theta JA}$	115	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-800	nA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±80 ±800	nA	V_{GS} = ±12V, V_{DS} = 0V V_{GS} = ±15V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.3	-1.8	-2.1	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	97 165	122 190	mΩ	V_{GS} = -10V, I_D = -2.7A V_{GS} = -4.5V, I_D = -2.0A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	5.9	—	S	V_{DS} = -5V, I_D = -2.7A
Diode Forward Voltage (Note 7)	V_{SD}	—	—	-1.26	V	V_{GS} = 0V, I_S = -2.7A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	384.4	—	pF	V_{DS} = -10V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	59.4	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	52.8	—	pF	
Gate Resistance	R_G	—	17.1	—	Ω	V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	4.0	—	nC	V_{GS} = -10V/-4.5V, V_{DS} = -15V, I_D = -3A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	8.2	—	nC	
Gate-Source Charge	Q_{gs}	—	0.9	—	nC	
Gate-Drain Charge	Q_{gd}	—	1.2	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	4.8	—	ns	V_{DS} = -15V, V_{GS} = -10V, R_G = 6Ω, I_D = -1A
Turn-On Rise Time	t_R	—	7.3	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	22.5	—	ns	
Turn-Off Fall Time	t_F	—	13.4	—	ns	

Notes: 5. Device mounted on FR-4 PCB. t ≤10 sec.
6. Pulse width ≤10µS, Duty Cycle ≤1%.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

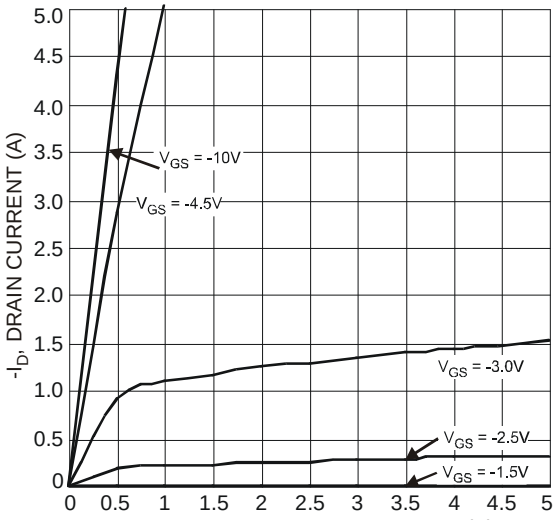


Fig. 1 Typical Output Characteristics

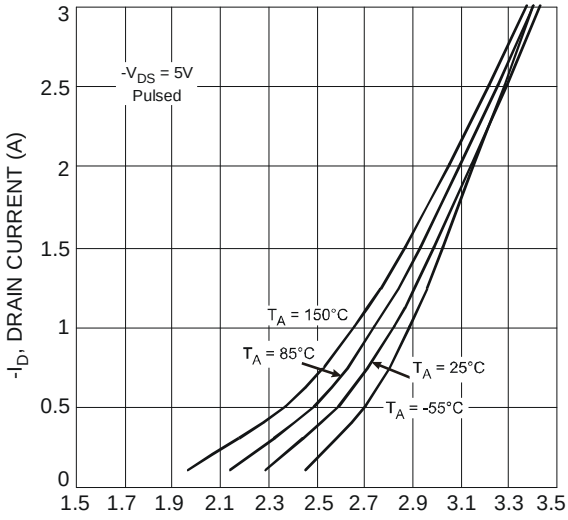


Fig. 2 Typical Transfer Characteristics

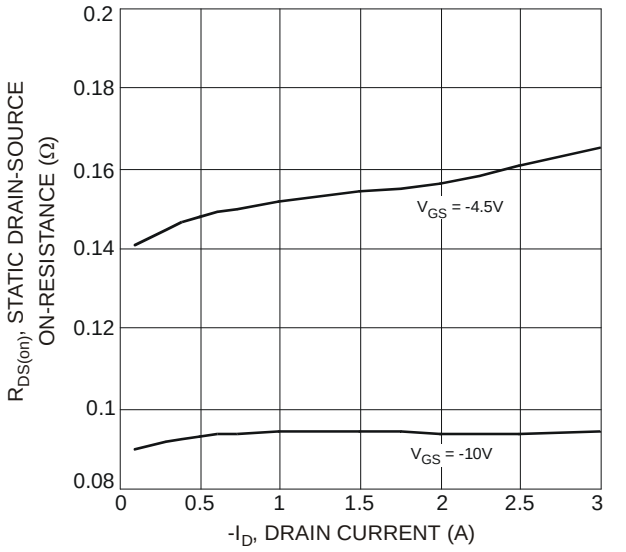


Fig. 3 On-Resistance vs. Drain Current and Gate Voltage

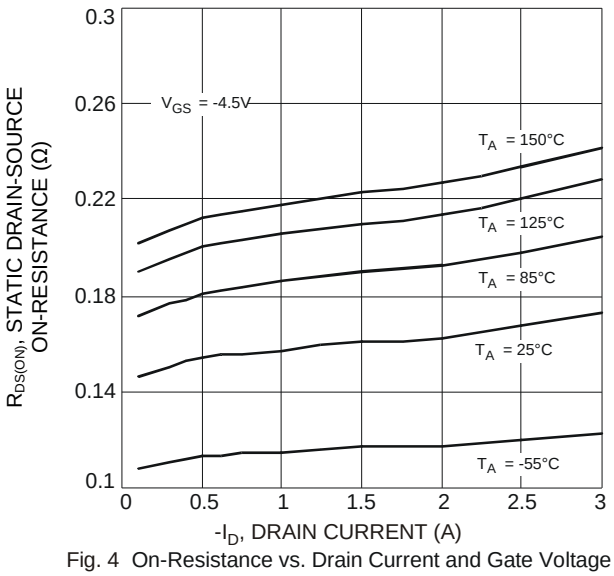


Fig. 4 On-Resistance vs. Drain Current and Gate Voltage

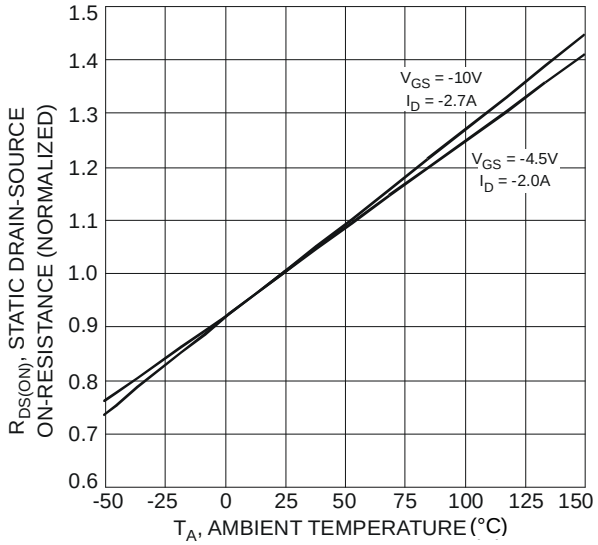


Fig. 5 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

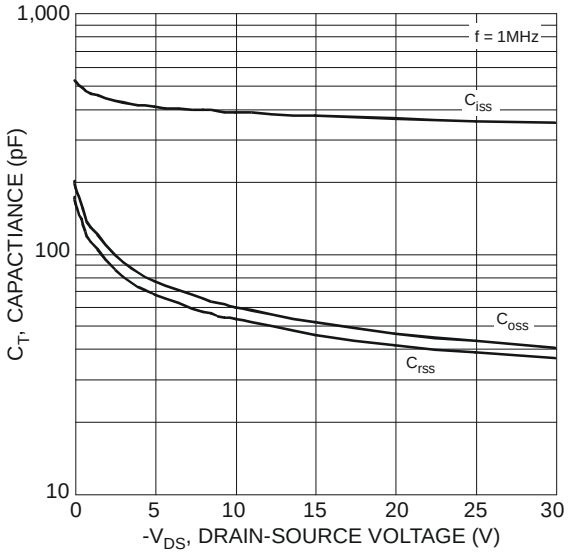


Fig. 6 Typical Capacitance

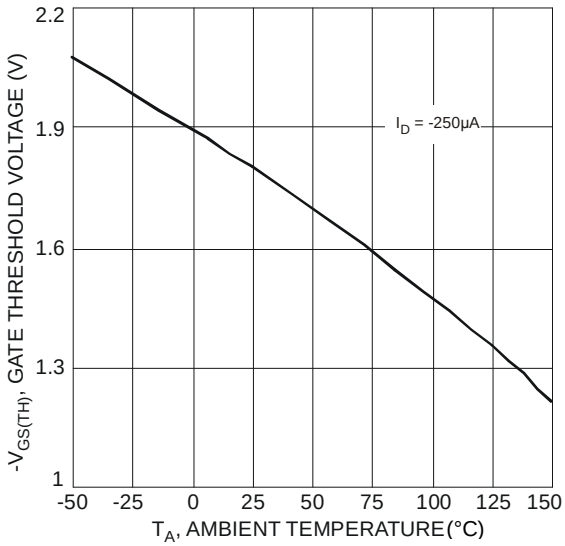


Fig. 7 Gate Threshold Voltage vs. Ambient Temperature

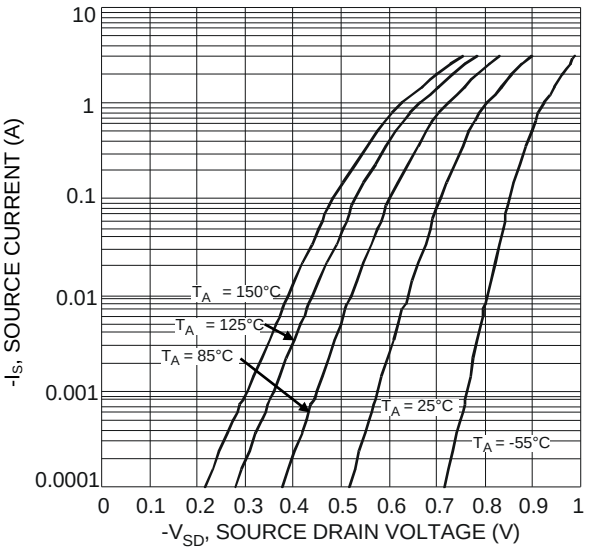


Fig. 8 Reverse Source Current vs. Source-Drain Voltage

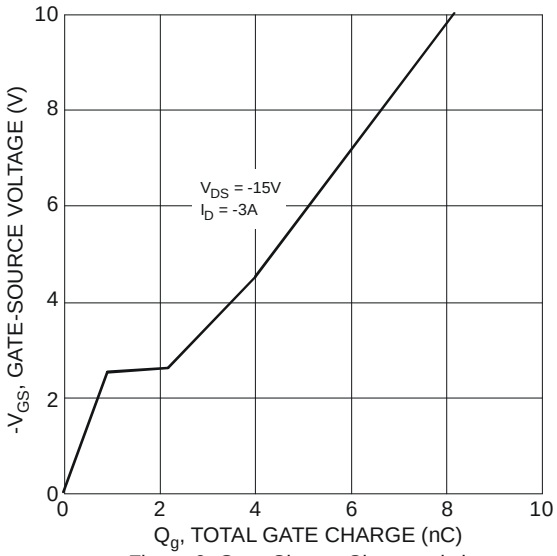
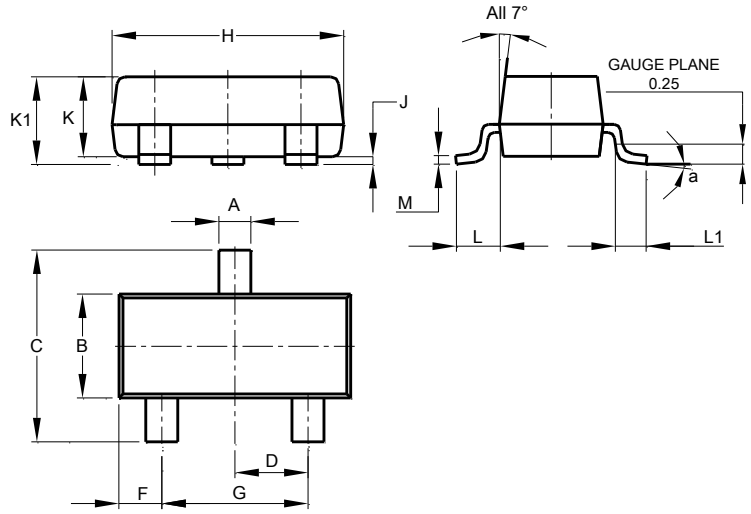


Figure 9 Gate-Charge Characteristics

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

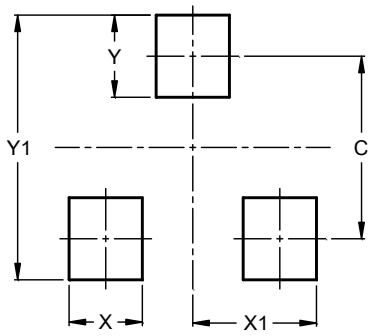


SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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