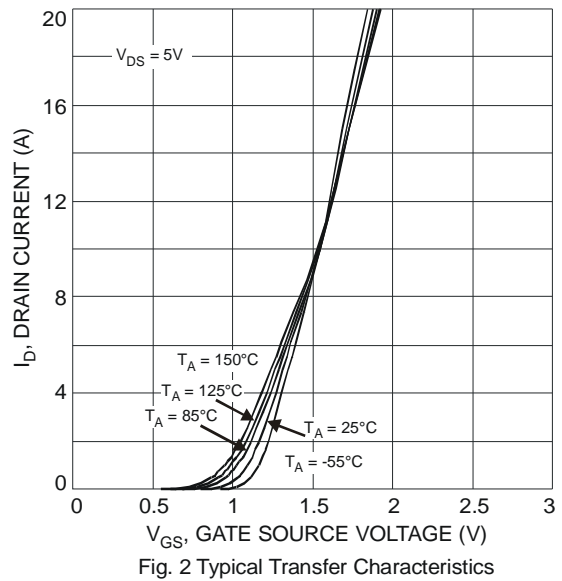
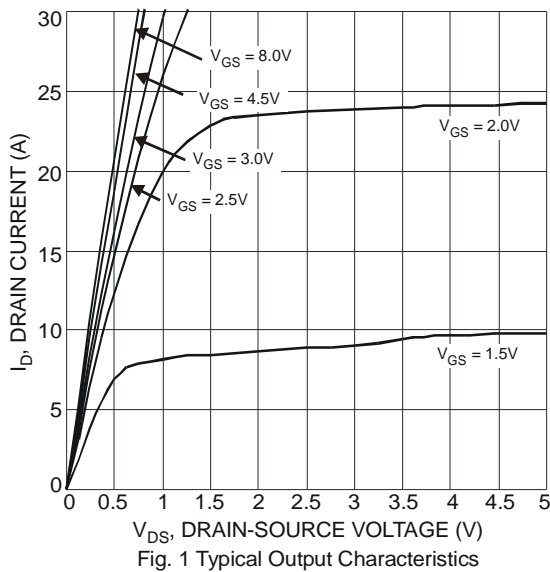


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	-	-	-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±10	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	-0.4	-0.7	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	23	35	mΩ	V _{GS} = -4.5V, I _D = -4.0A
			30	45		V _{GS} = -2.5V, I _D = -4.0A
			41	62		V _{GS} = -1.8V, I _D = -2.0A
Forward Transfer Admittance	Y _{fs}	-	14	-	S	V _{DS} = -5V, I _D = -4A
Diodes Forward Voltage	V _{SD}	-	-0.7	-1.0	V	I _S = -1A, V _{GS} = 0V
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C _{iss}	-	1610	-	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	157	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	145	-	pF	
Gate Resistance	R _g	-	9.45	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q _g	-	15.4	-	nC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -4A
Gate-Source Charge	Q _{gs}	-	2.5	-	nC	
Gate-Drain Charge	Q _{gd}	-	3.3	-	nC	
Turn-On Delay Time	t _{D(on)}	-	16.8	-	ns	V _{DS} = -10V, V _{GS} = -4.5V, R _L = 10Ω, R _G = 6.0Ω, I _D = -1A
Turn-On Rise Time	t _r	-	12.4	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	94.1	-	ns	
Turn-Off Fall Time	t _f	-	42.4	-	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effects.
6. Guaranteed by design. Not subject to production testing.



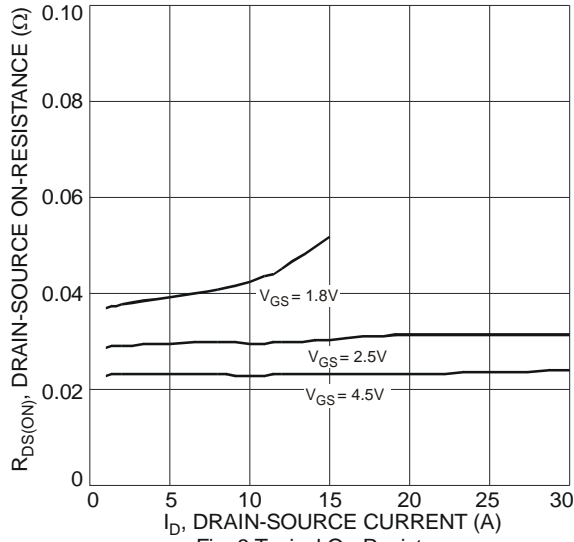


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

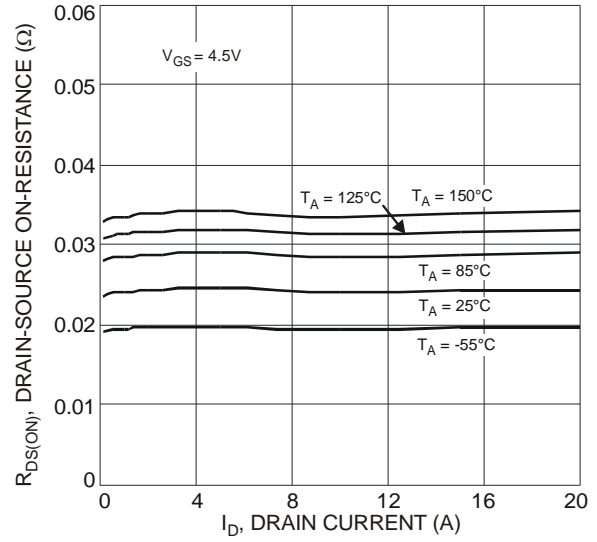


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

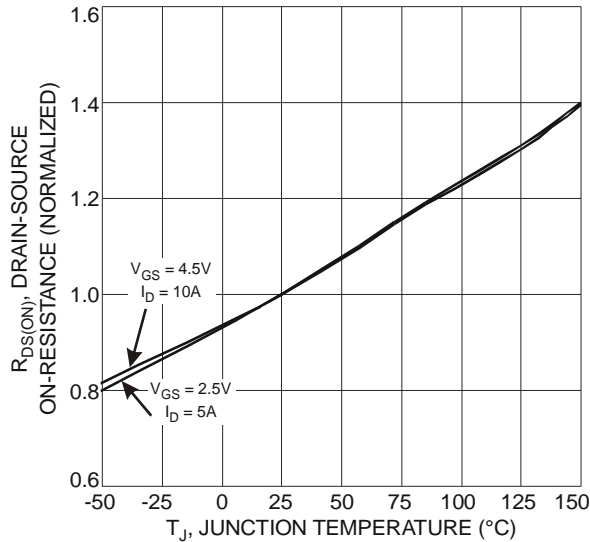


Fig. 5 On-Resistance Variation with Temperature

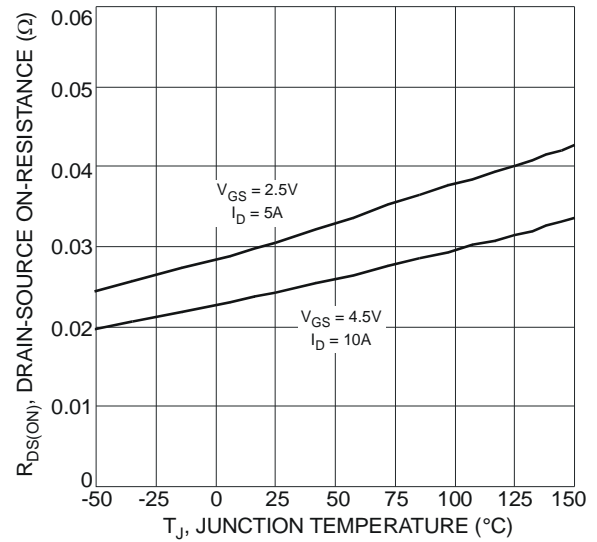


Fig. 6 On-Resistance Variation with Temperature

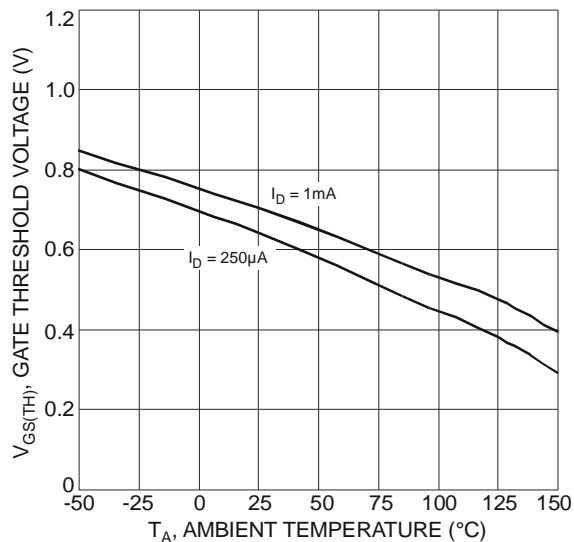


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

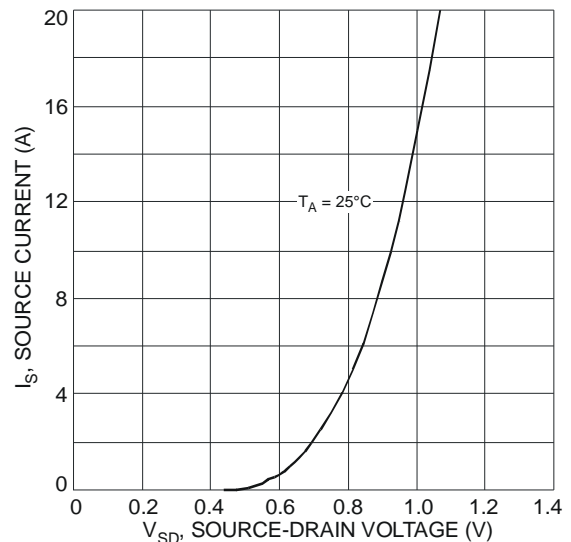


Fig. 8 Diode Forward Voltage vs. Current

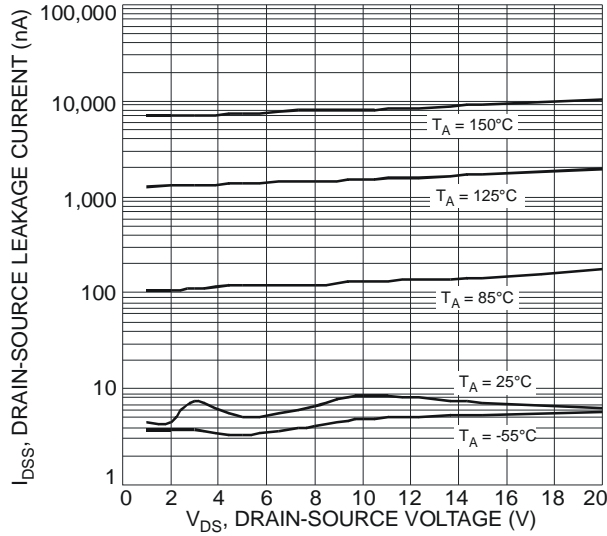


Fig. 9 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

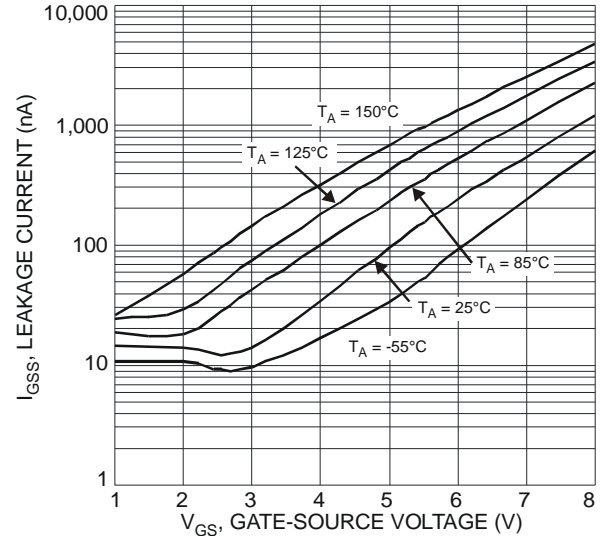


Fig. 10 Leakage Current vs. Gate-Source Voltage

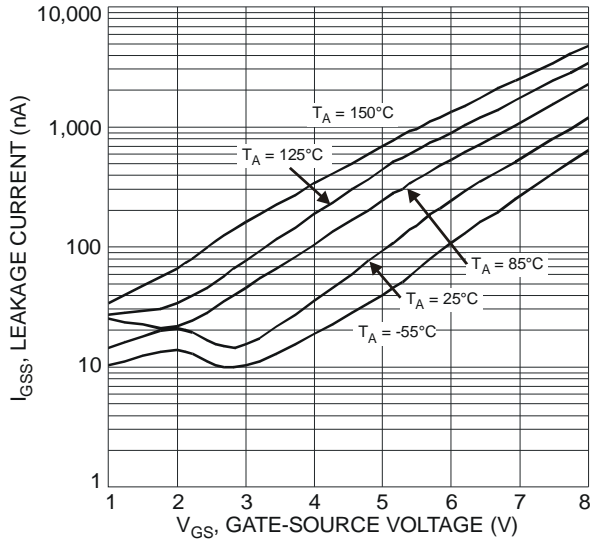


Fig. 11 Leakage Current vs. Gate-Source Voltage

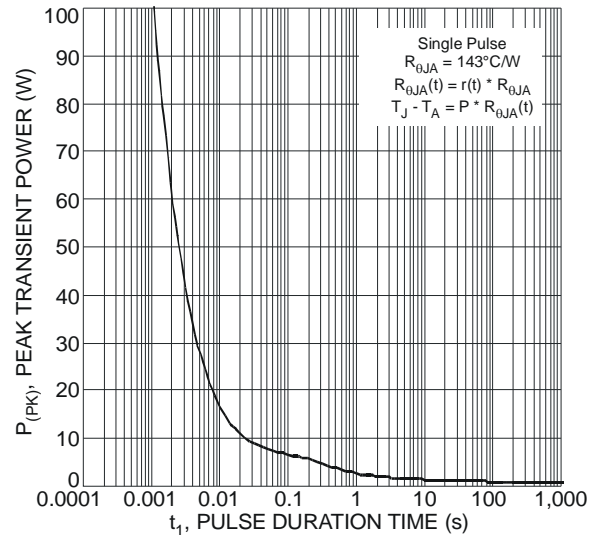


Fig. 12 Single Pulse Maximum Power Dissipation

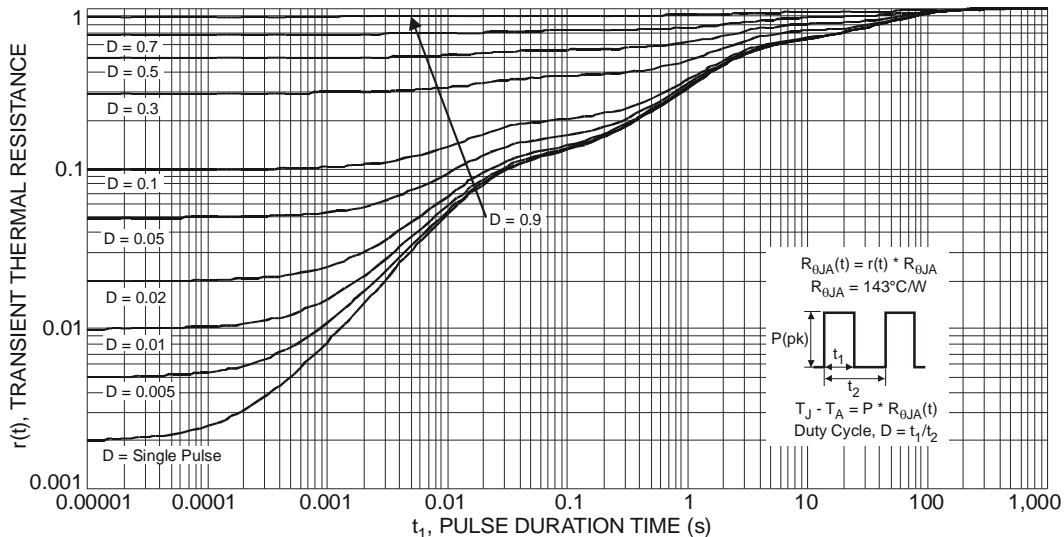


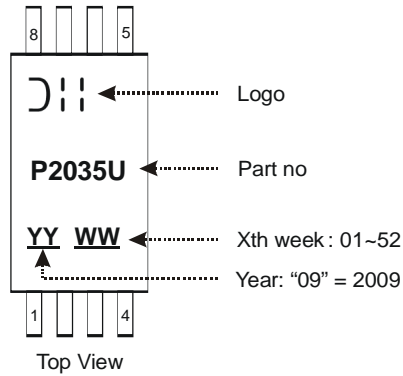
Fig. 13 Transient Thermal Response

Ordering Information (Note 7)

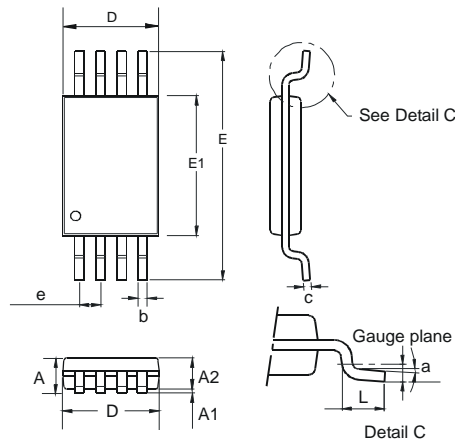
Part Number	Case	Packaging
DMP2035UTS-13	TSSOP-8L	2500 / Tape & Reel

Notes: 7. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

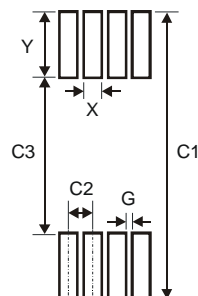


Package Outline Dimensions



TSSOP-8L			
Dim	Min	Max	Typ
a	0.09	—	—
A	—	1.20	—
A1	0.05	0.15	—
A2	0.825	1.025	0.925
b	0.19	0.30	—
c	0.09	0.20	—
D	2.90	3.10	3.025
e	—	—	0.65
E	—	—	6.40
E1	4.30	4.50	4.425
L	0.45	0.75	0.60
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
X	0.45
Y	1.78
C1	7.72
C2	0.65
C3	4.16
G	0.20

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