

# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	75	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Dusin Courset (Nata CVV - 40V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	7.8 6.2	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	10.5 8.4	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	56	Α
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2.1	Α
Avalanche Current, L = 0.1mH			I <sub>AS</sub>	28.8	Α
Avalanche Energy, L = 0.1mH			E <sub>AS</sub>	42.2	mJ

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)		$P_{D}$	0.9	W
Thermal Desistance Innetion to Ambient (Note 5)	Steady state	1	125	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	67	
Total Power Dissipation (Note 6)		P <sub>D</sub>	2	W
Thermal Desigtance, Junction to Ambient (Note 6)	Steady state	-	62	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	34	
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	6.9	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

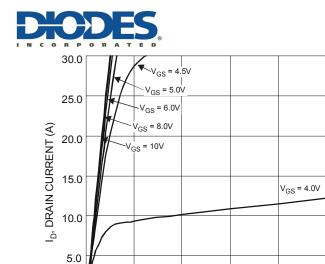
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

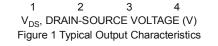
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	75		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>			1	μΑ	$V_{DS} = 75V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	-						
Gate Threshold Voltage	$V_{GS(th)}$	1		3	٧	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	14.6	22	mΩ	$V_{GS} = 10V, I_D = 7.2A$	
Static Dialii-Source Off-Resistance	R <sub>DS(ON)</sub>		20.5	28		$V_{GS} = 4.5V, I_D = 6.4A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.72	_	V	$V_{GS} = 0V, I_S = 3.2A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	-	2737	_	рF	V <sub>DS</sub> = 35V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss	_	126	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	96.1	_	рF		
Gate Resistance	Rg	_	0.89	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	26.4	_	nC	V <sub>DS</sub> = 38V, I <sub>D</sub> = 7.2A	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	56.5	_	nC		
Gate-Source Charge	Qgs	_	12	_	nC		
Gate-Drain Charge	$Q_{gd}$	_	11.8	_	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	6.1	_	ns		
Turn-On Rise Time	t <sub>r</sub>	_	5.7	_	ns	$V_{GS}$ = 10V, $V_{DS}$ = 38V, $R_{G}$ = 1 $\Omega$ , $I_{D}$ = 5.7A	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	19.6	_	ns		
Turn-Off Fall Time	t <sub>f</sub>	_	3.9	_	ns		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	_	26.2	_	ns	- I <sub>F</sub> = 5.7A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	_	25.2	_	nC		

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

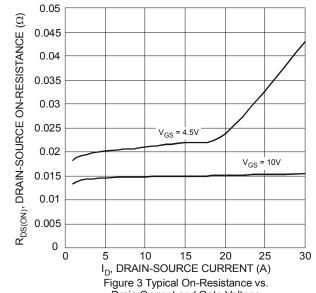
0.0

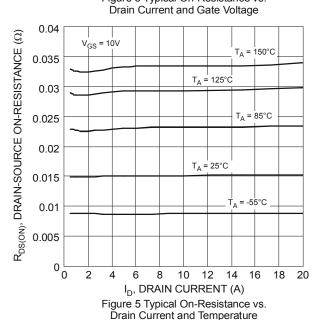


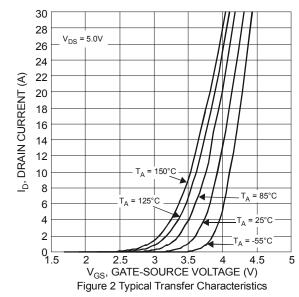


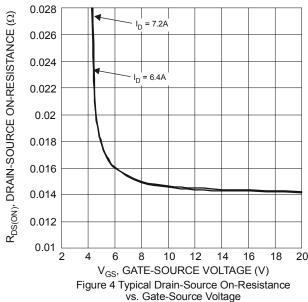
V<sub>GS</sub> = 3.5V

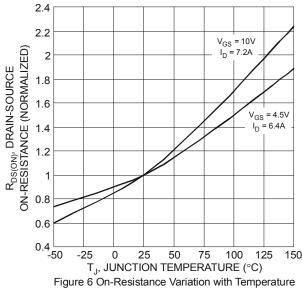
5



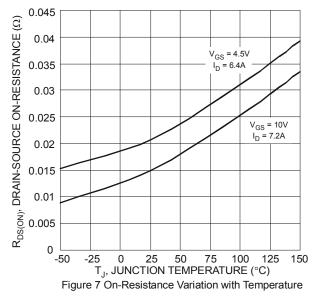


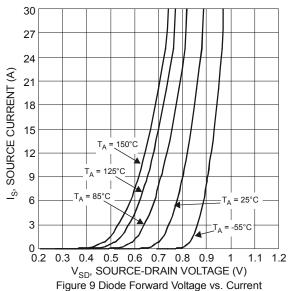


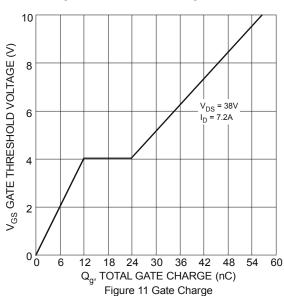












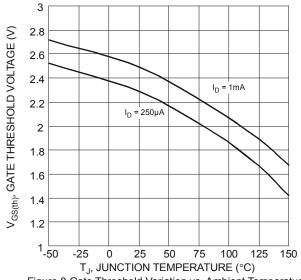
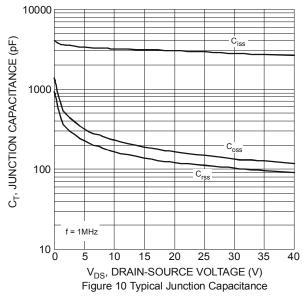
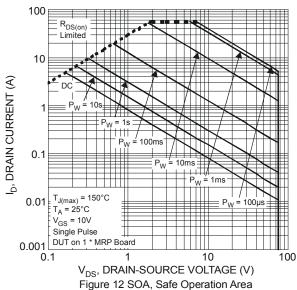
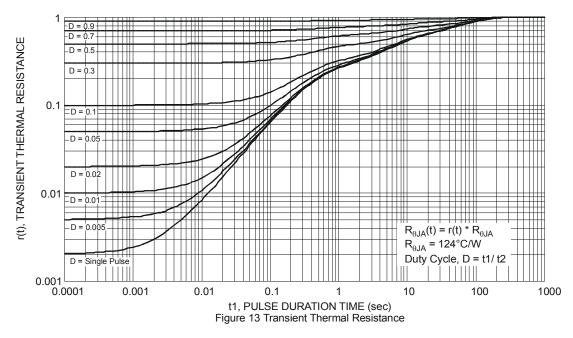


Figure 8 Gate Threshold Variation vs. Ambient Temperature



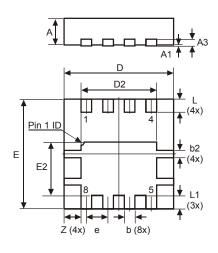






### **Package Outline Dimensions**

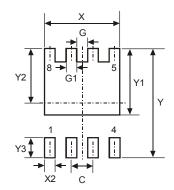
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



POWERDI®3333-8					
Dim	Min	Max	Тур		
D	3.25	3.35	3.30		
Е	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E2	1.56	1.66	1.61		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	_	_	0.203		
b	0.27	0.37	0.32		
b2	_	_	0.20		
L	0.35	0.45	0.40		
L1	_	_	0.39		
е	_	_	0.65		
Z	_	_	0.515		
All Dimensions in mm					

### **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



C 0.650   G 0.230   G1 0.420   Y 3.700   Y1 2.250   Y2 1.850   Y3 0.700   X 2.370	Dimensions	Value (in mm)
G1 0.420   Y 3.700   Y1 2.250   Y2 1.850   Y3 0.700	С	0.650
Y 3.700 Y1 2.250 Y2 1.850 Y3 0.700	G	0.230
Y1 2.250 Y2 1.850 Y3 0.700	G1	0.420
Y2 1.850 Y3 0.700	Y	3.700
Y3 0.700	Y1	2.250
	Y2	1.850
X 2.370	Y3	0.700
	X	2.370
<b>X2</b> 0.420	X2	0.420



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